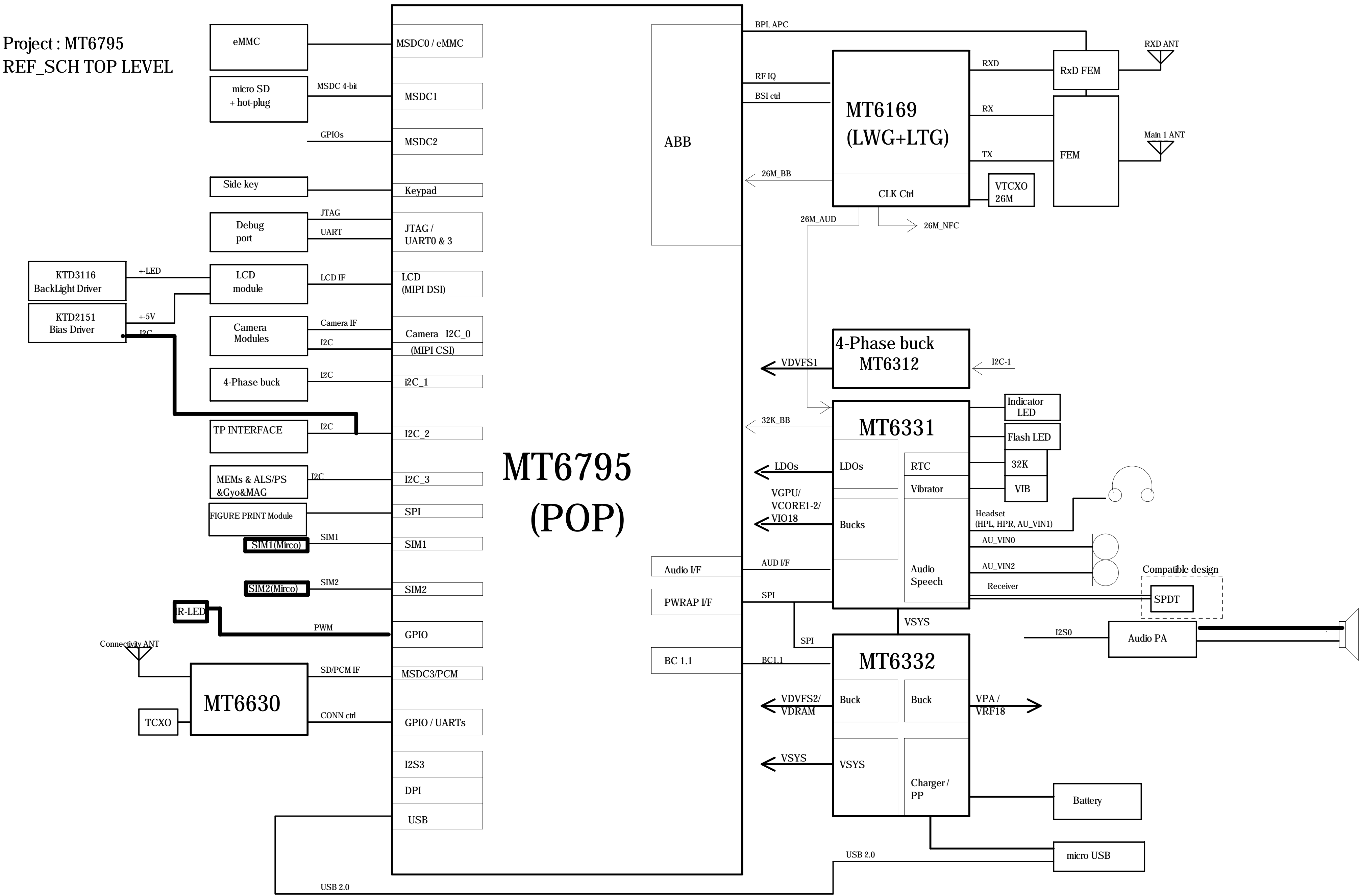
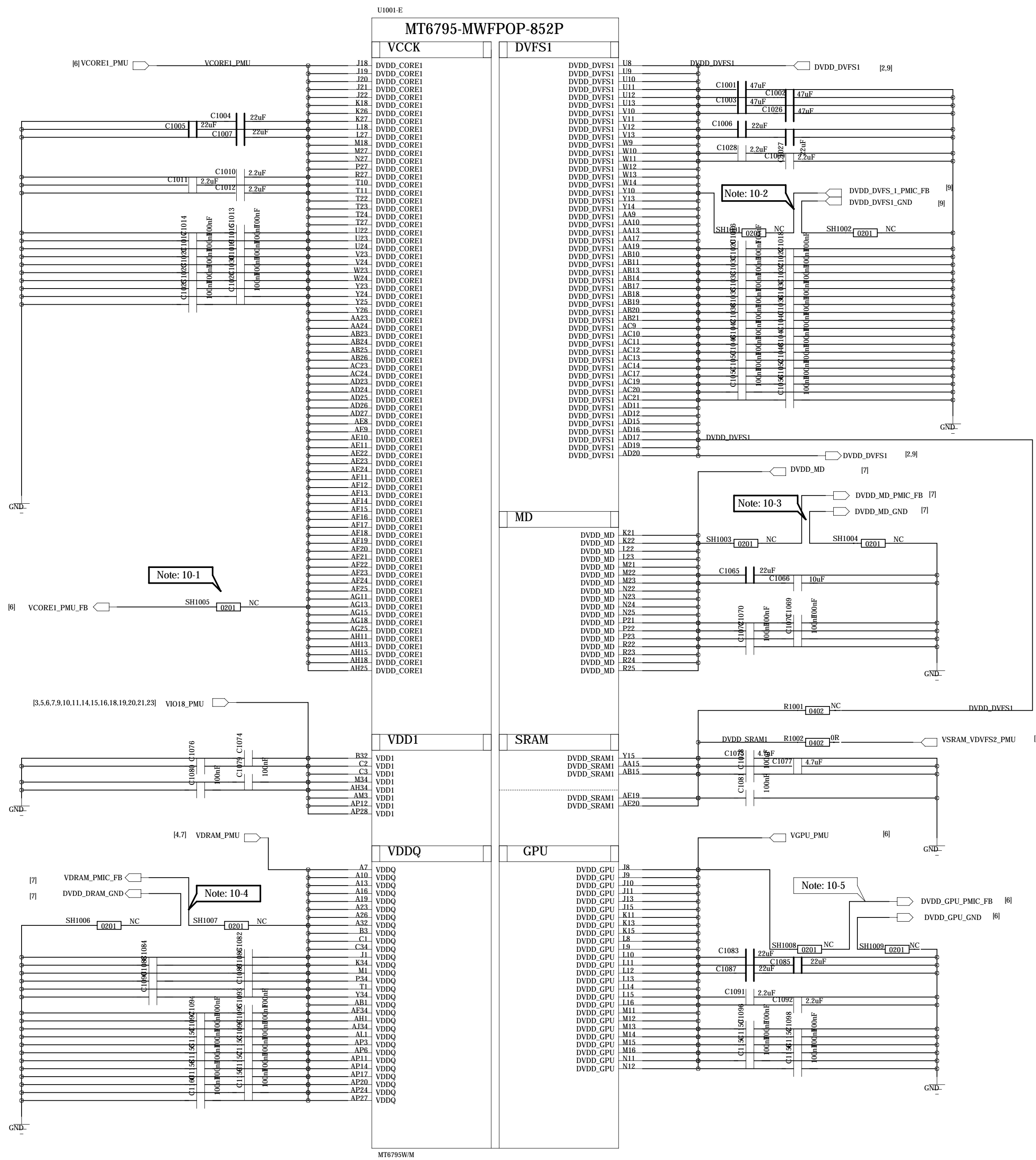


Project : MT6795
REF_SCH TOP LEVEL



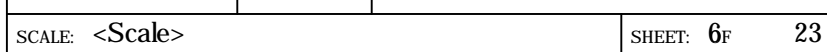
REVISION RECORD			
LTB	ECO NO	APPROVED	DATE



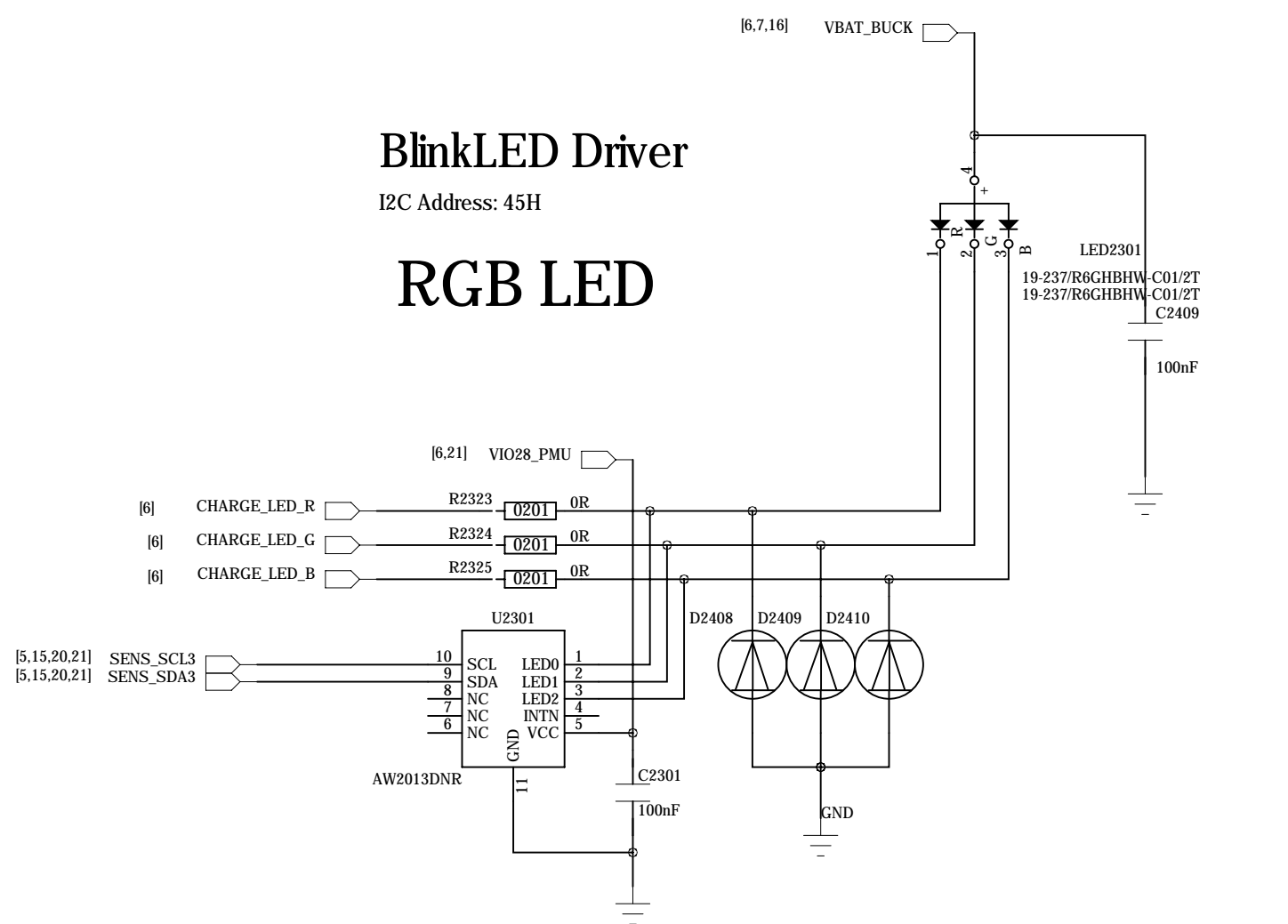
Schematic design notice of "10_BB_POWER_1" page.			
Note 10-1:	VCCORE_1 remote sense must be close to MT6795's AG13 ball.	Remote sense trace with GNDshielding to PMIC (Differential)	
Note 10-2:	Differential pair of DVFS1 remote sense must be close to MT6795's Y10 ball.		
Note 10-3:	Differential pair of DVDD_MD remote sense must be close to MT6795's K22 ball.		
Note 10-4:	VDRAM remote sense must be close to MT6795's A32 ball.		
Note 10-5:	Differential pair of CPU remote sense must be close to MT6795's J8 ball.		

COMPANY: <Company Name>			
TITLE: <Title>			
DESIGN: <Drawn By>	DATE: <Drawn Date>		
CHECKED: <Checked By>	DATE: <Checked Date>		
QUALITY CONTROL: <QC By>	DATE: <QC Date>		
RELEASED: <Released By>	DATE: <Release Date>		
CODE: <Code>		SIZE: A0	DRAWING NO: <Drawing Number>
		REVISION: <Revision>	
SCALE: <Scale>		SHEET: 28 / 23	

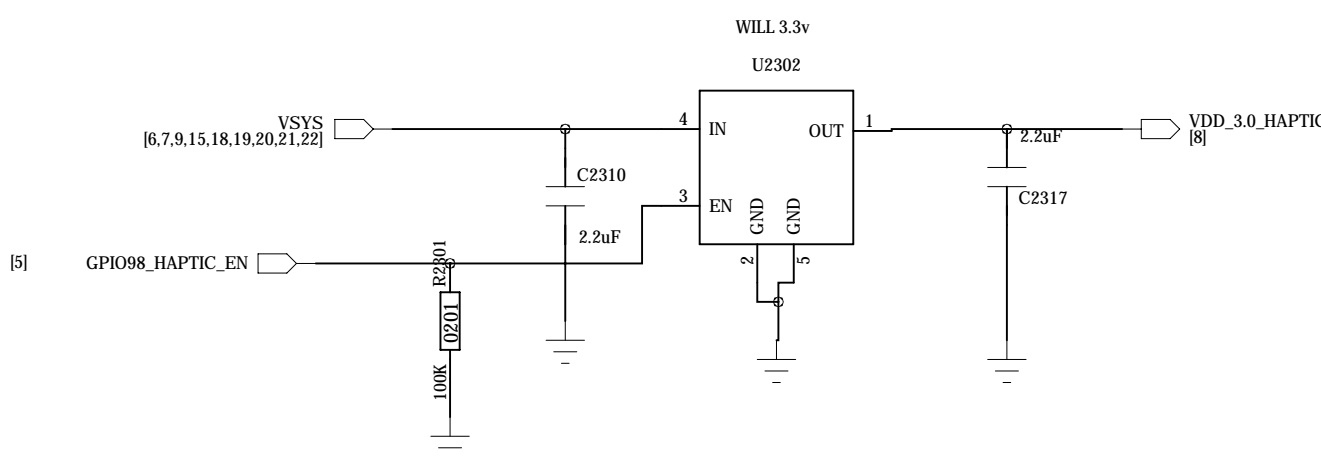
Note 20-3: Connect MT6331's K9 pin to "VCORE1_PMU" when VCORE2 is not used.



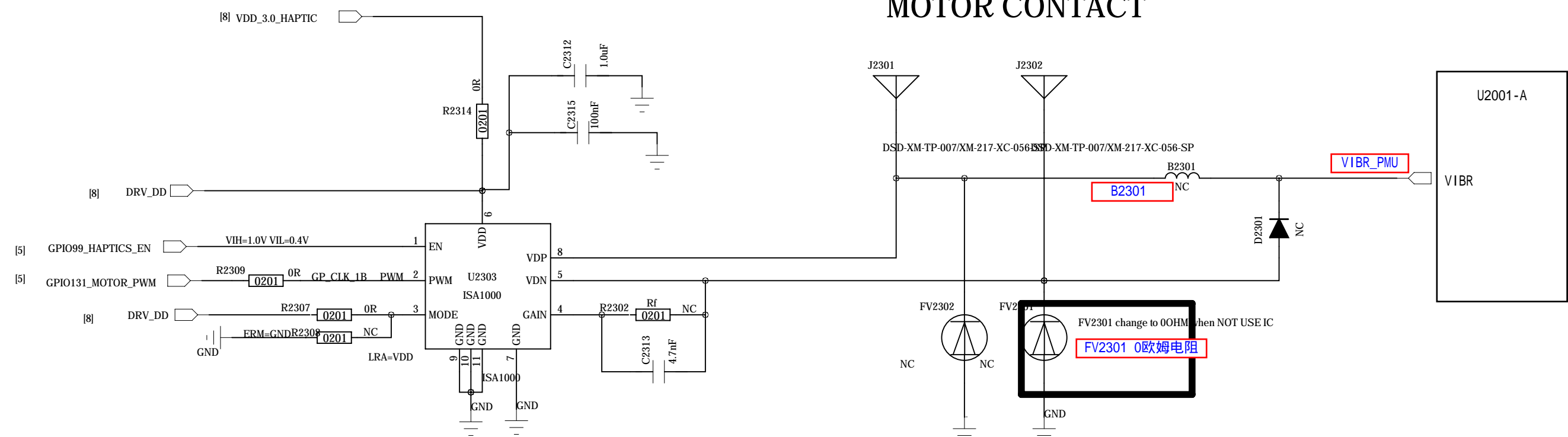
REVISION RECORD			
ITER	REV.NO	APPROVED	DATE



MOTOR DRIVER CIRCUIT



MOTOR CONTACT

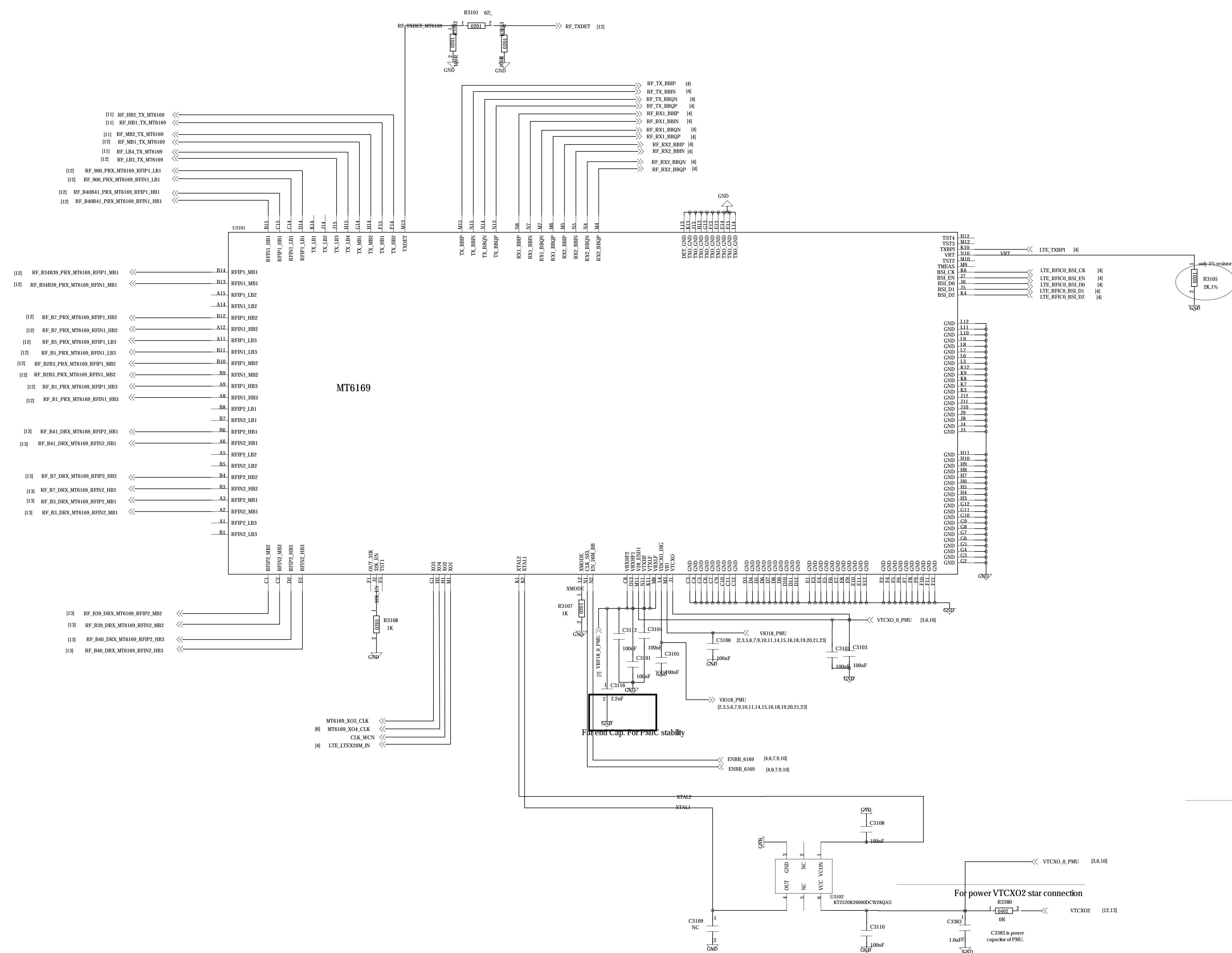


COMPANY: <Company Name>

TITLE: <Title>

DRAWN	<Drawn By>	DATED	<Drawn Date>	CODE	SIZE	DRAWING NO.	REV.
CHECKED	<Checked By>	DATED	<Checked Date>				
QUALITY CONTROL	<QC By>	DATED	<QC Date>				
RELEASED	<Released By>	DATED	<Release Date>				

SCALE: <Scale> SHEET: 8 OF 23



ASM_Main

2.0*2.0*0.6mm

SP16T

2.5*2.5*1.5mm

2G_PAIN_HB

2G_PAIN_LB

??????????????

B3 TRX

B2 TRX

B7 TRX

1.8*1.4

B1 TRX

1.8*1.4

B5 TRX

1.8*1.4

B40/B41 PRX

B8 TRX

1.8*1.4

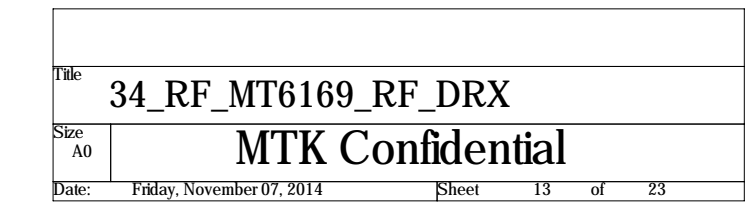
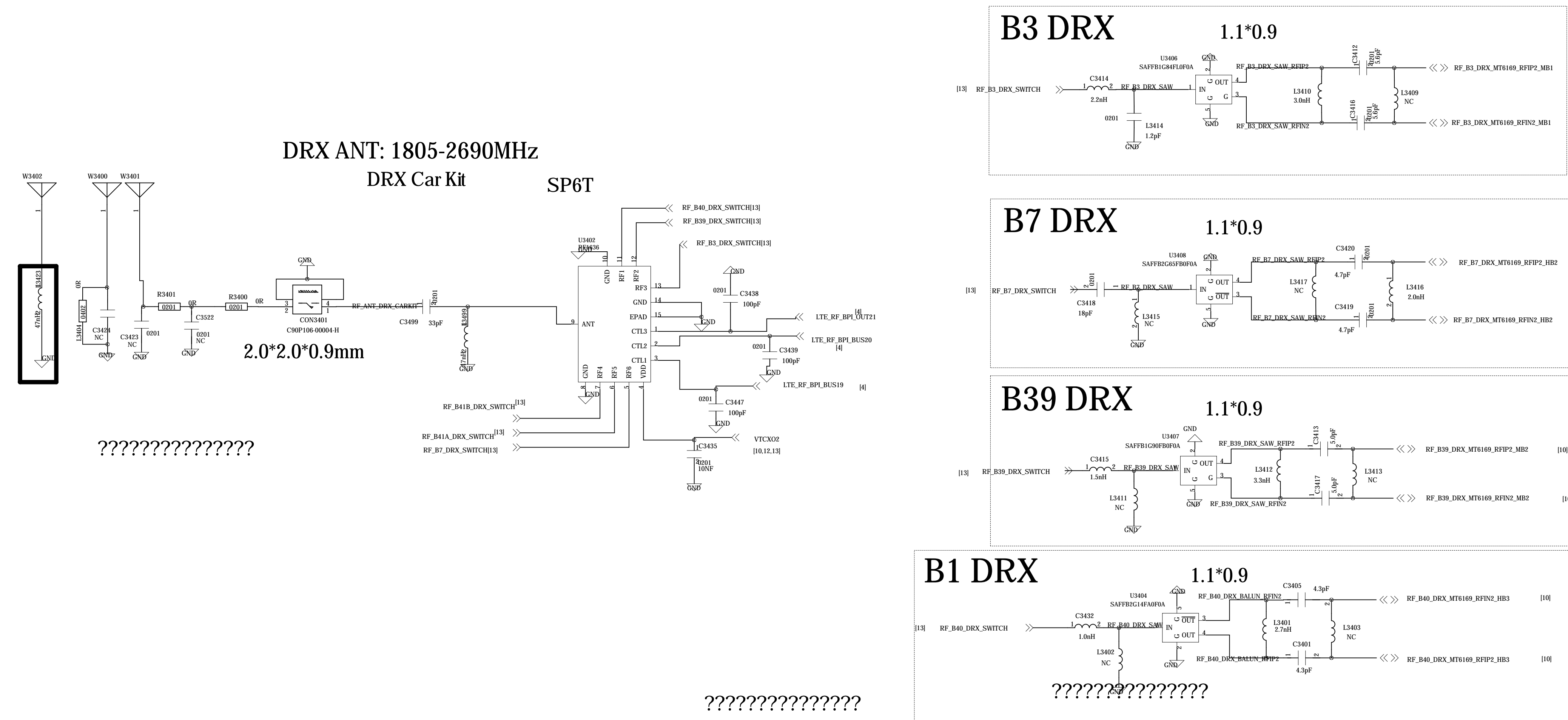
B34&B39 PRX

1.5*1.1

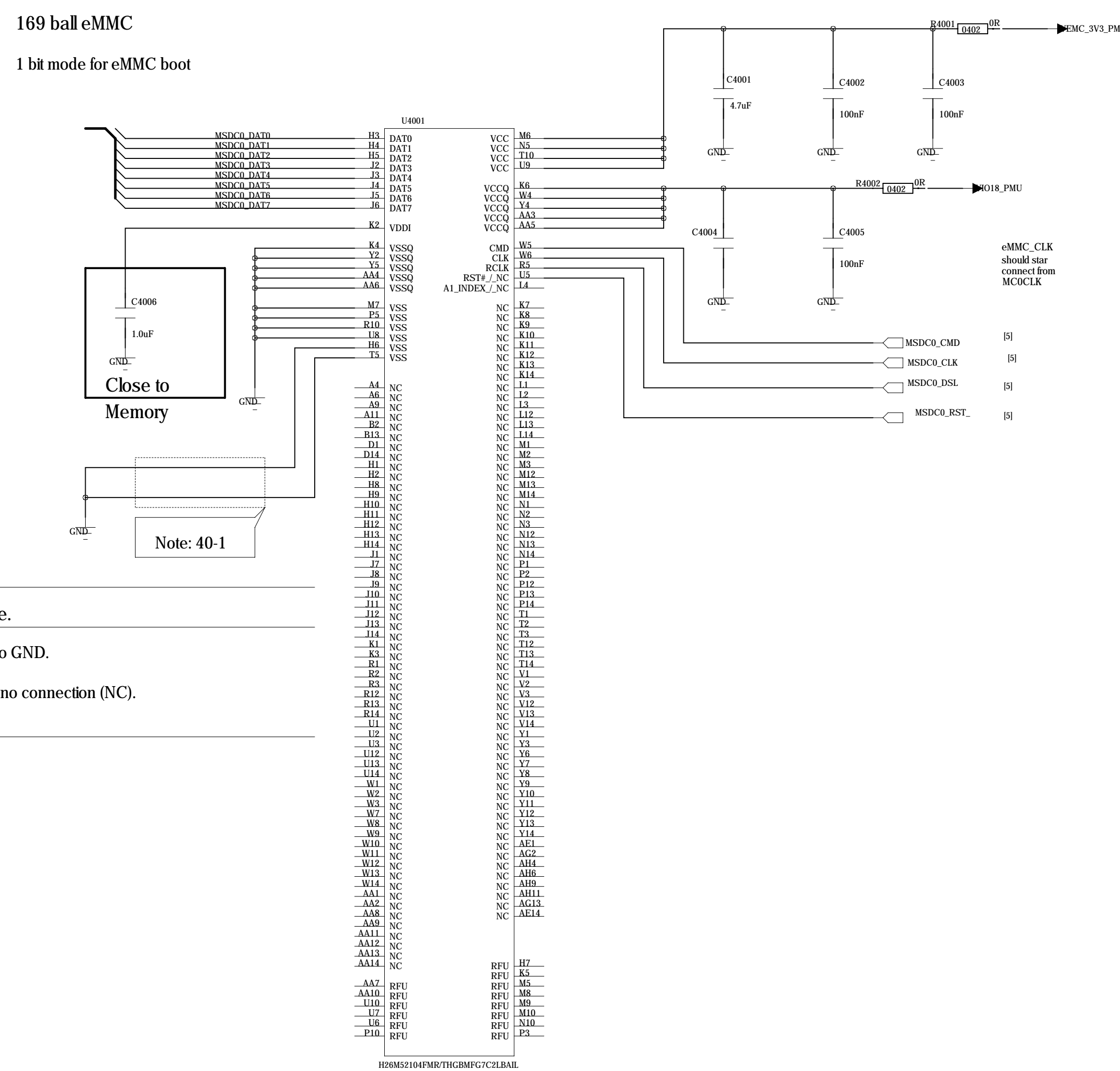
33_RF_MT6169_RF_PRX

MTK Confidential

Date: Friday, November 07, 2014 Sheet: 19 of 33



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

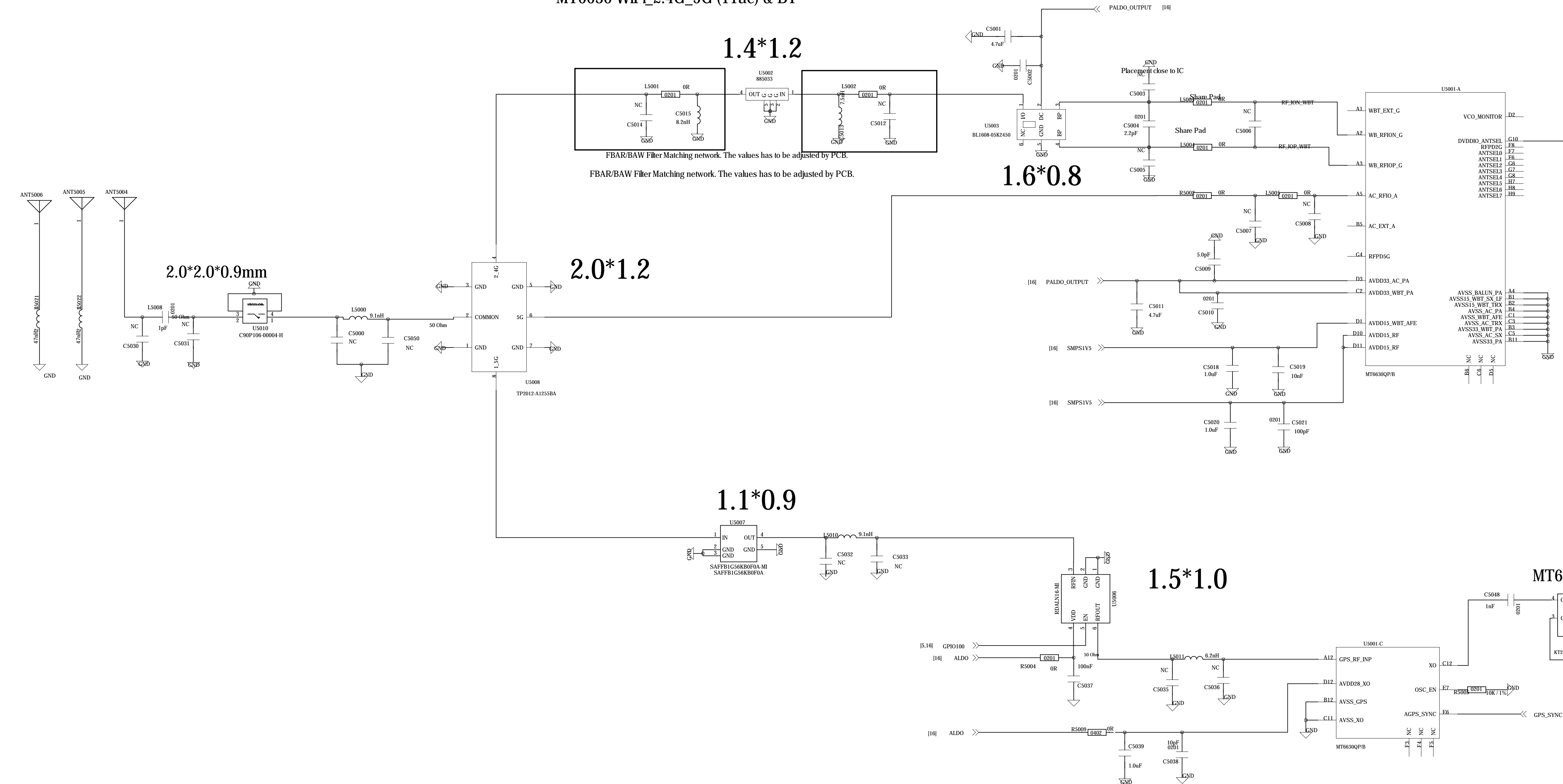


Schematic design notice of "40_MEMORY_eMMC" page.

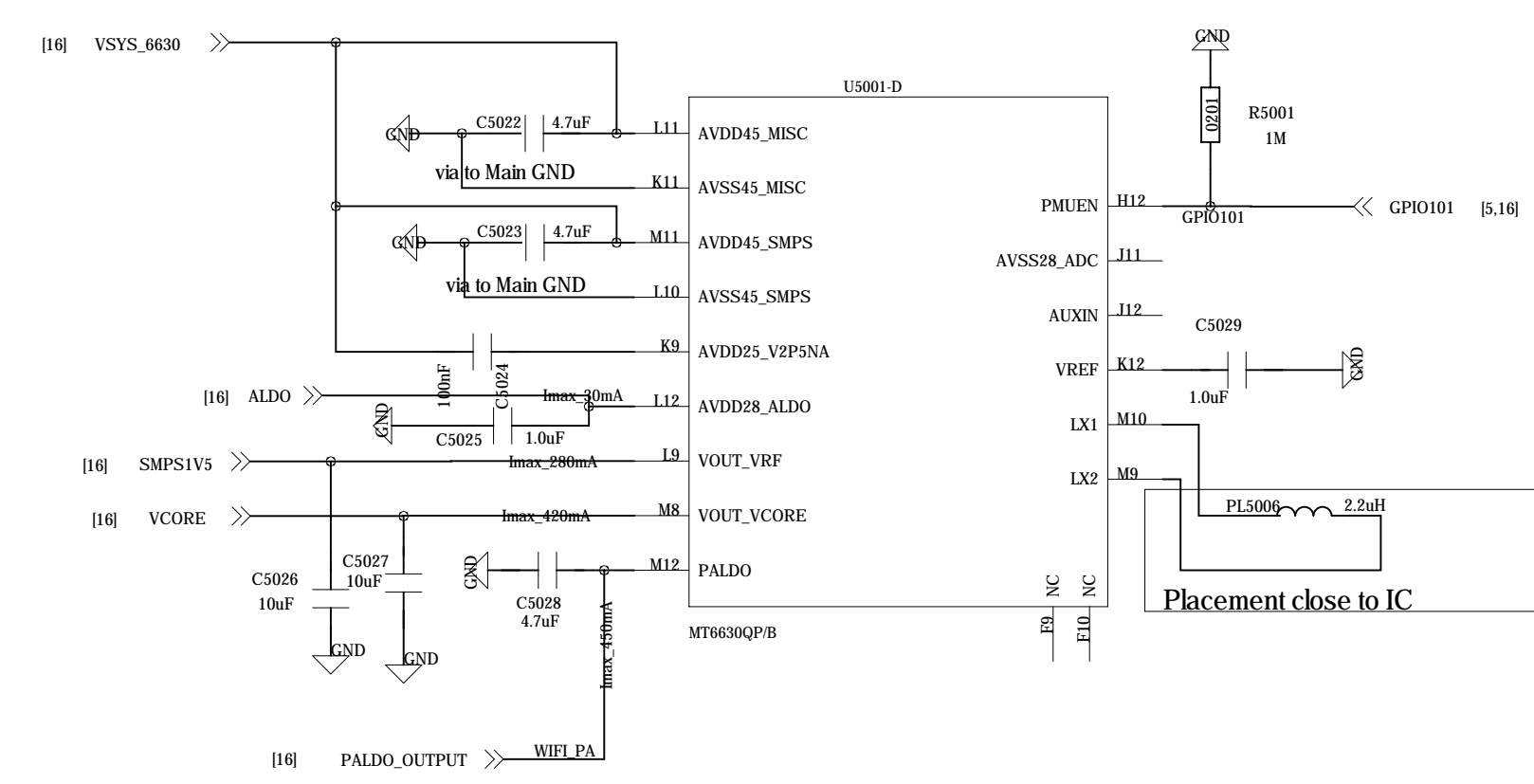
Note 40-1: For eMMC 5.0, connect eMMC's H6 & T5 pin to GND.
For eMMC 4.5, check eMMC's H6 & T5 is real no connection (NC).

		COMPANY: <div><Company Name></div>			
		TITLE: <div><Title></div>			
DRAWN: <div><Drawn By></div>	DATED: <div><Drawn Date></div>				
CHECKED: <div><Checked By></div>	DATED: <div><Checked Date></div>				
QUALITY CONTROL: <div><QC By></div>	DATED: <div><QC Date></div>	CODE: <div><Code></div>	SIZE: <div>A0</div>	DRAWING NO: <div><Drawing Number></div>	REV: <div><Revision></div>
RELEASED: <div><Released By></div>	DATED: <div><Release Date></div>	SCALE: <div><Scale></div>		SHEET: <div>44</div> <div>23</div>	

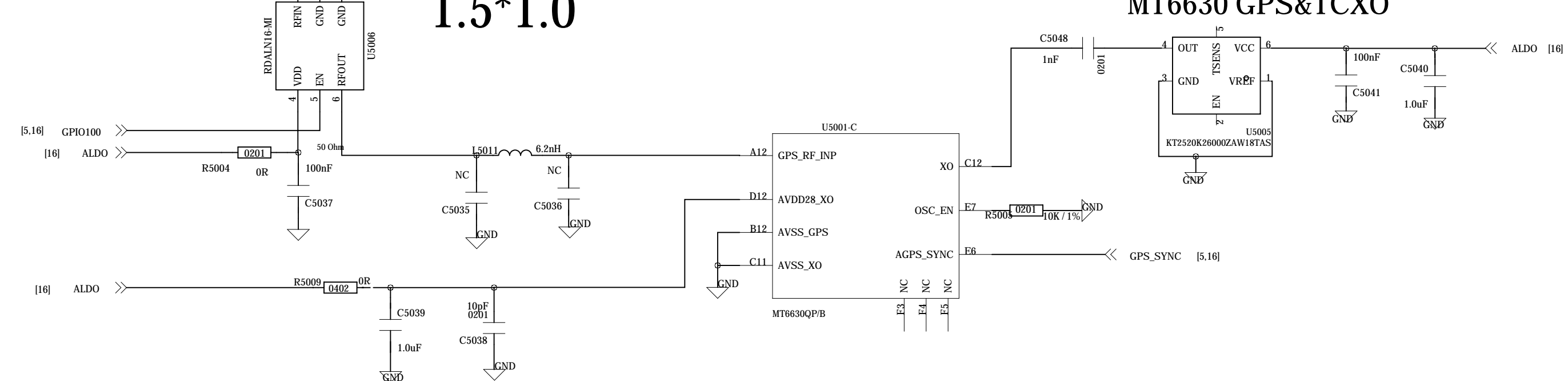
MT6630 WiFi_2.4G_5G (11ac) & BT



MT6630 PMU

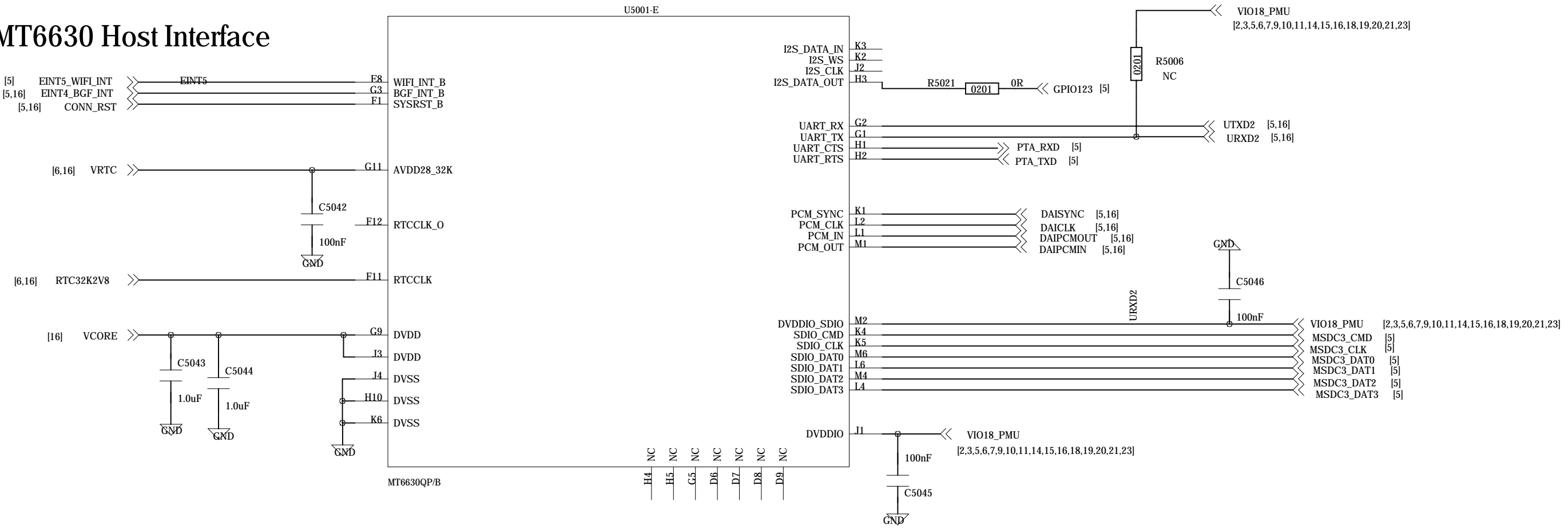


MT6630 GPS&TCXO

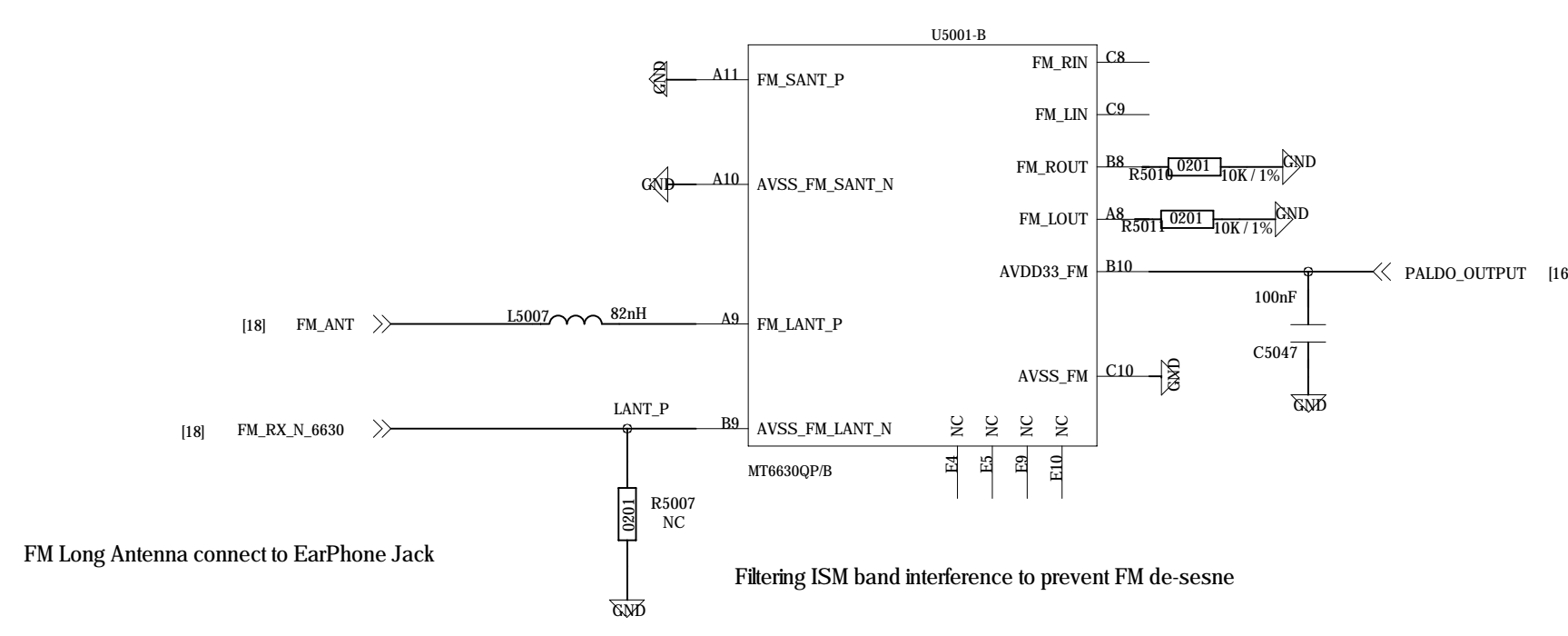
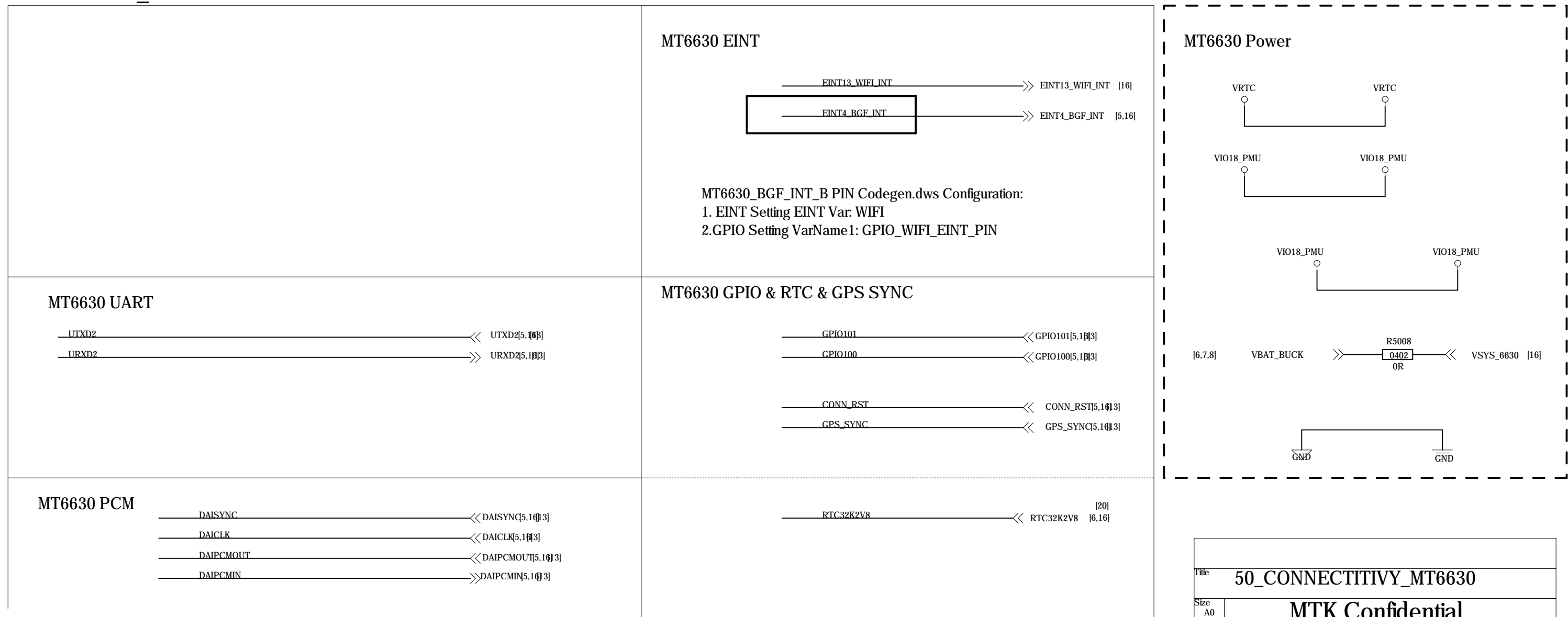


MT6630 Host Interface

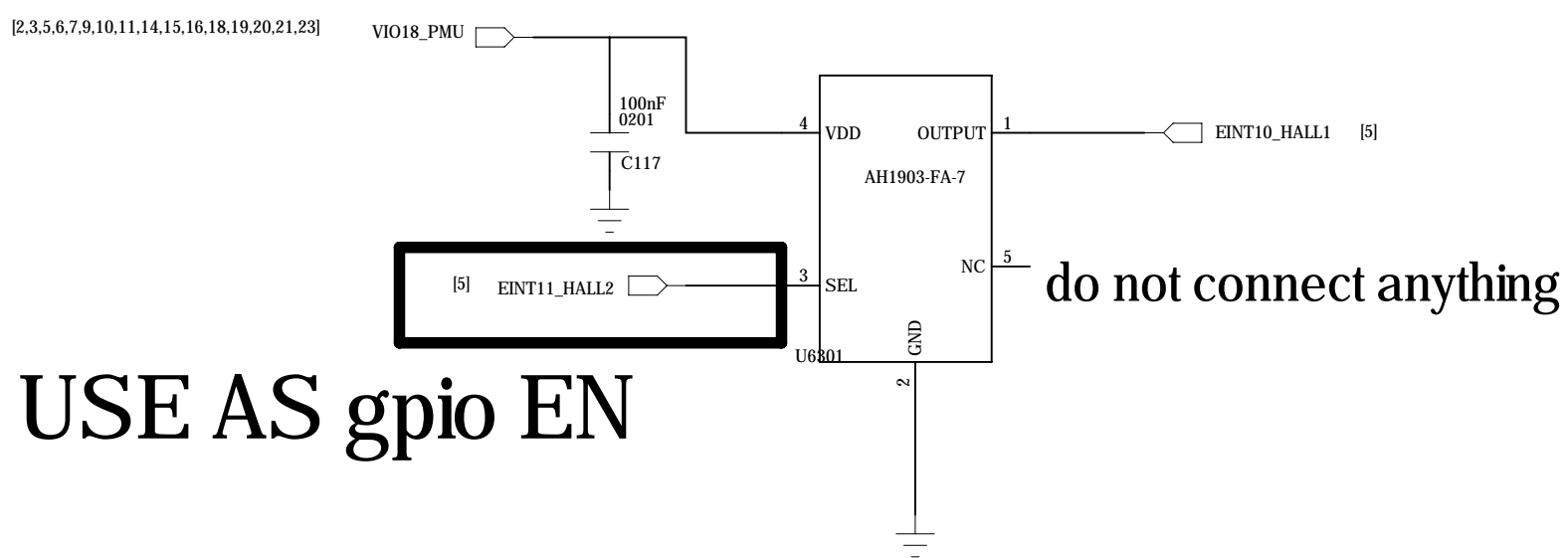
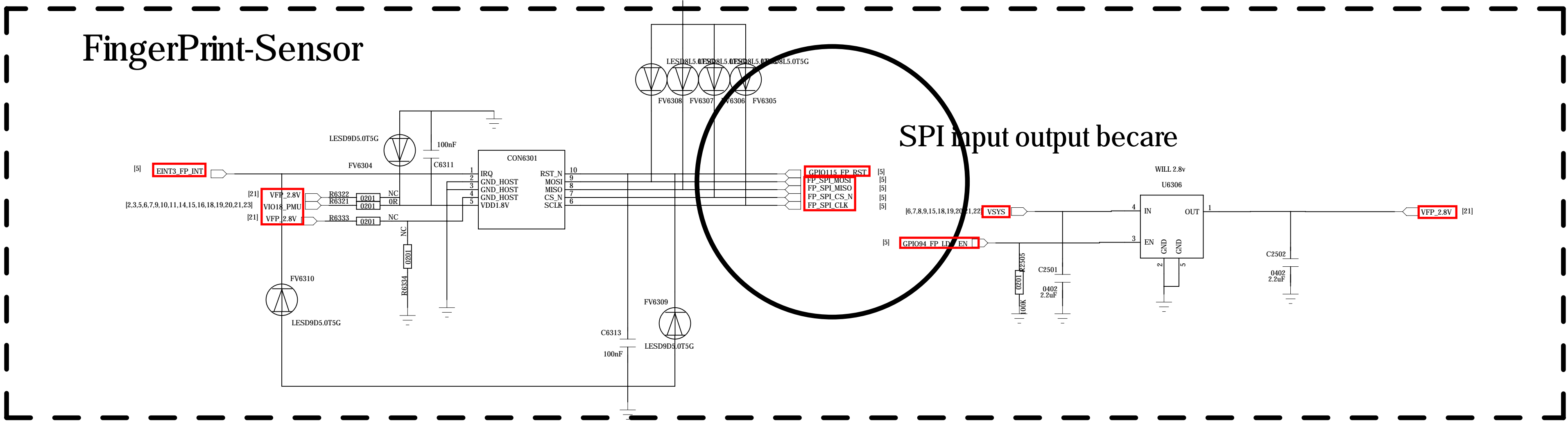
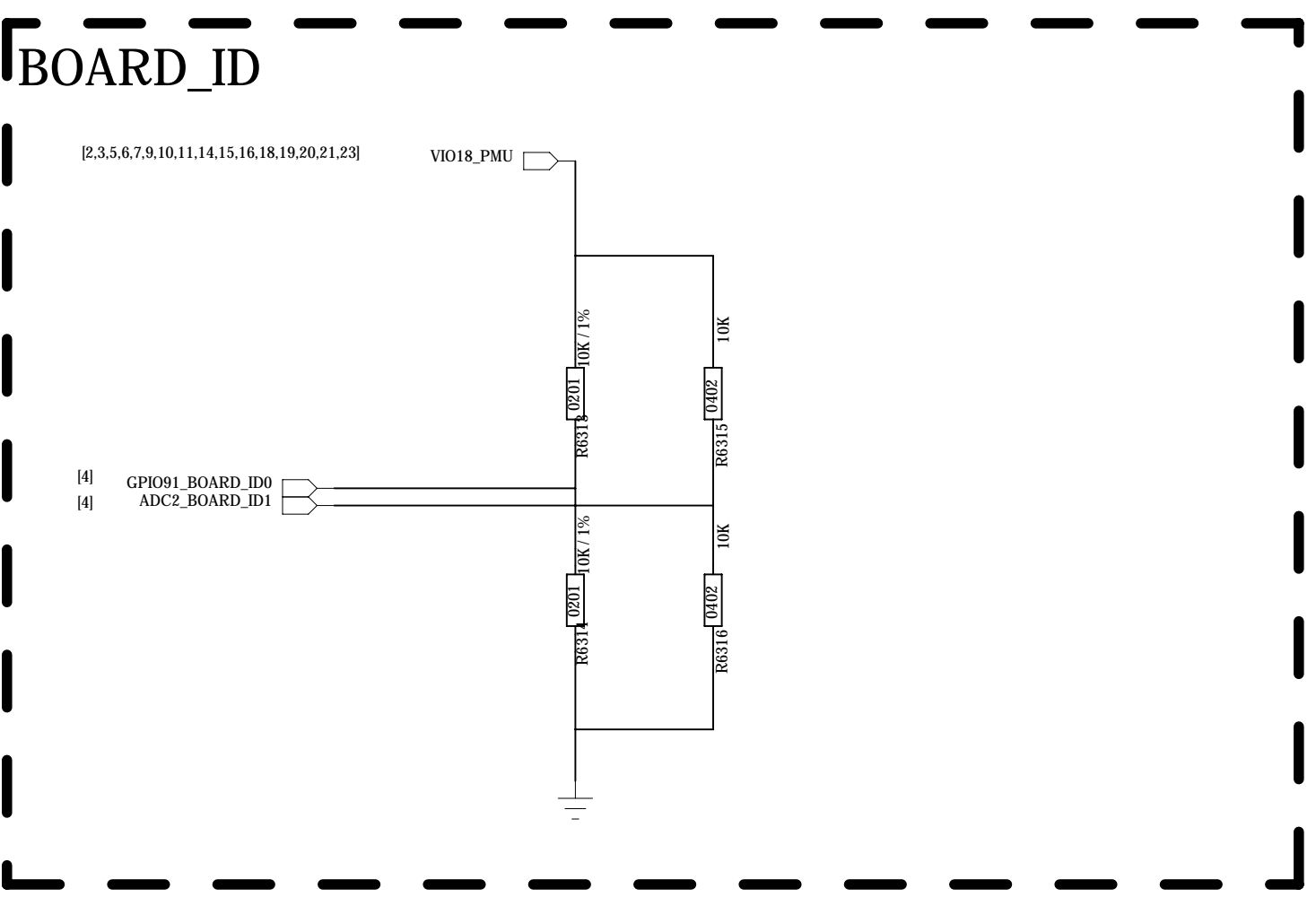
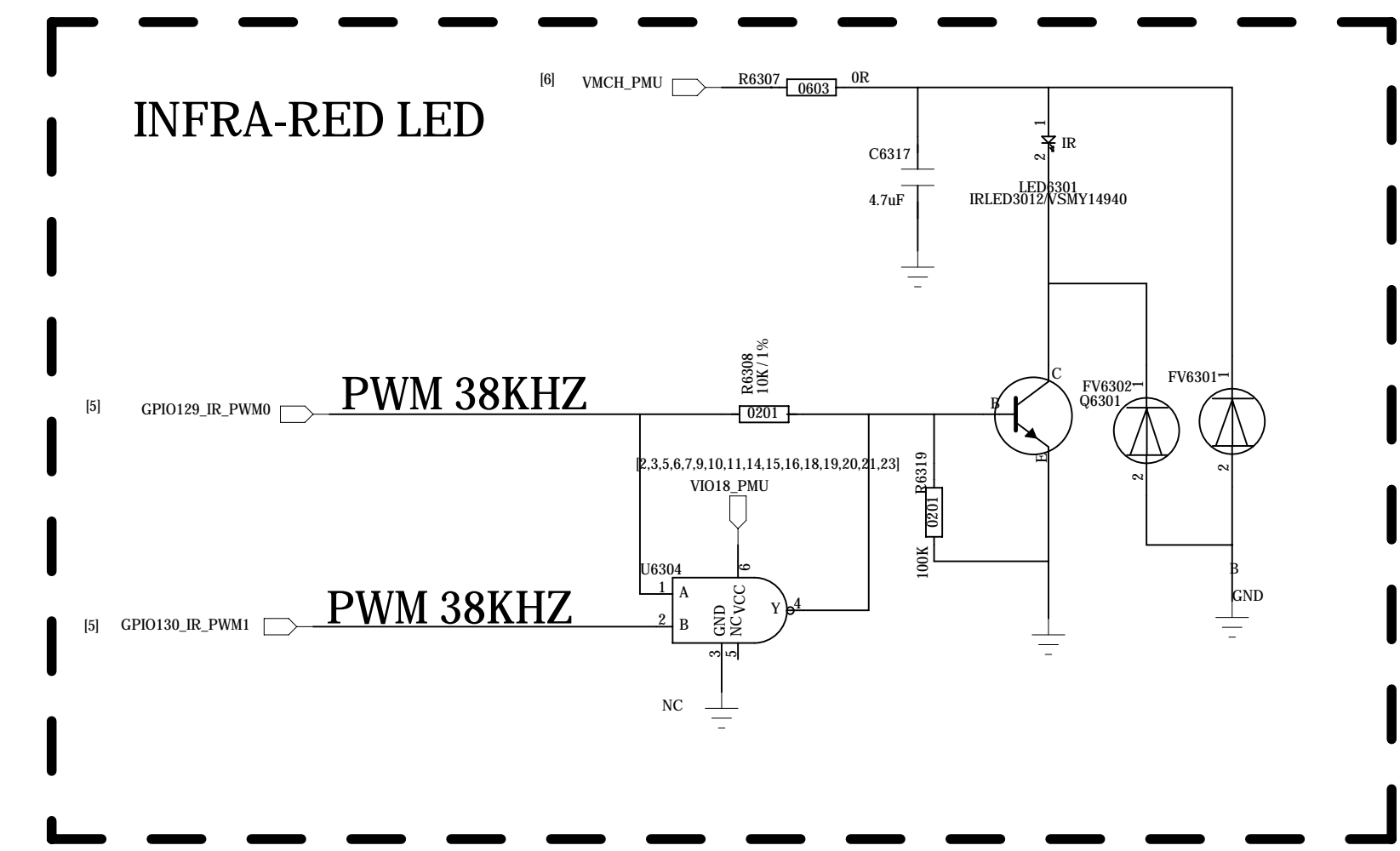
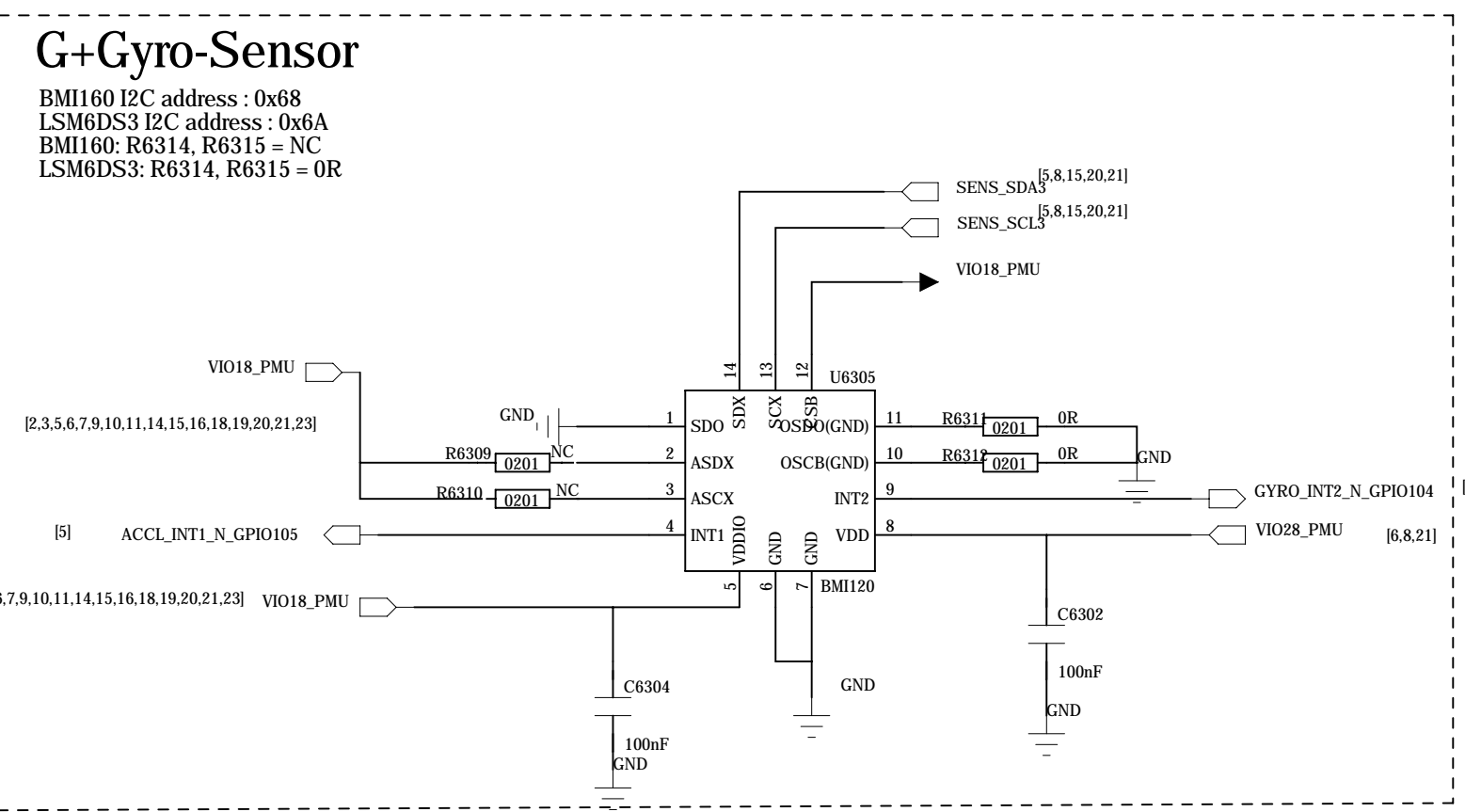
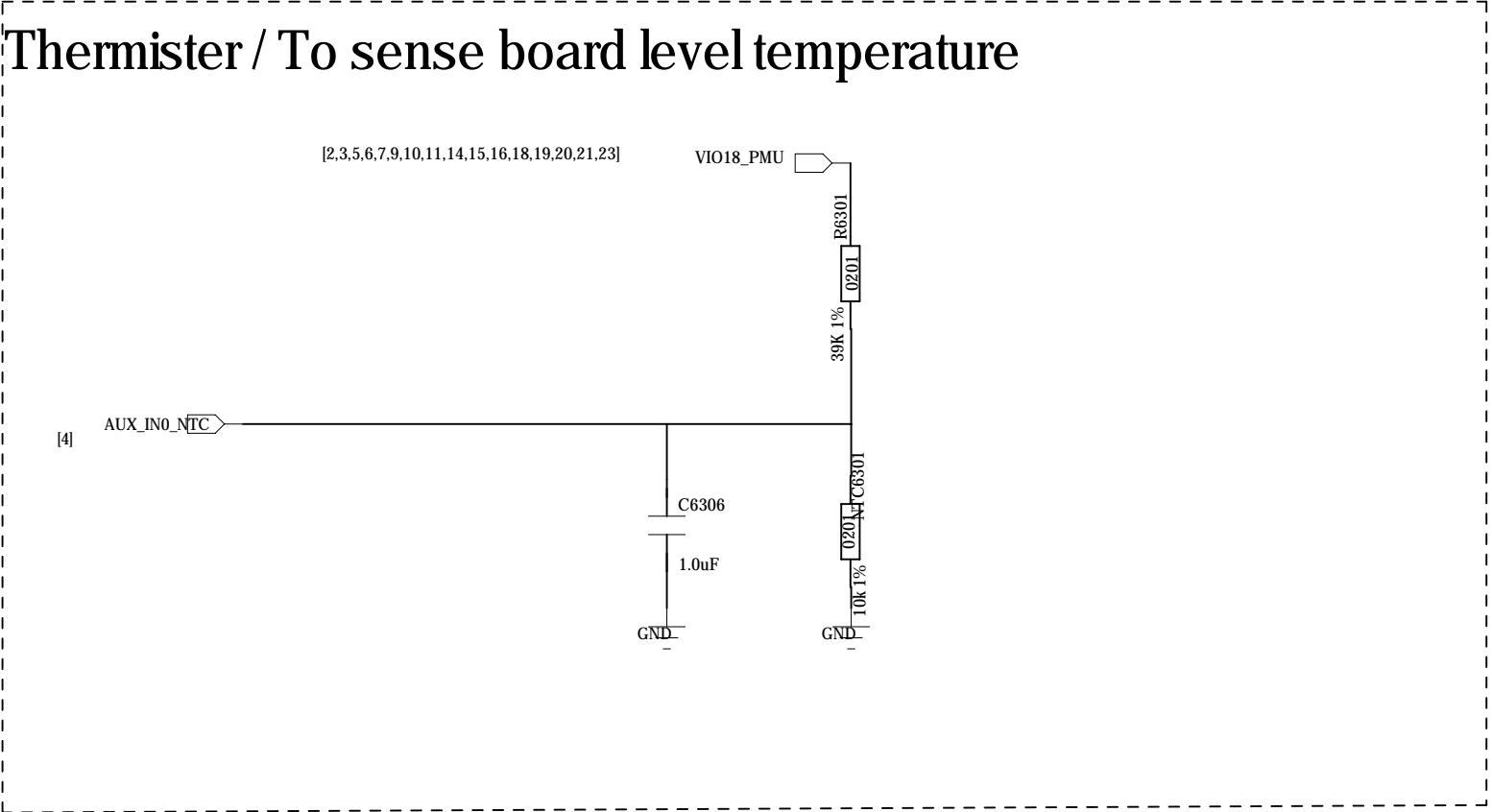
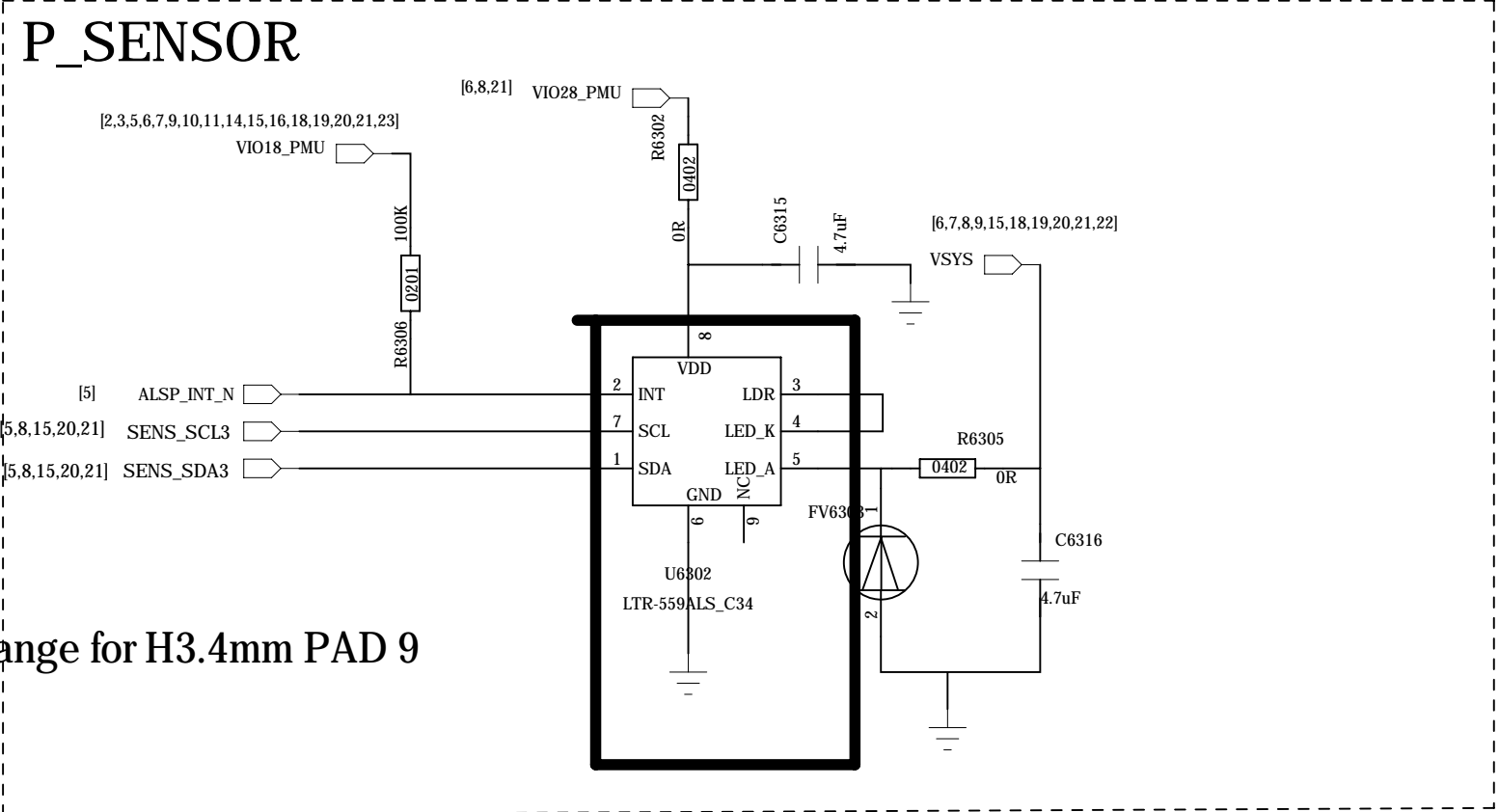
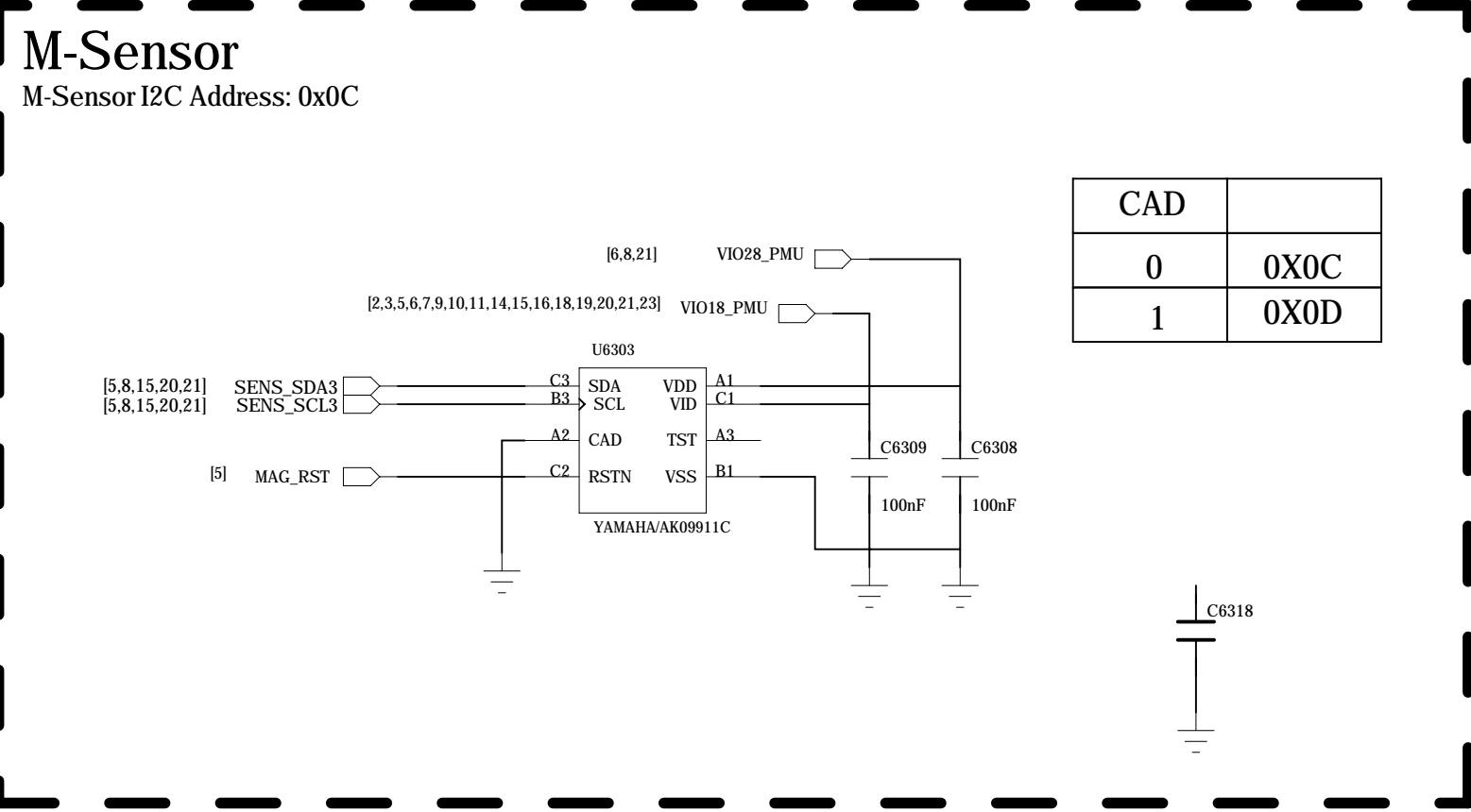
??????????????



OFF-PAGE_MT6630 Host Interface

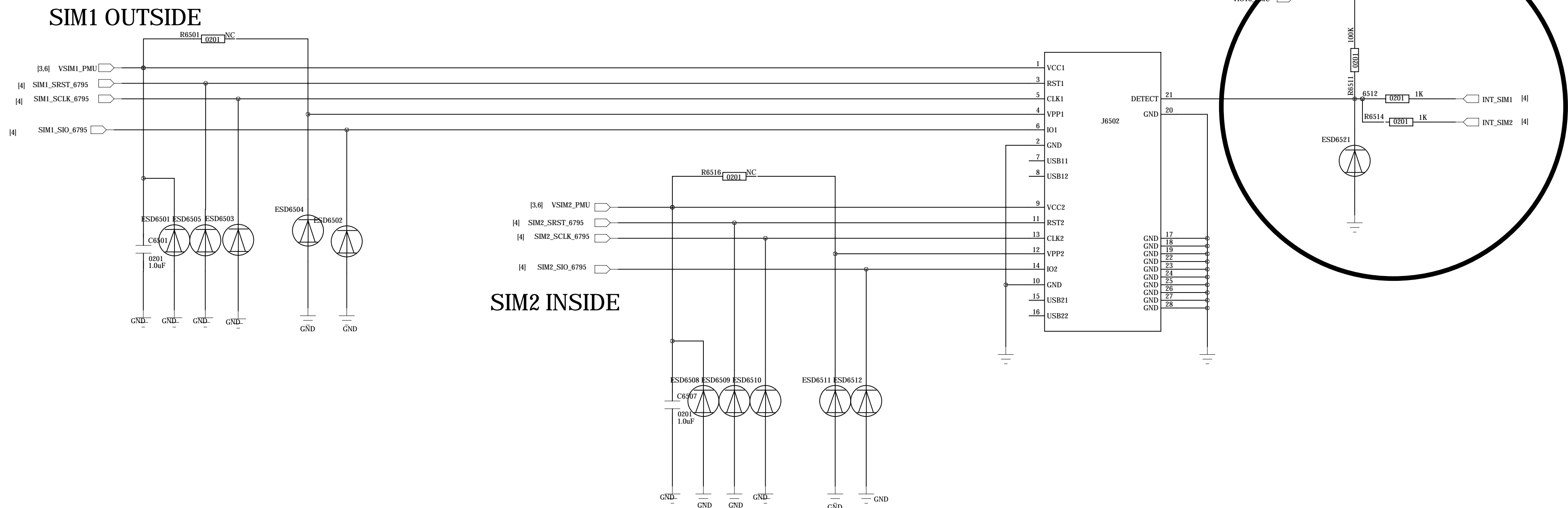


REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

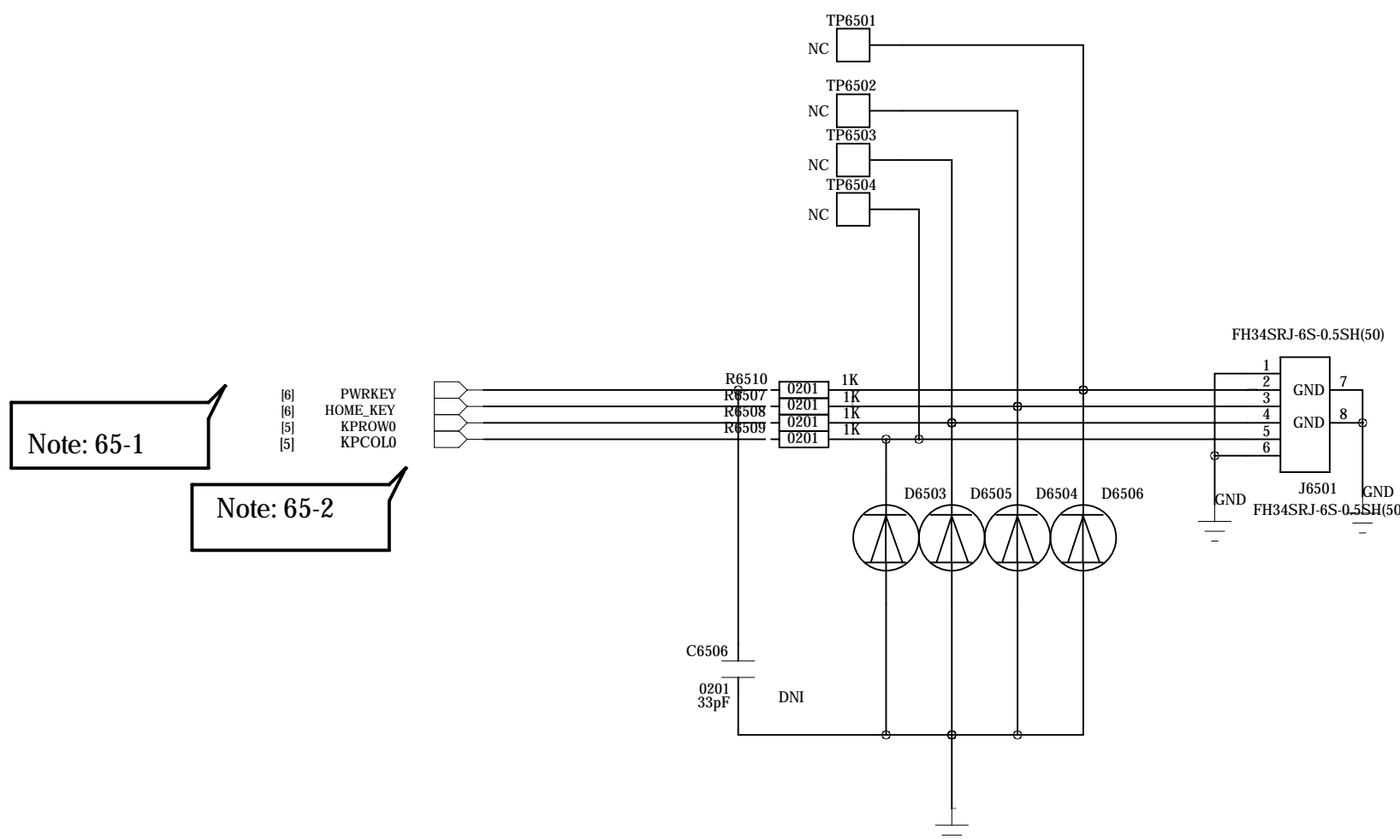


COMPANY: <Company Name>			
TITLE: <Title>			
DRAWN: <Drawn By>	DATE: <Drawn Date>	CODE: <Code>	
CHECKED: <Checked By>	DATE: <Checked Date>	SIZE: A0	DRAWING NO: <Drawing Number>
QUALITY CONTROL: <QC By>	DATE: <QC Date>	REV: <Revision>	
RELEASED: <Released By>	DATE: <Release Date>	SCALE: <Scale>	SHEET: 24 / 23

REVISION RECORD			
TYPE	REASON	APPROVED	DATE



Power Key



Schematic design notice of "65_PERI_Dual_SIM_ICUSB_KEYPAD" page.

Note 65-1: DO NOT put pull-up resistor on PWRKEY

Note 65-2: Volume Up : HOME Key / GND
Volume Down : (KPROW/KPCOL) or KPCOL / GND

COMPANY			
<Company Name>			
TITLE			
<Title>			
DRAWN	<Drawn By>	DATE	<Drawn Date>
CHECKED	<Checked By>	DATE	<Checked Date>
QUALITY CONTROL	<QC By>	DATE	<QC Date>
RELEASED	<Released By>	DATE	<Release Date>
CODE		SIZE	DRAWING NO
<Code>		A0	<Drawing Number><Revision>
SCALE		<Scale>	SHEET 23