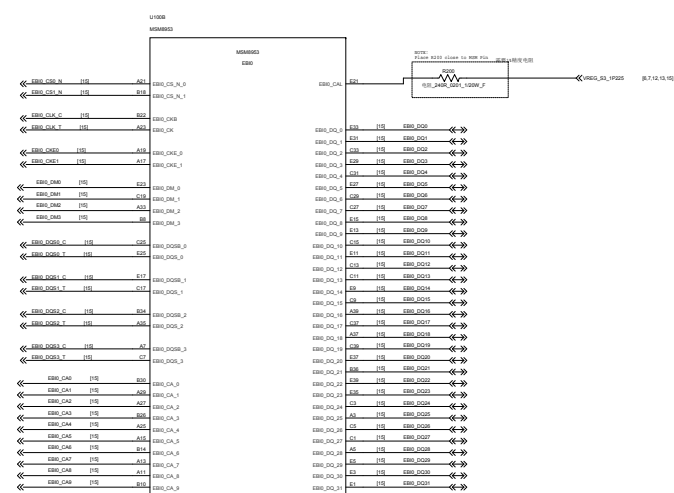


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asterisks (*) indicate modem power management (MPM) wake-up pins

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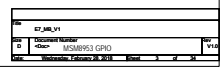
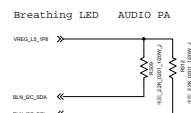


00000000	00000000
00000001	0001 -> 0002 -> 0002.0
00000010	0002 -> 0001 -> 0002.0
00000011	0003 -> 0002.0
00000100	0002.0

Default Root Config (0000): is xDCI/w



NOTE:
Ensure SW sets these GPIOs (Sensor, CTP and Camera I2C bus) to inout pull down when the peripherals are powered off to eliminate leakage.



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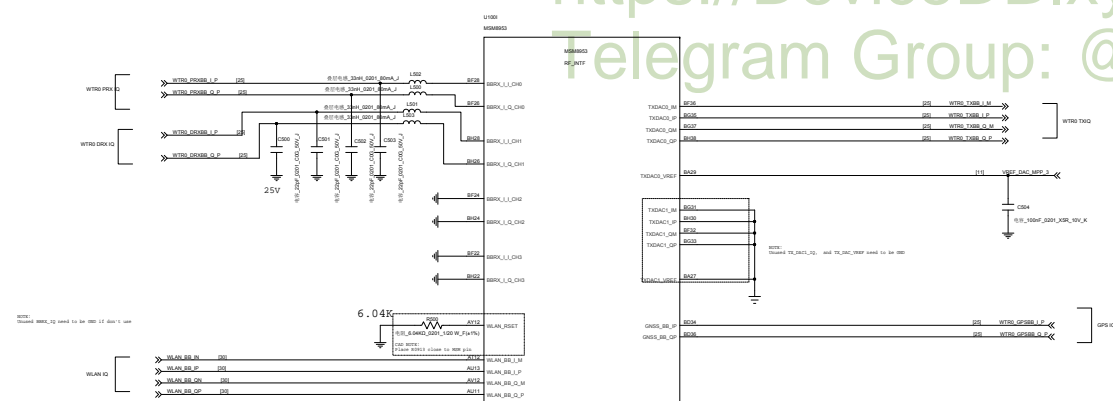
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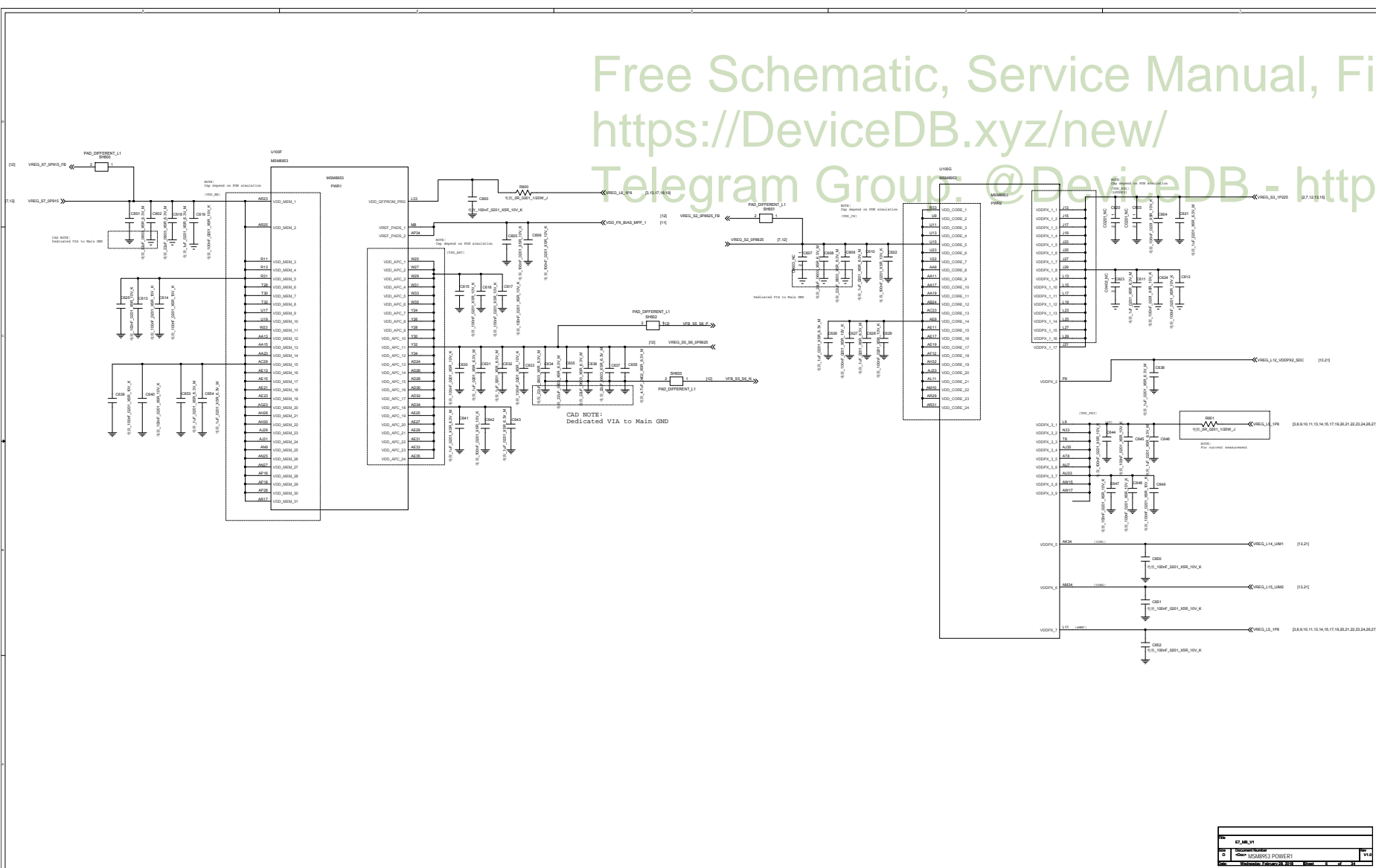


CSI Pin Name	CSI DPHY 4-lane	CSI DPHY 2+1 Mode	CSI CPHY 3Phase Mode
CS10_3PHASE_P1N0	CS10_CLKP	CS10_2LANE_CLKP	NC
CS10_3PHASE_P1N1	CS10_CLKN	CS10_2LANE_CLKN	CS10_TRI0_A
CS10_3PHASE_P1N2	CS10_DP0	CS10_2LANE_DP0	CS10_TRI0_B
CS10_3PHASE_P1N3	CS10_DP1	CS10_2LANE_DP1	CS10_TRI0_C
CS10_3PHASE_P1N4	CS10_DP1	CS10_2LANE_DP1	CS10_TRI1_A
CS10_3PHASE_P1N5	CS10_DP1	CS10_2LANE_DP1	CS10_TRI1_B
CS10_3PHASE_P1N6	CS10_DP2	CS10_2LANE_DP2	CS10_TRI1_C
CS10_3PHASE_P1N7	CS10_DP2	CS10_2LANE_DP2	CS10_TRI2_A
CS10_3PHASE_P1N8	CS10_DP3	CS10_2LANE_CLKP	CS10_TRI2_B
CS10_3PHASE_P1N9	CS10_DP3	CS10_2LANE_CLKN	CS10_TRI2_C
CS11_3PHASE_P1N0	CS11_CLKP	CS11_2LANE_CLKP	NC
CS11_3PHASE_P1N1	CS11_CLKN	CS11_2LANE_CLKN	CS11_TRI0_C
CS11_3PHASE_P1N2	CS11_DP0	CS11_2LANE_DP0	CS11_TRI0_B
CS11_3PHASE_P1N3	CS11_DP1	CS11_2LANE_DP1	CS11_TRI0_A
CS11_3PHASE_P1N4	CS11_DP1	CS11_2LANE_DP1	CS11_TRI1_A
CS11_3PHASE_P1N5	CS11_DP1	CS11_2LANE_DP1	CS11_TRI1_B
CS11_3PHASE_P1N6	CS11_DP2	CS11_2LANE_DP2	CS11_TRI1_C
CS11_3PHASE_P1N7	CS11_DP2	CS11_2LANE_DP2	CS11_TRI2_A
CS11_3PHASE_P1N8	CS11_DP3	CS11_2LANE_CLKP	CS11_TRI2_B
CS11_3PHASE_P1N9	CS11_DP3	CS11_2LANE_CLKN	CS11_TRI2_C
CS12_3PHASE_P1N0	CS12_CLKP	CS12_2LANE_CLKP	NC
CS12_3PHASE_P1N1	CS12_CLKN	CS12_2LANE_CLKN	CS12_TRI0_A
CS12_3PHASE_P1N2	CS12_DP0	CS12_2LANE_DP0	CS12_TRI0_B
CS12_3PHASE_P1N3	CS12_DP0	CS12_2LANE_DP0	CS12_TRI0_C
CS12_3PHASE_P1N4	CS12_DP1	CS12_2LANE_DP1	CS12_TRI1_A
CS12_3PHASE_P1N5	CS12_DP1	CS12_2LANE_DP1	CS12_TRI1_B
CS12_3PHASE_P1N6	CS12_DP2	CS12_2LANE_DP2	CS12_TRI1_C
CS12_3PHASE_P1N7	CS12_DP2	CS12_2LANE_DP2	CS12_TRI2_A
CS12_3PHASE_P1N8	CS12_DP3	CS12_2LANE_CLKP	CS12_TRI2_B
CS12_3PHASE_P1N9	CS12_DP3	CS12_2LANE_CLKN	CS12_TRI2_C

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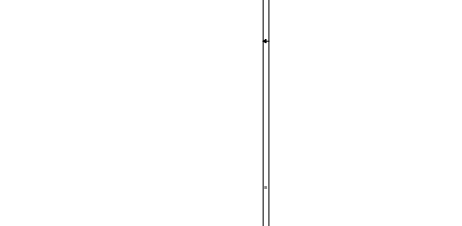
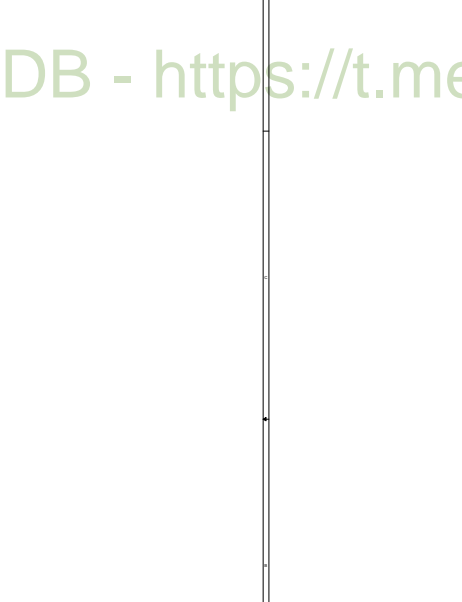
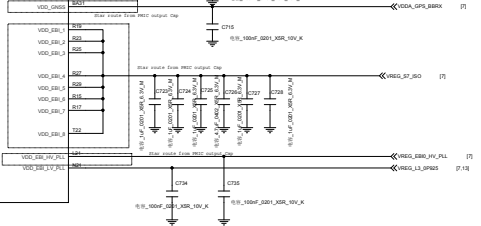
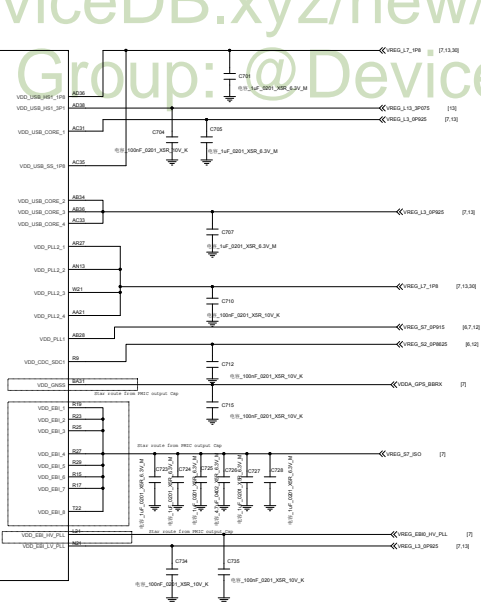
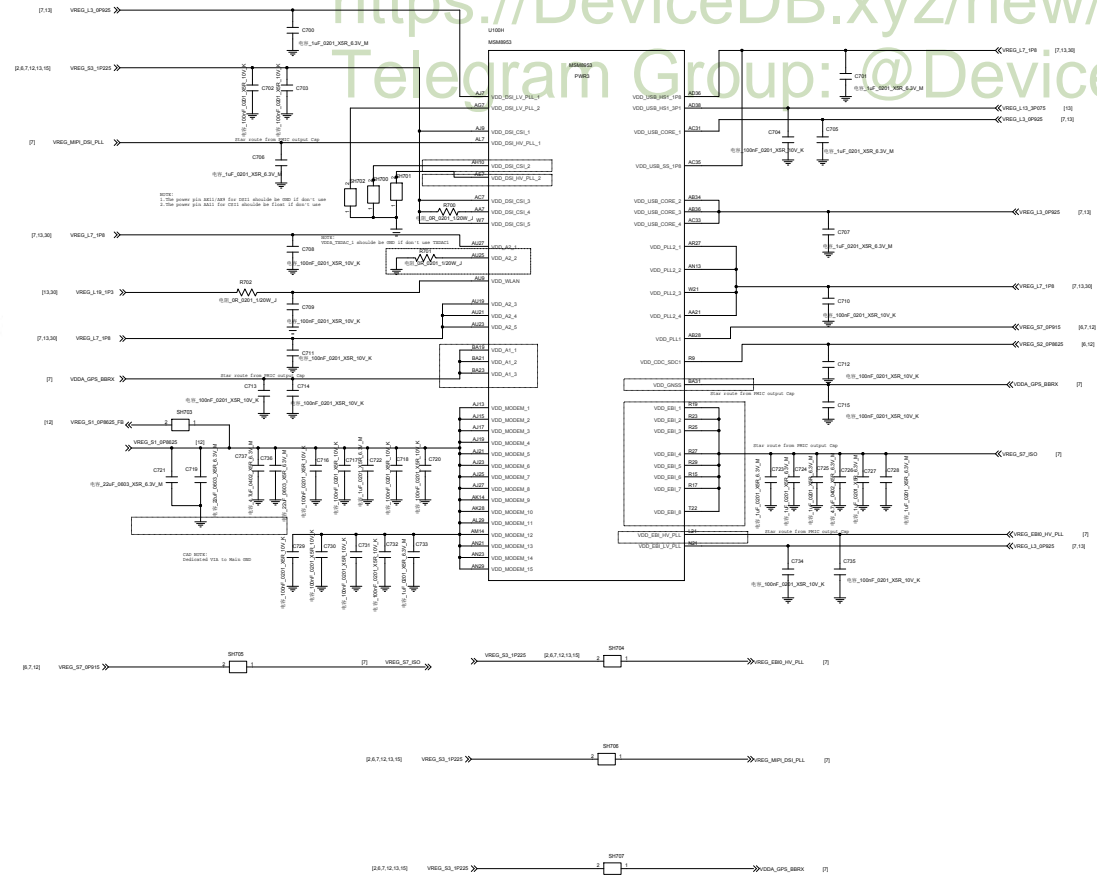
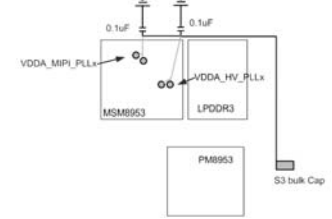
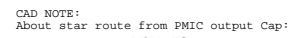


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Doc ID	Document Number	MSM8953 POWER1	
Date		Wednesday, February 28, 2018	

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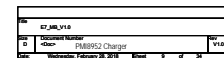
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Date	Wednesday, February 28, 2018	Page	7 of 34

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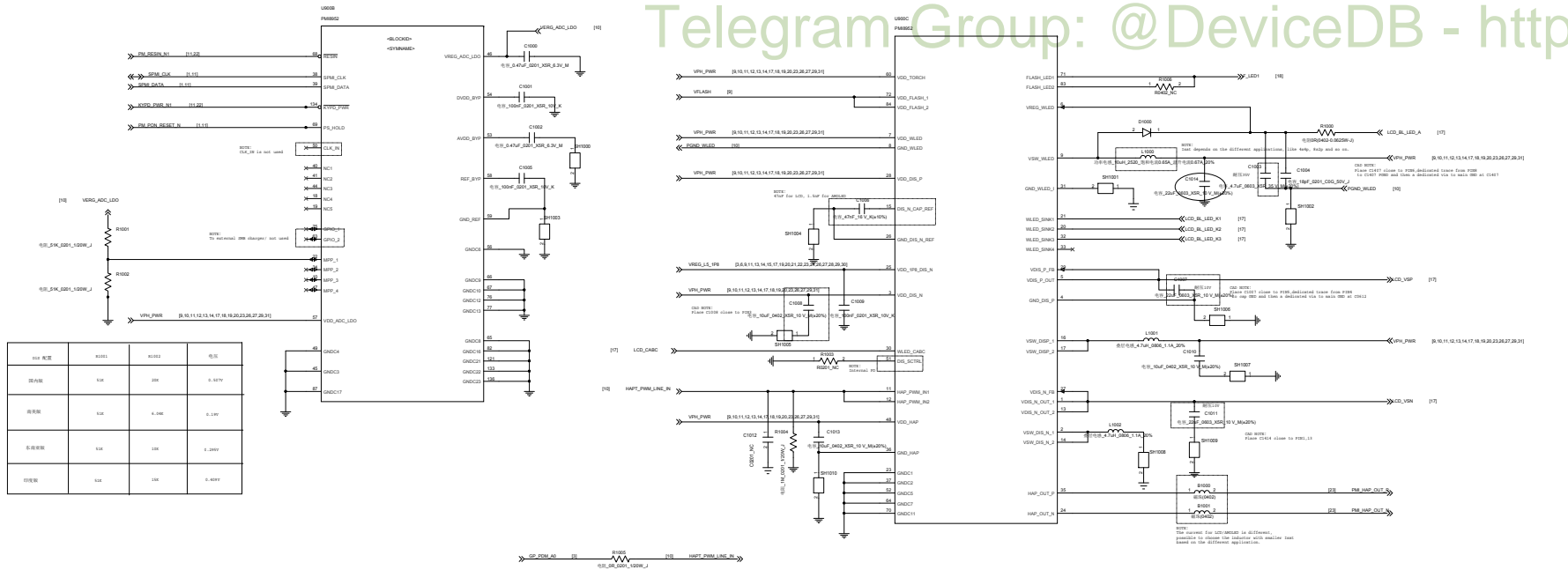
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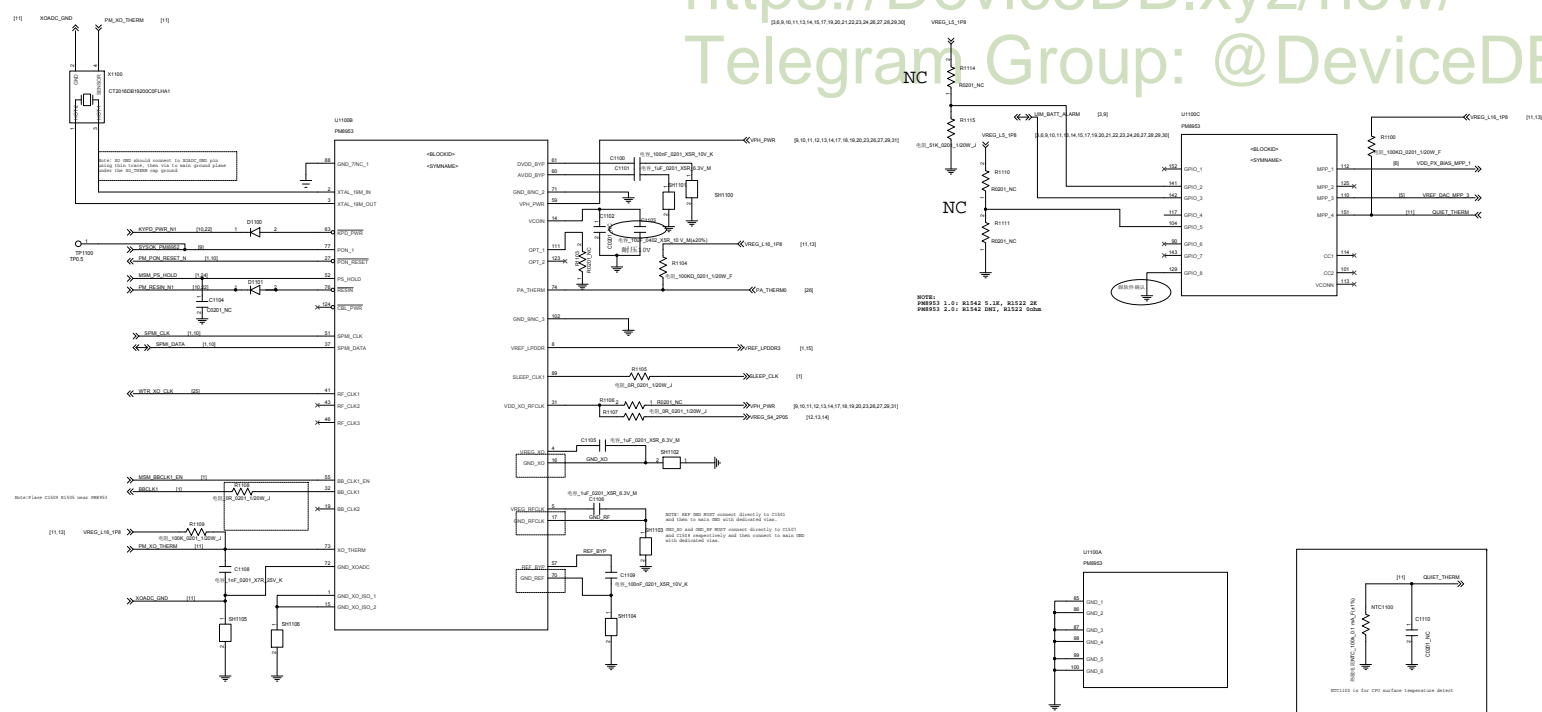


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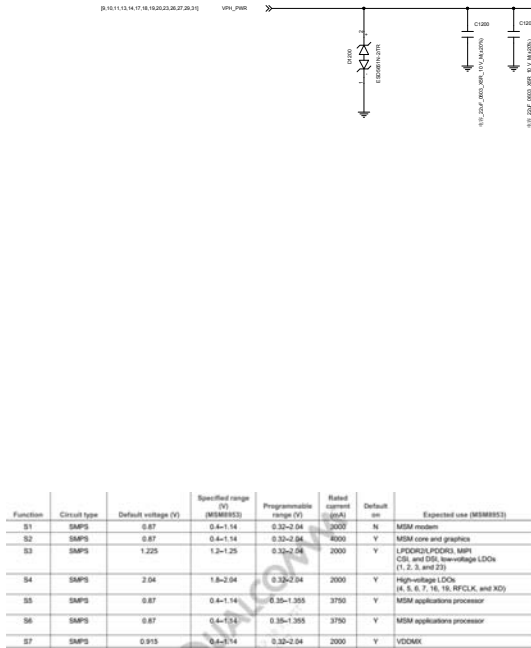
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Date: Wednesday, February 28, 2018 Email: 11 of 34									

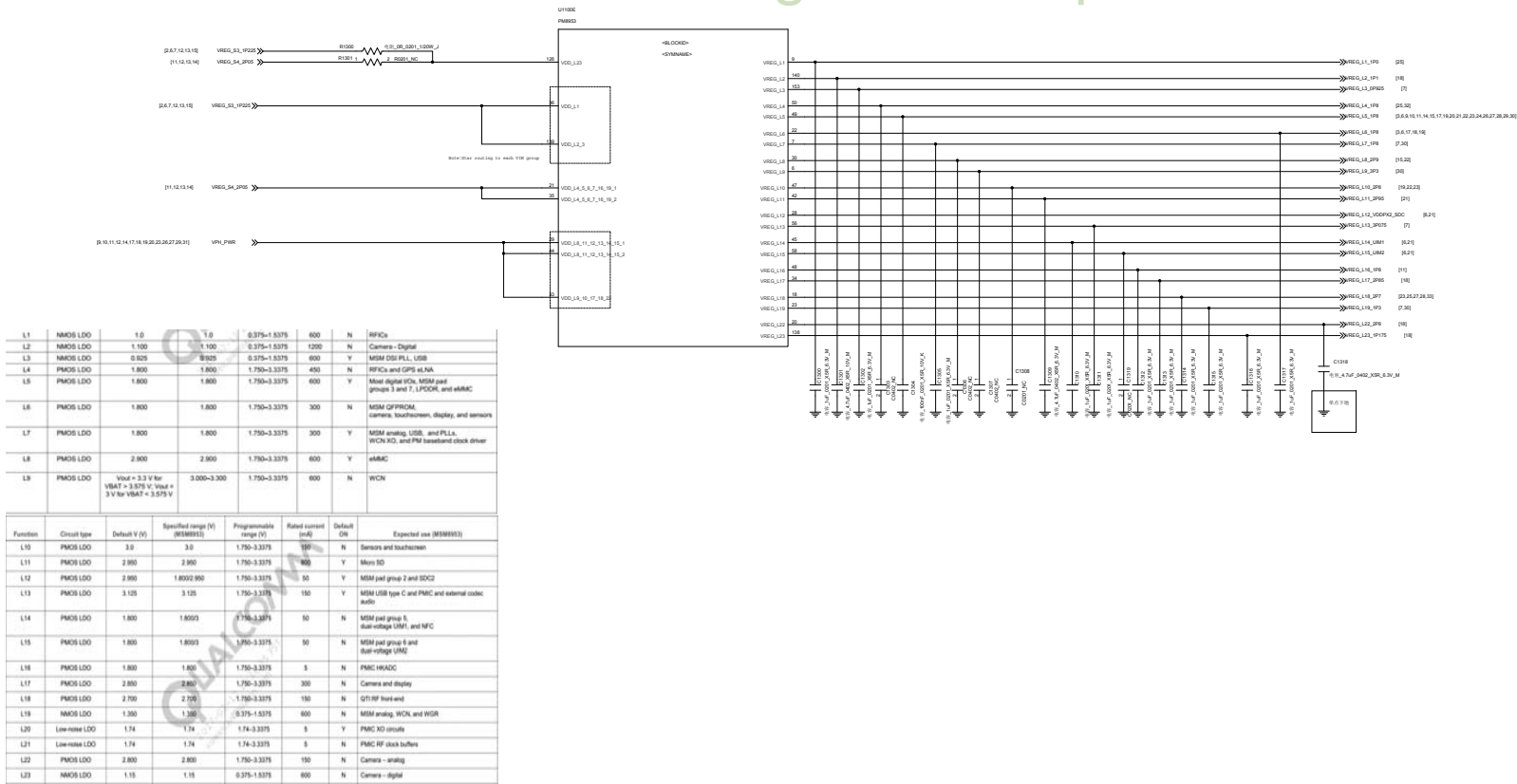
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Function	Circuit type	Default voltage (V)	Specified range (V) (MSPB0033)	Programmable range (V) (MSPB0033)	Rated voltage (V) (MSPB0033)	Default on	Expected use (MSPB0033)
53	SMPS	0.875	0.4-1.5	0.3-2.0-2.4	8000	Yes	MSPM core and graphics
52	SMPS	0.6	0.4-1.5	0.3-2.0-2.4	6000	Yes	PMICs, capacitors, LEDs, and I/Os (including LDOs)
54	SMPS	2.04	1.6-2.4	0.3-2.4-2.4	2000	Yes	High-voltage LDOs
55	SMPS	0.87	0.4-1.5	0.35-1.355	3750	Yes	PMICs, capacitors, LEDs, and I/Os (including LDOs)
56	SMPS	0.87	0.4-1.5	0.35-1.355	3750	Yes	MSPM applications processor
57	SMPS	0.915	0.4-1.5	0.3-2.4	2000	Yes	VIOCAN

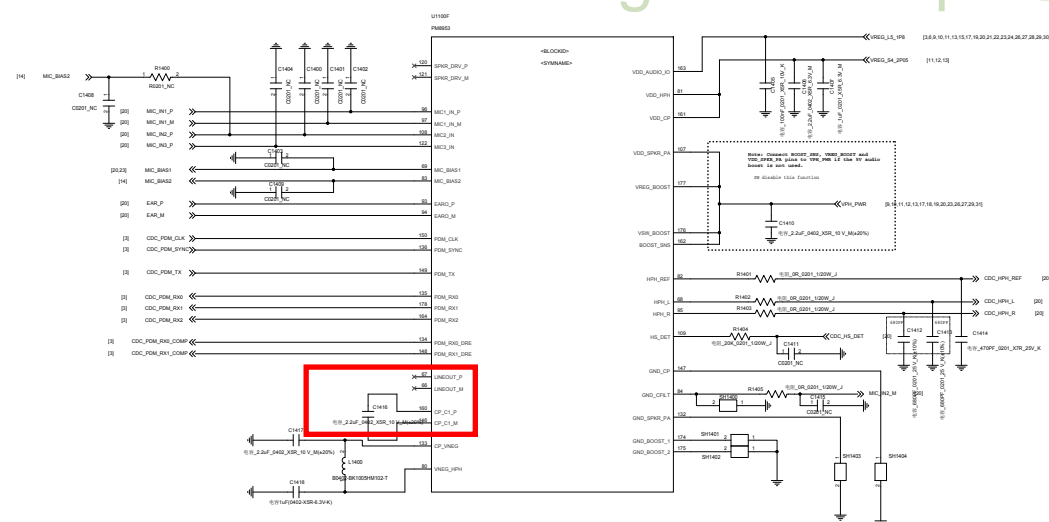
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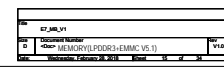
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Doc ID	Document Number PM8953 CODEC/Audio PA

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1. RU

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- FAQ, инструкция пользователя
- Обзоры, отзывы
- Техническая документация (схема, сервис мануал)
- Прошивки, драйвера, утилиты для прошивки
- и др. справочная информация

Для каждого устройства в одном месте.

Еще больше схем, сервис мануалов, прошивок и т.д.

в Группе TELEGRAM @DeviceDB - <https://teleg.one/DeviceDB>

Группа Vkontakte - <https://vk.com/devicedb>

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Добровольное поощрение за помощь

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<https://money.yandex.ru/to/410014674371723>

2.ENG

This file has been downloaded from

<https://DeviceDB.xyz> - Device Database

- FAQ, user manual
- Reviews, reviews
- Technical documentation (schematic, service manual)
- Firmware, drivers, utilities for firmware
- and other reference information

For each device in one place.

Even more schematics, service manuals, firmware, etc.

in the TELEGRAM Group @DeviceDB - <https://teleg.one/DeviceDB>

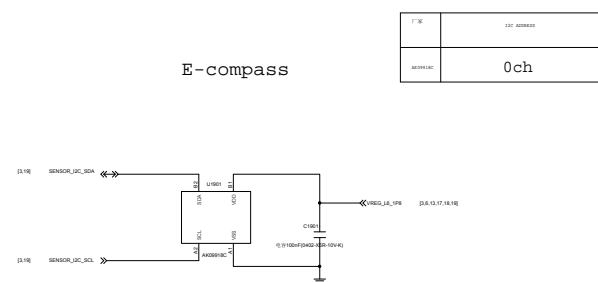
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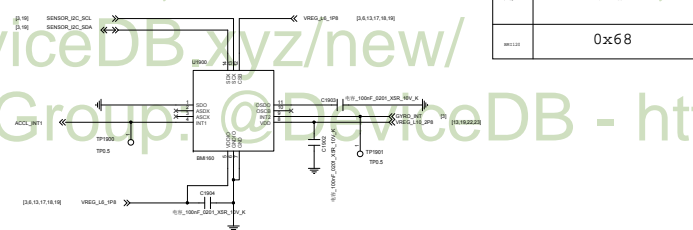
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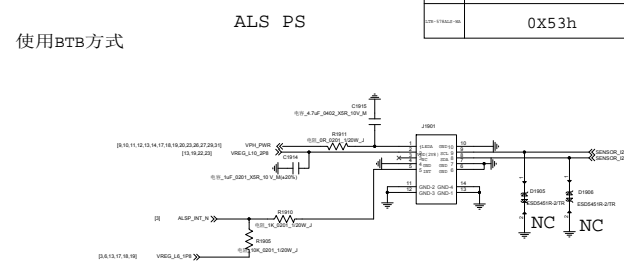
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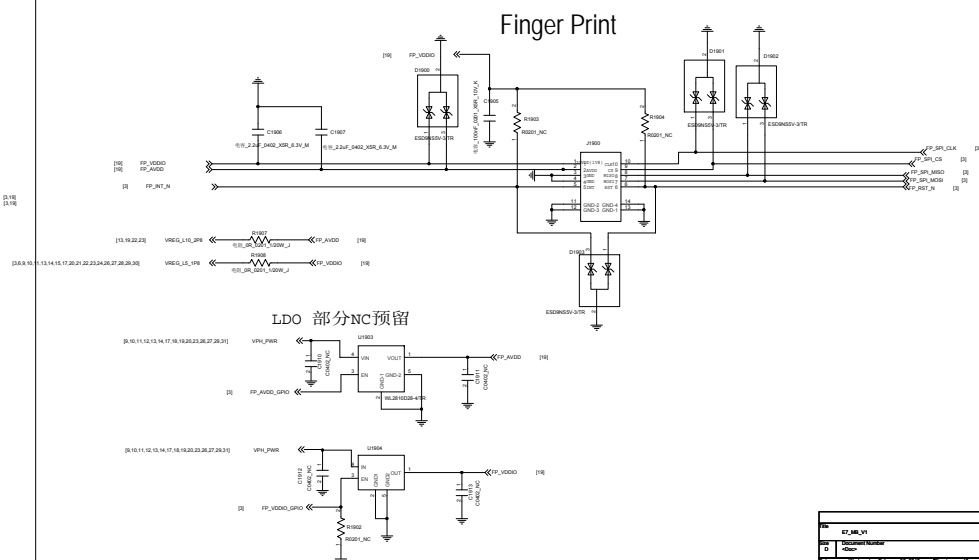
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ADDRESS	0ch



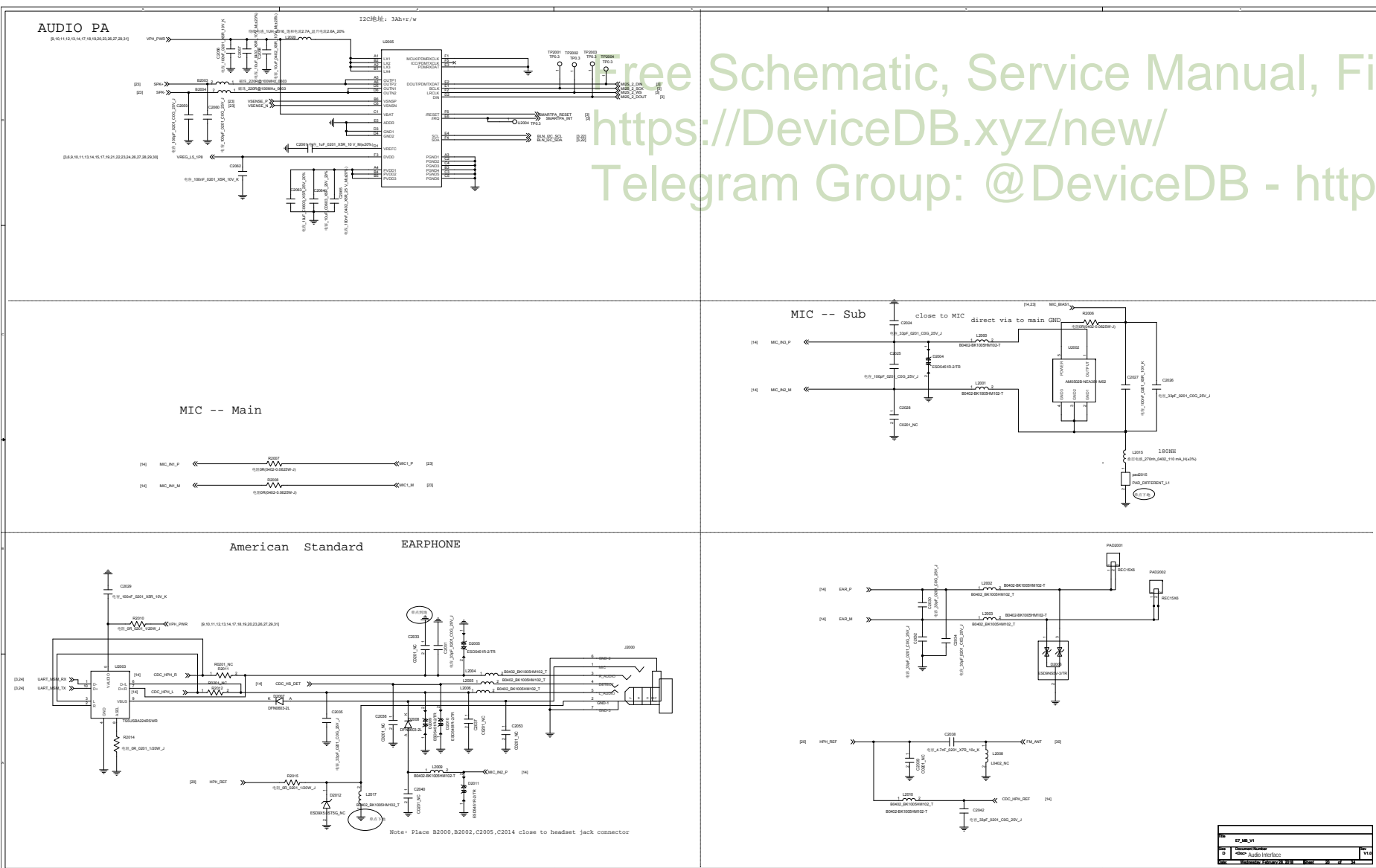
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厂家	13C ADDRESS
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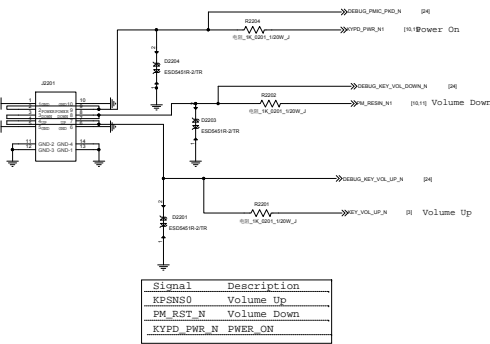
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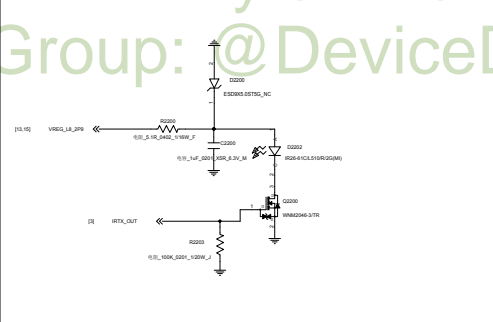
Diagram showing the connection of the USB cable to the device. The USB cable is connected to the USB port of the device. The diagram includes labels for the USB cable pins (D+, D-, GND, VCC) and the device pins (D+, D-, GND, VCC). A note indicates that the USB cable should be connected to the USB port of the device.

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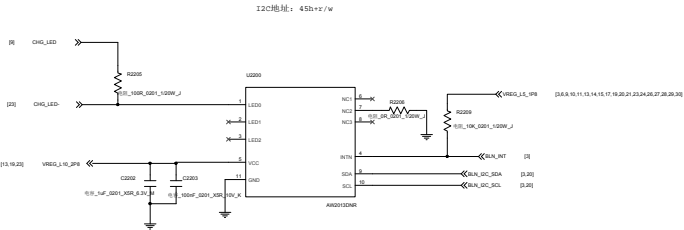
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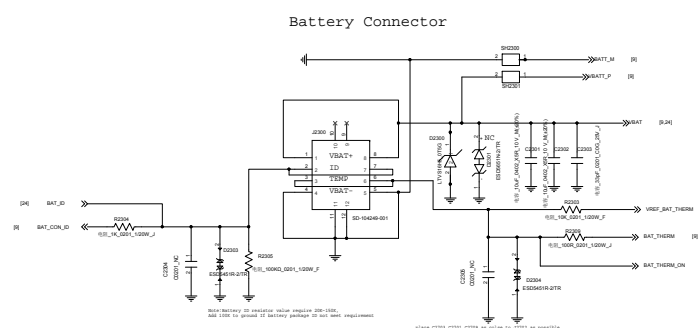


IR_LED



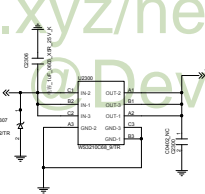
呼吸灯



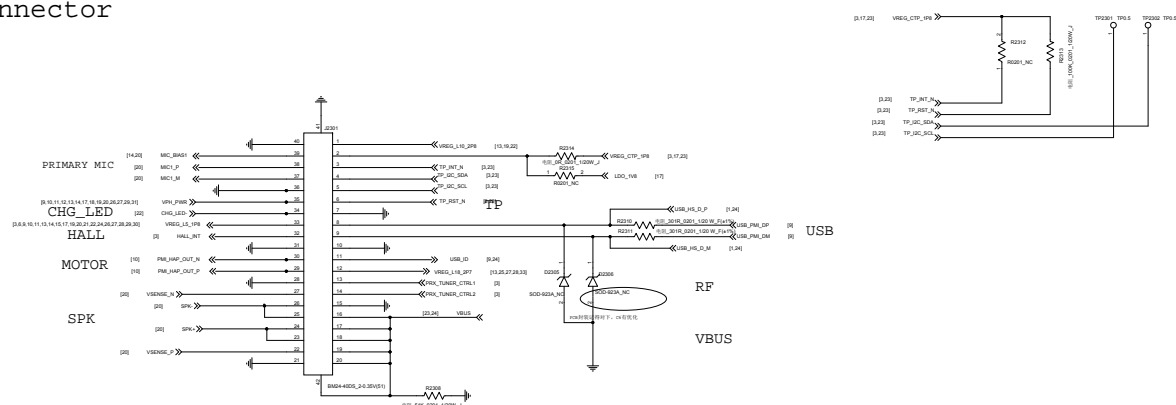


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OVP 6.8V AW33205和WS3220C6-9/TR



BTB Connector



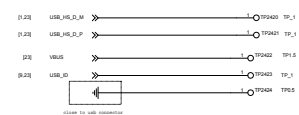
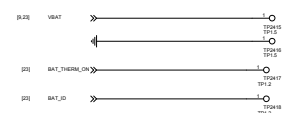
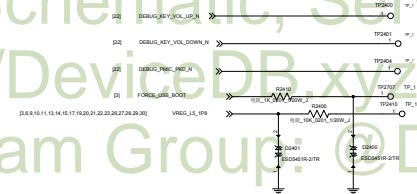
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3	BTB_M3
4	BTB_M4
5	BTB_M5
6	BTB_M6
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8	BTB_M8
9	BTB_M9
10	BTB_M10
11	BTB_M11
12	BTB_M12
13	BTB_M13
14	BTB_M14
15	BTB_M15
16	BTB_M16
17	BTB_M17
18	BTB_M18
19	BTB_M19
20	BTB_M20

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MARK POINT

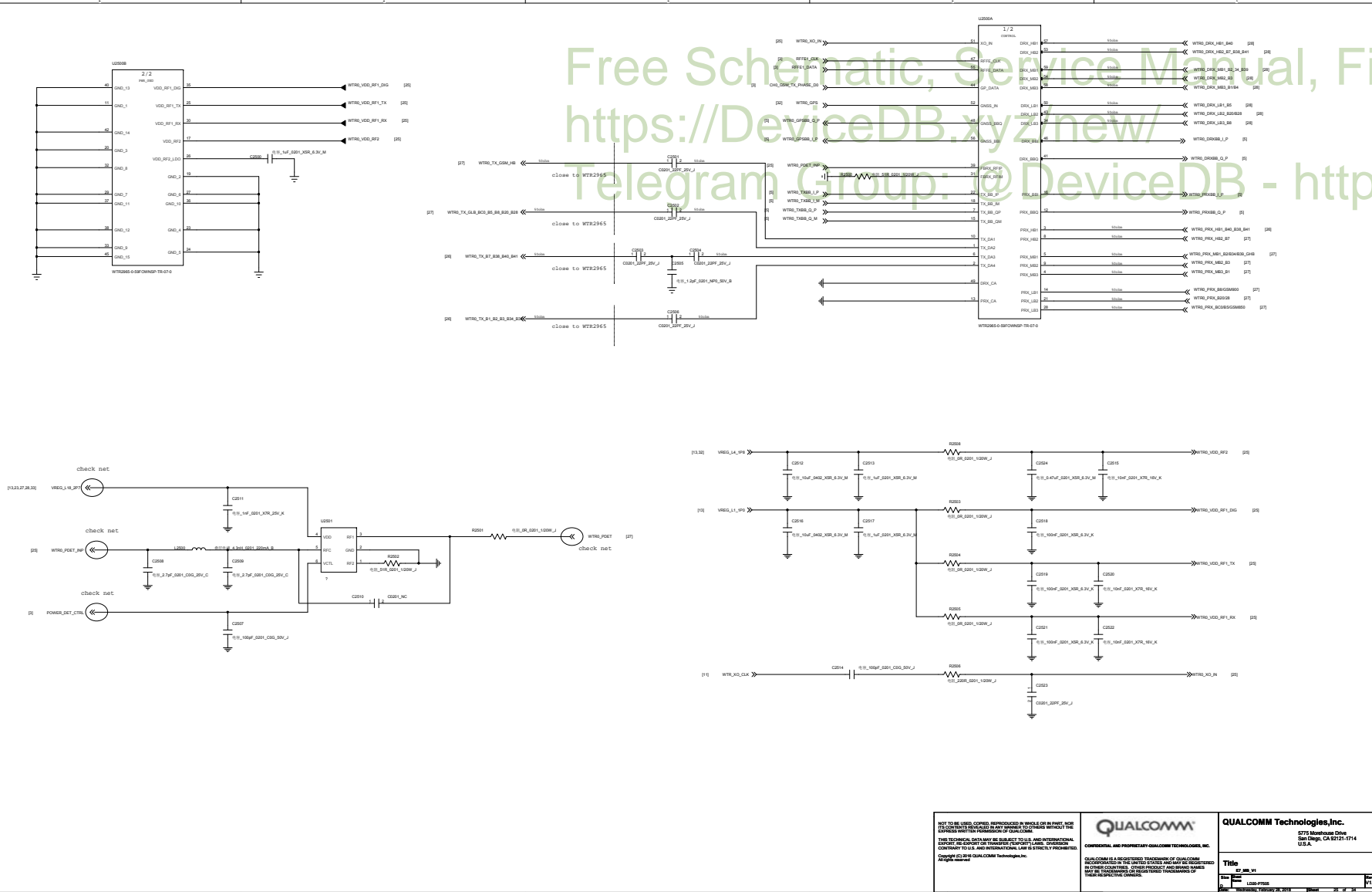
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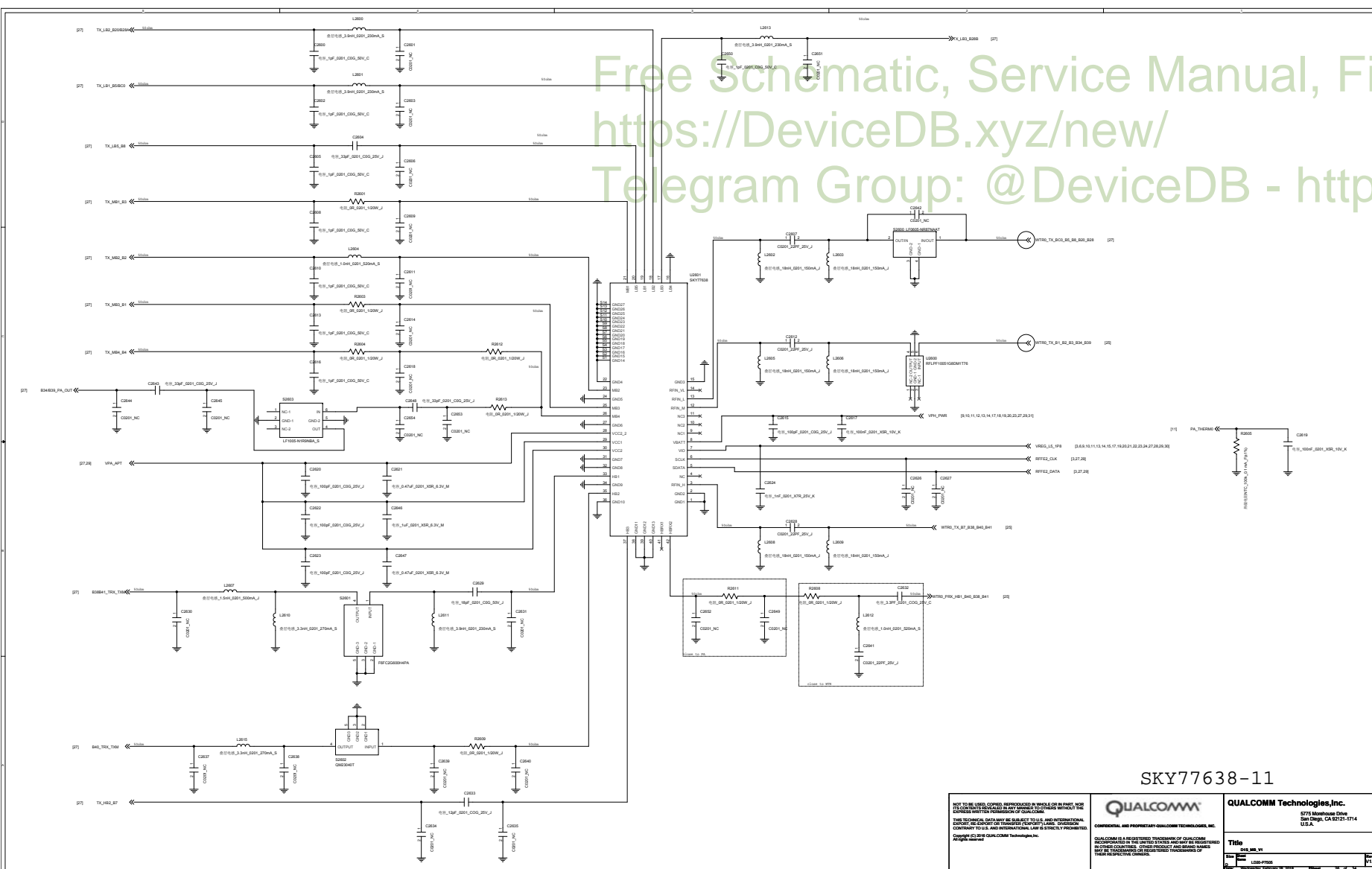


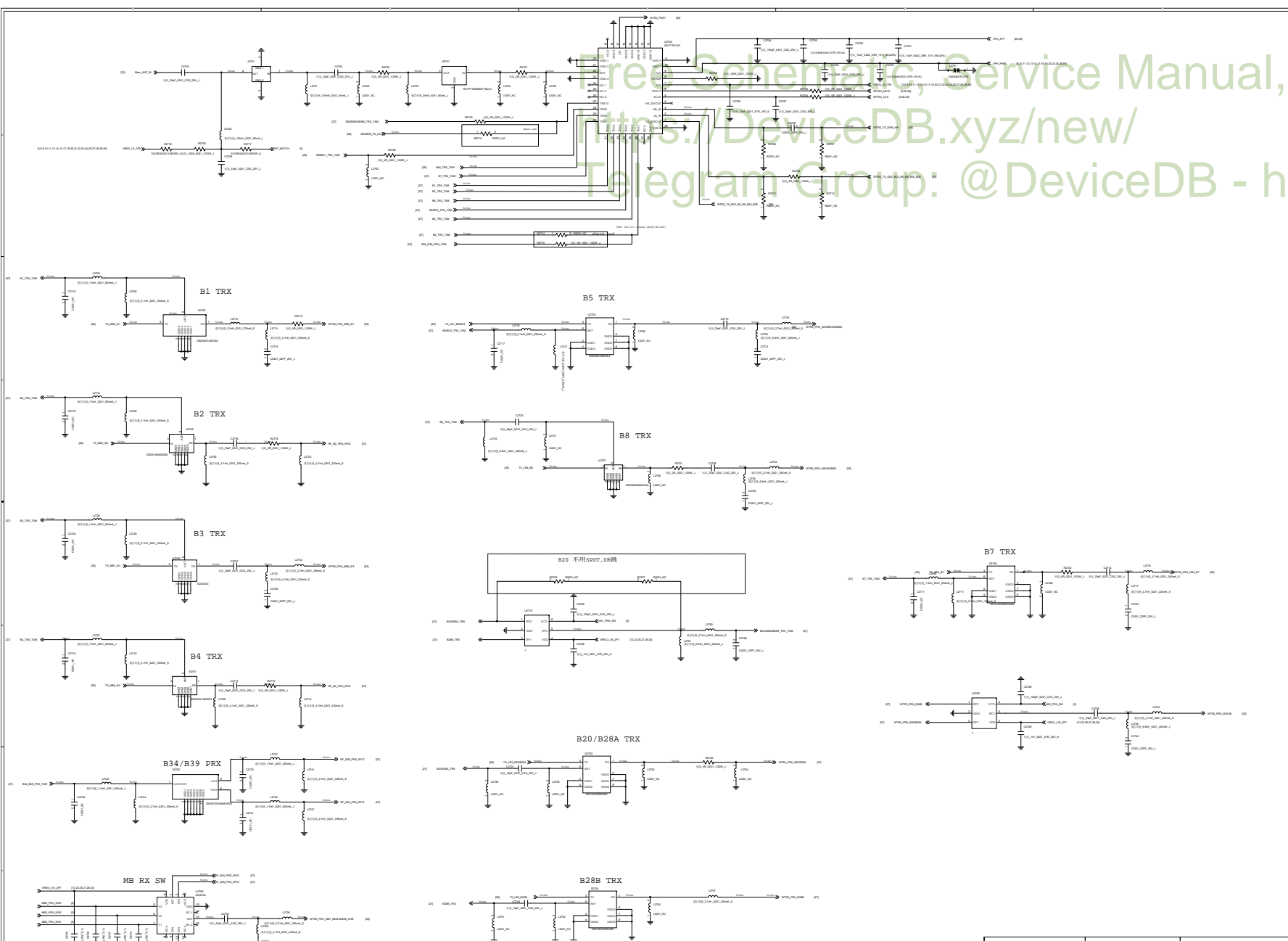
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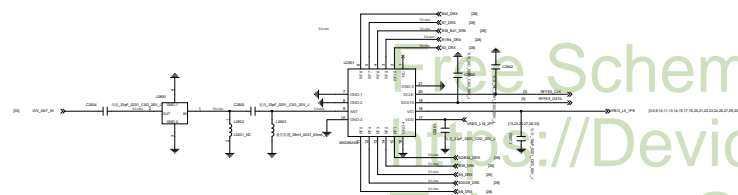
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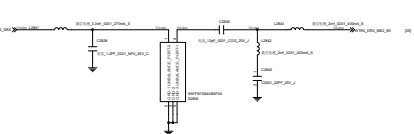


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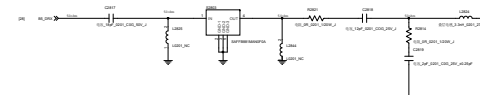
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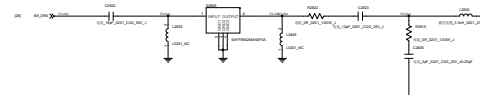
B3 DRX



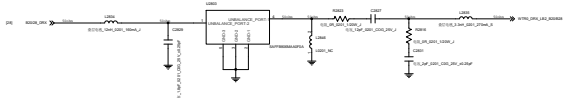
B5 DRX



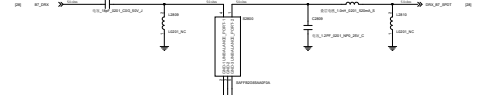
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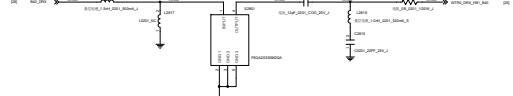
B20/B28 DRX



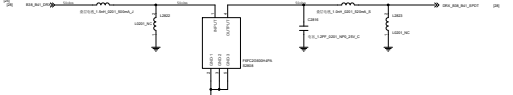
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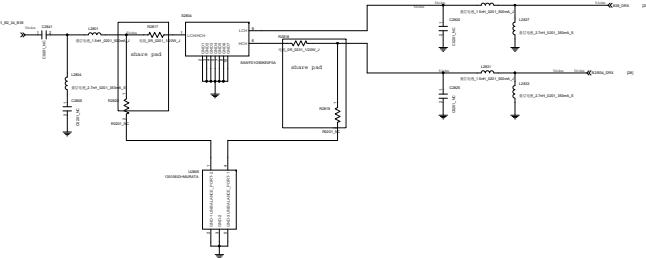
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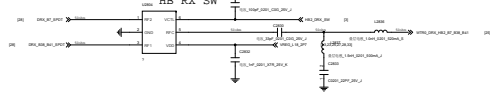
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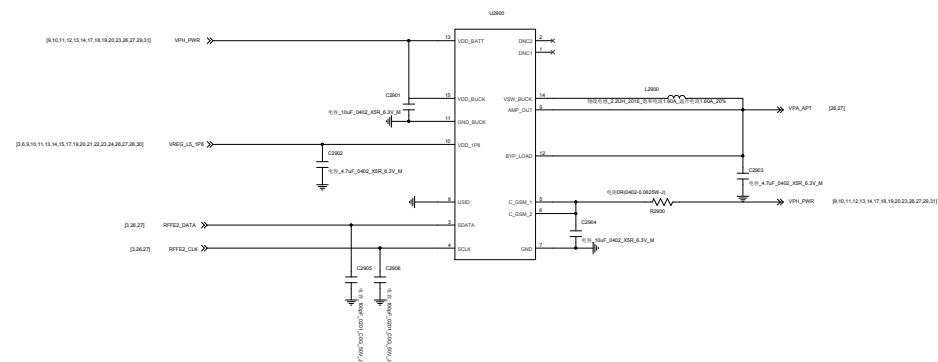
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HB RX SW

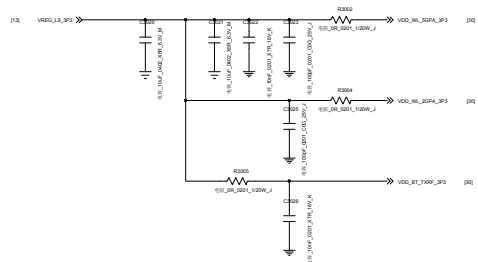
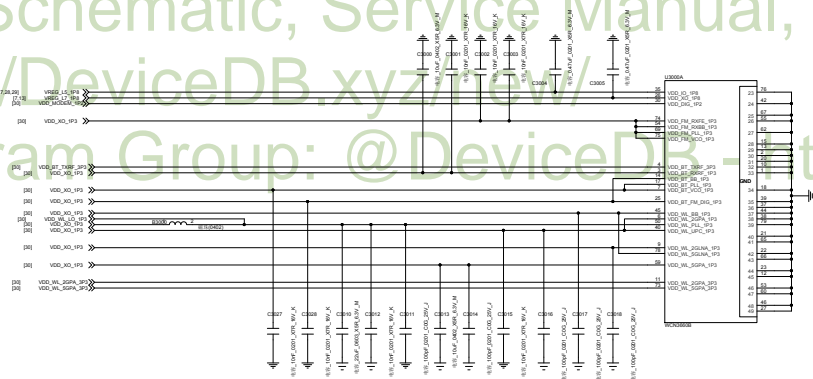
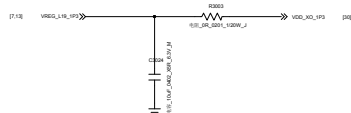


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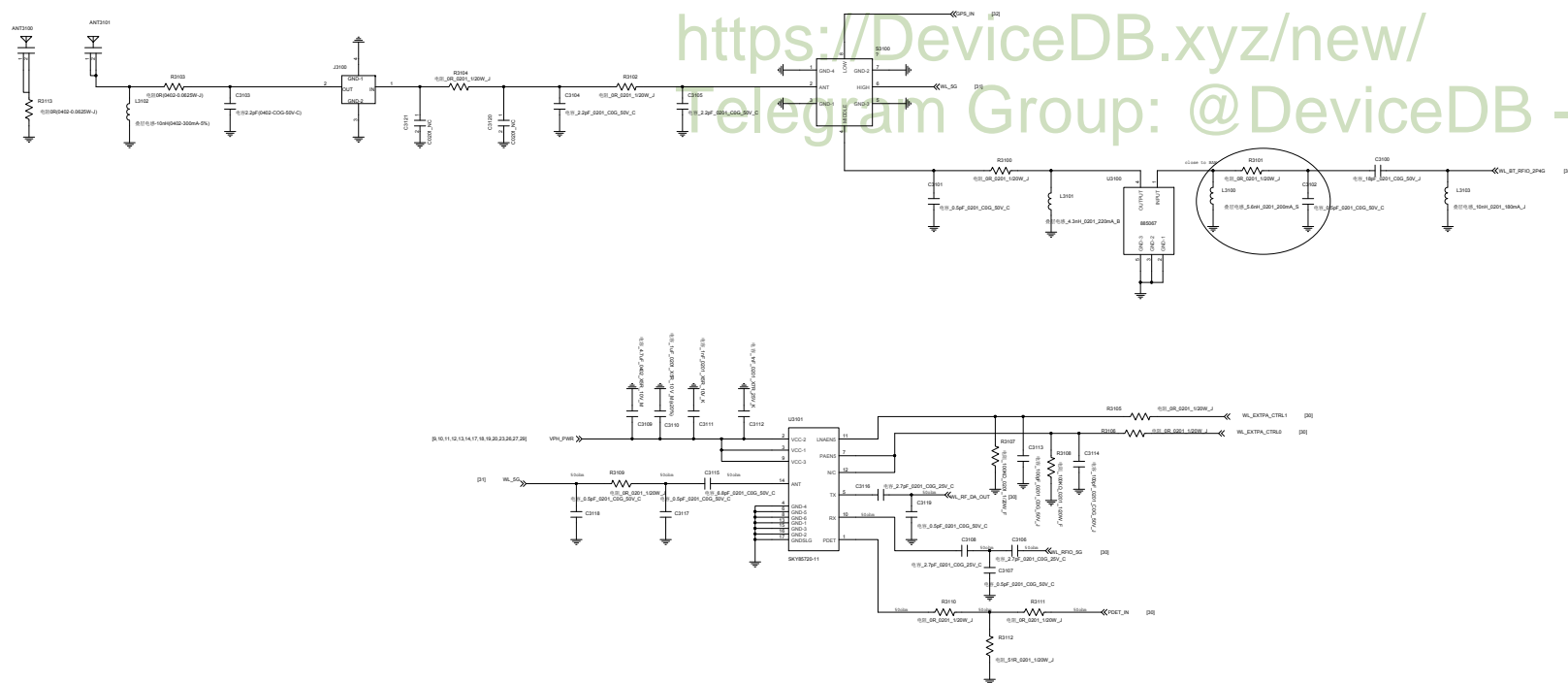
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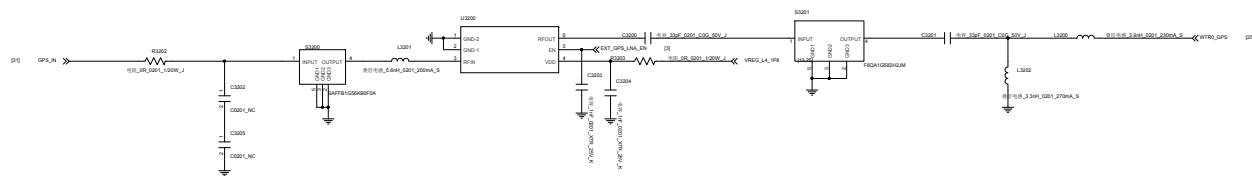
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	DIS_MB_VI
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	extra S3201 option	Non-extra S3201 option
R3200	DNI	0ohm
R3201	DNI	0ohm
C3200	33pF	DNI
C3201	33pF	DNI
S3201	SAFFB1G56KB0F0A	DNI

GPS

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