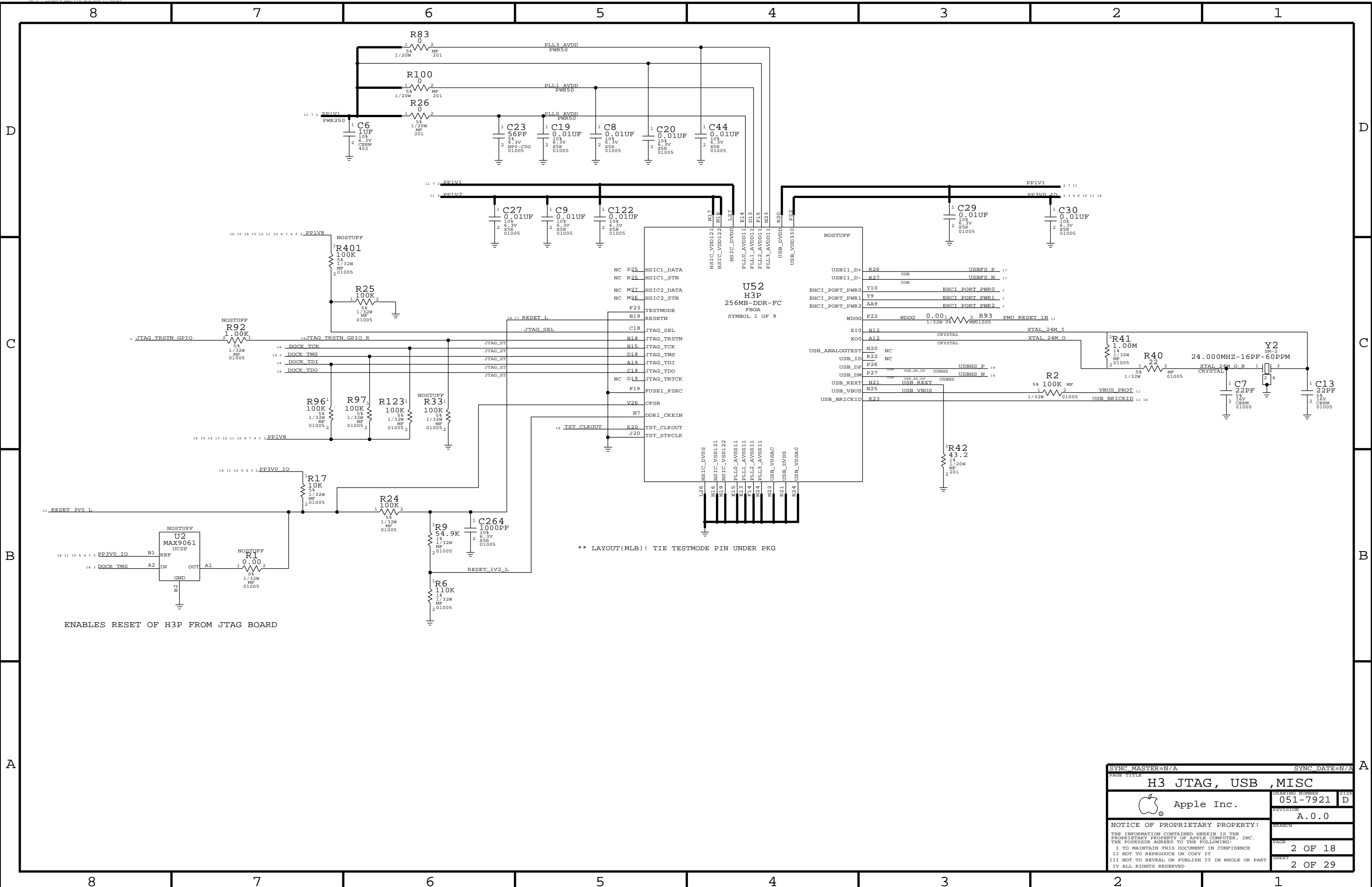

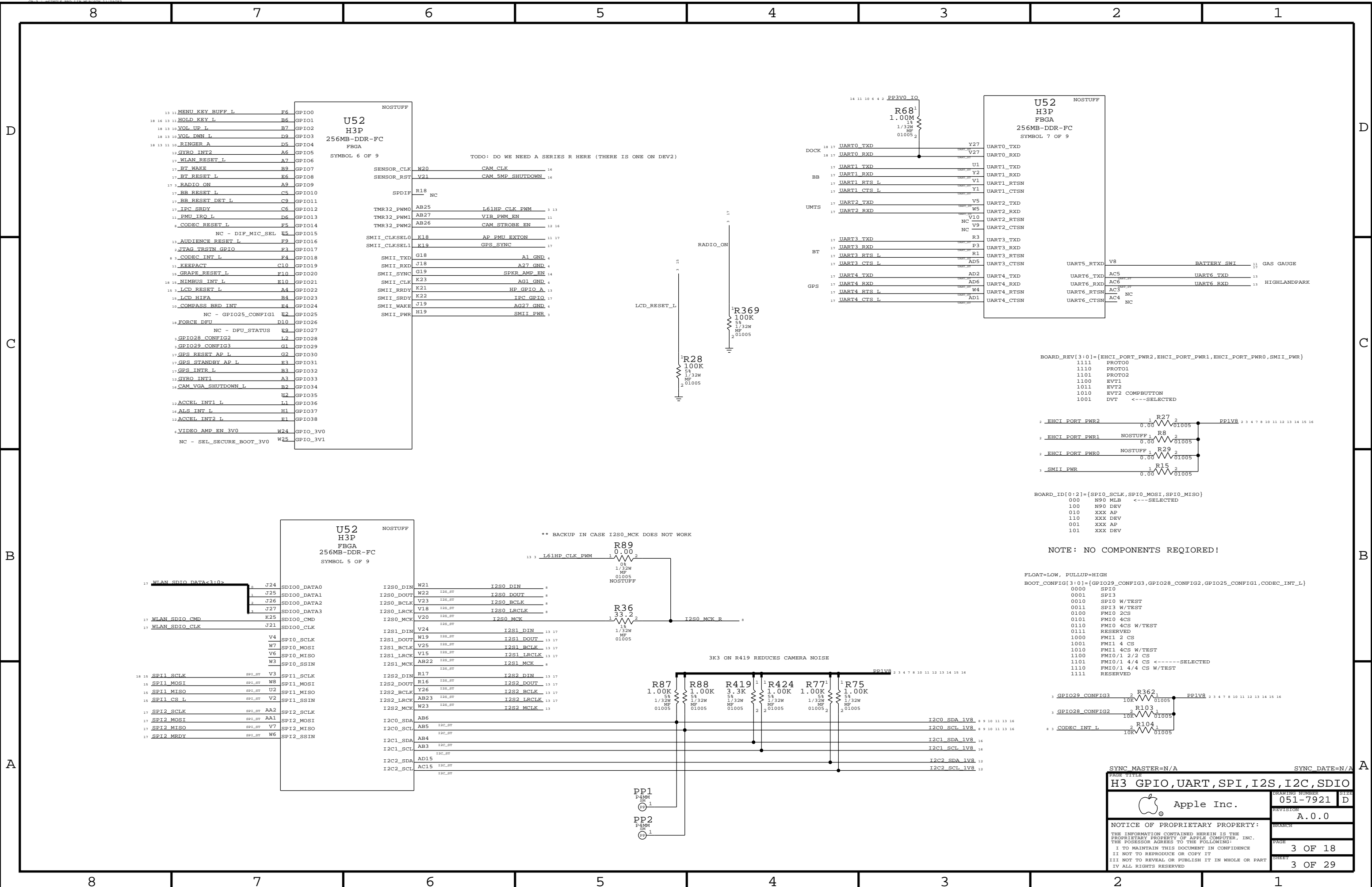


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H3 JTAG, USB		MISC	
	Apple Inc.	DRAWING NUMBER	051-7921
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13 11	MENU KEY BUFF L	F6	GPIO0
18 16 13 11	HOLD KEY L	B6	GPIO1
18 13 10	VOL UP L	B7	GPIO2
18 13 10	VOL DWN L	D9	GPIO3
18 13 11 10	RINGER A	D5	GPIO4
12	GYRO INT2	A6	GPIO5
17	WLAN RESET L	A7	GPIO6
17	BT WAKE	B9	GPIO7
17	BT RESET L	E6	GPIO8
17 9	RADIO ON	A9	GPIO9
17	BB RESET L	C5	GPIO10
17	BB RESET DET L	C9	GPIO11
17	IPC SRDY	C6	GPIO12
17	PMU IRQ L	D6	GPIO13
9	CODEC RESET L	F5	GPIO14
NC	- DIF_MIC_SEL	E5	GPIO15
13	AUDIENCE RESET L	F9	GPIO16
2	UTAG TRSTN GPIO	F3	GPIO17
8 3	CODEC INT L	F4	GPIO18
11	KEEPACT	C10	GPIO19
16	GRAPE RESET L	F10	GPIO20
18 15	NIMBUS INT L	E10	GPIO21
15 3	LCD RESET L	A4	GPIO22
15	LCD HIFA	B4	GPIO23
10	COMPASS BRD INT	E4	GPIO24
NC	- GPIO25_CONFIG1	E2	GPIO25
14	FORCE DFU	D10	GPIO26
NC	- DFU_STATUS	E9	GPIO27
9	GPIO28_CONFIG2	L2	GPIO28
9	GPIO29_CONFIG3	G1	GPIO29
17	GPS RESET AP L	G2	GPIO30
17	GPS STANDBY AP L	E3	GPIO31
17	GPS INTR L	B3	GPIO32
12	GYRO INT1	A3	GPIO33
16	CAM VGA SHUTDOWN L	B2	GPIO34
12	ACCEL INT1 L	H2	GPIO35
14	ALS INT L	L1	GPIO36
14	ALS INT2 L	H1	GPIO37
12	ACCEL INT2 L	E1	GPIO38
4	VIDEO AMP EN 3V0	W24	GPIO_3V0
NC	- SEL_SECURE_BOOT_3V0	W25	GPIO_3V1

U52 H3P 256MB-DDR-FC FBGA SYMBOL 6 OF 9		TODO: DO WE NEED A SERIES R HERE (THERE IS ONE ON DEV2)	
SENSOR_CLK	W20	CAM_CLK	16
SENSOR_RST	V21	CAM 5MP_SHUTDOWN	16
SPDIF	R18	NC	
TMR32_PWM0	AB25	L61HP_CLK_PWM	3 13
TMR32_PWM1	AB27	VIB_PWM_EN	11
TMR32_PWM2	AB26	CAM_STROBE_EN	12 16
SMII_CLKSEL0	K18	AP_PMU_EXTON	11 17
SMII_CLKSEL1	K19	GPS_SYNC	17
SMII_TXD	G18	A1_GND	4
SMII_RXD	J18	A27_GND	4
SMII_SYNC	G19	SPKR_AMP_EN	14
SMII_CLK	K21	AG1_GND	4
SMII_SRDY	K22	HP_GPIO_A	13
SMII_SRDY	J19	IPC_GPIO	17
SMII_WAKE	J19	AG27_GND	4
SMII_PWR	H19	SMII_PWR	3

U52 H3P 256MB-DDR-FC FBGA SYMBOL 7 OF 9		NOSTUFF	
UART0_TXD	Y27	UART0_TXD	
UART0_RXD	V27	UART0_RXD	
UART1_TXD	U1	UART1_TXD	
UART1_RXD	Y2	UART1_RXD	
UART1_RTSN	V1	UART1_RTSN	
UART1_CTSN	Y1	UART1_CTSN	
UART2_TXD	V5	UART2_TXD	
UART2_RXD	W5	UART2_RXD	
UART2_RTSN	V10	UART2_RTSN	
UART2_CTSN	V9	UART2_CTSN	
UART3_TXD	R3	UART3_TXD	
UART3_RXD	P3	UART3_RXD	
UART3_RTSN	R1	UART3_RTSN	
UART3_CTSN	AD5	UART3_CTSN	
UART4_TXD	AD2	UART4_TXD	
UART4_RXD	AD6	UART4_RXD	
UART4_RTSN	W4	UART4_RTSN	
UART4_CTSN	AD1	UART4_CTSN	
UART5_RTXD	V8	BATTERY_SWI	11
UART6_TXD	AC5	UART6_TXD	13
UART6_RXD	AC6	UART6_RXD	13
UART6_RTSN	AC3	NC	
UART6_CTSN	AC4	NC	

BOARD\_REV[3:0]={EHCI\_PORT\_PWR2,EHCI\_PORT\_PWR1,EHCI\_PORT\_PWR0,SMII\_PWR}  
1111 PROTO0  
1110 PROTO1  
1101 PROTO2  
1100 EVT1  
1011 EVT2  
1010 EVT2 COMBUTON  
1001 DVT <---SELECTED

2\_EHCI\_PORT\_PWR2 0.00 R27 01005 PP1V8 2 3 4 7 8 10 11 12 13 14 15 16  
2\_EHCI\_PORT\_PWR1 NOSTUFF 1 R8 01005  
2\_EHCI\_PORT\_PWR0 NOSTUFF 1 R29 01005  
3\_SMII\_PWR 0.00 R15 01005

BOARD\_ID[0:2]={SPI0\_SCLK,SPI0\_MOSI,SPI0\_MISO}  
000 N90 MLB <---SELECTED  
100 N90 DEV  
010 XXX AP  
110 XXX DEV  
001 XXX AP  
101 XXX DEV

NOTE: NO COMPONENTS REQIOERD!

FLOAT=LOW, PULLUP=HIGH  
BOOT\_CONFIG[3:0]={GPIO29\_CONFIG3,GPIO28\_CONFIG2,GPIO25\_CONFIG1,CODEC\_INT\_L}  
0000 SPI0  
0001 SPI3  
0010 SPI0 W/TEST  
0011 SPI3 W/TEST  
0100 FMIO 2CS  
0101 FMIO 4CS  
0110 FMIO 4CS W/TEST  
0111 RESERVED  
1000 FMII 2 CS  
1001 FMII 4 CS  
1010 FMII 4CS W/TEST  
1100 FMIO/1 2/2 CS  
1101 FMIO/1 4/4 CS <---SELECTED  
1110 FMIO/1 4/4 CS W/TEST  
1111 RESERVED

3\_GPIO29\_CONFIG3 10R R362 01005 PP1V8 2 3 4 7 8 10 11 12 13 14 15 16  
3\_GPIO28\_CONFIG2 10R R103 01005  
8\_CODEC\_INT\_L 10R R104 01005

SYNC MASTER=N/A

SYNC DATE=N/A

H3 GPIO, UART, SPI, I2S, I2C, SDIO

051-7921

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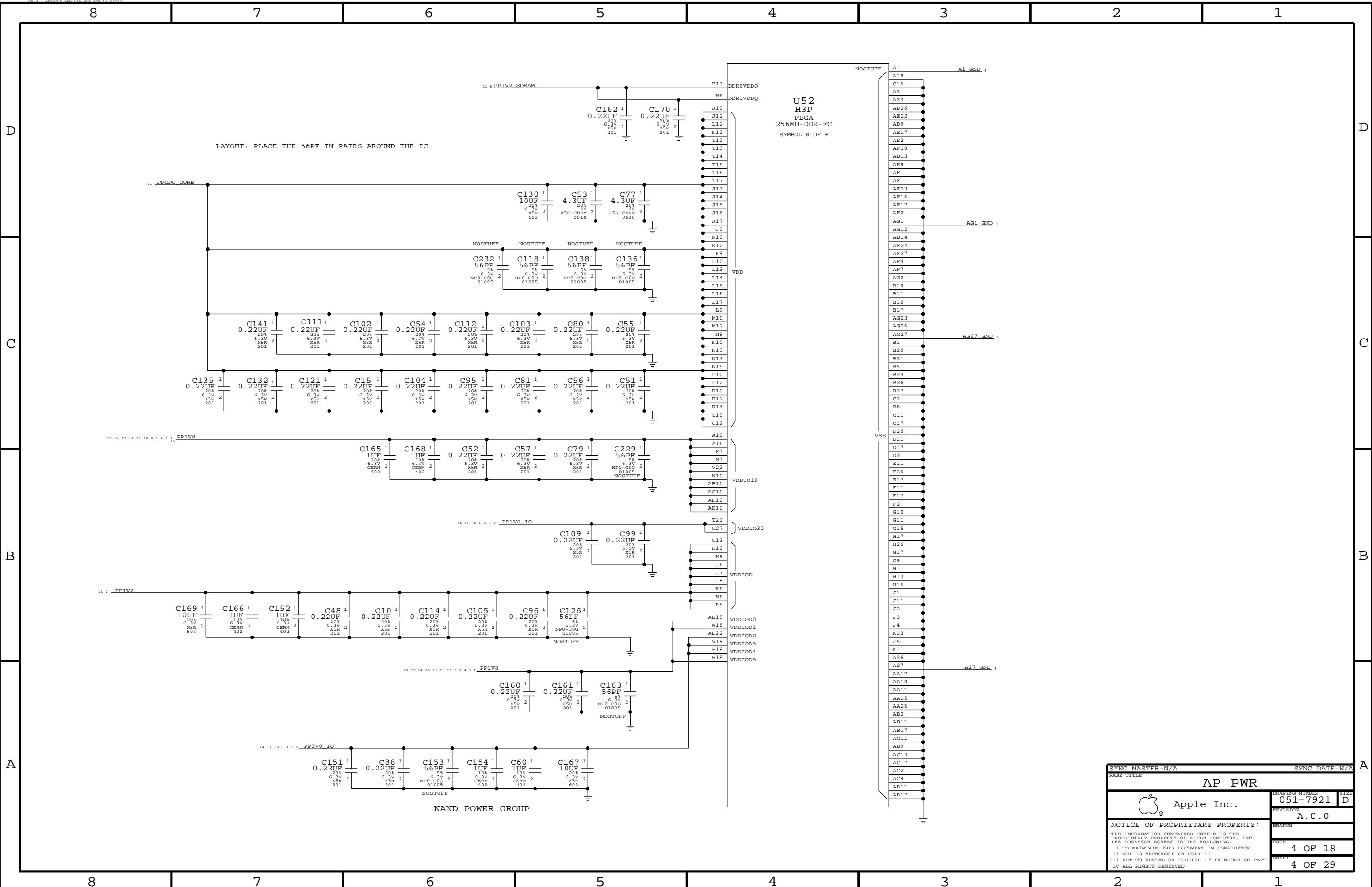
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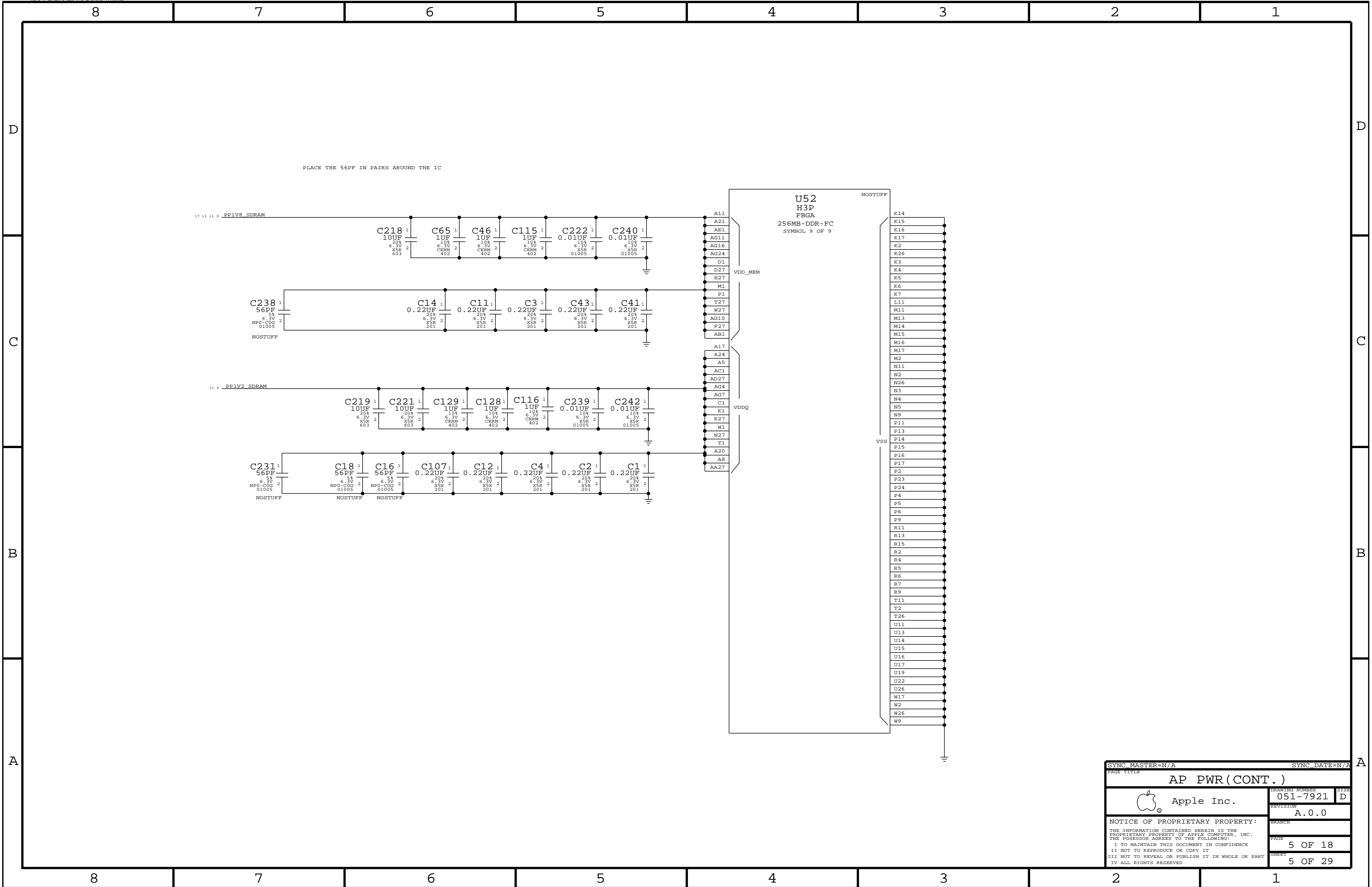
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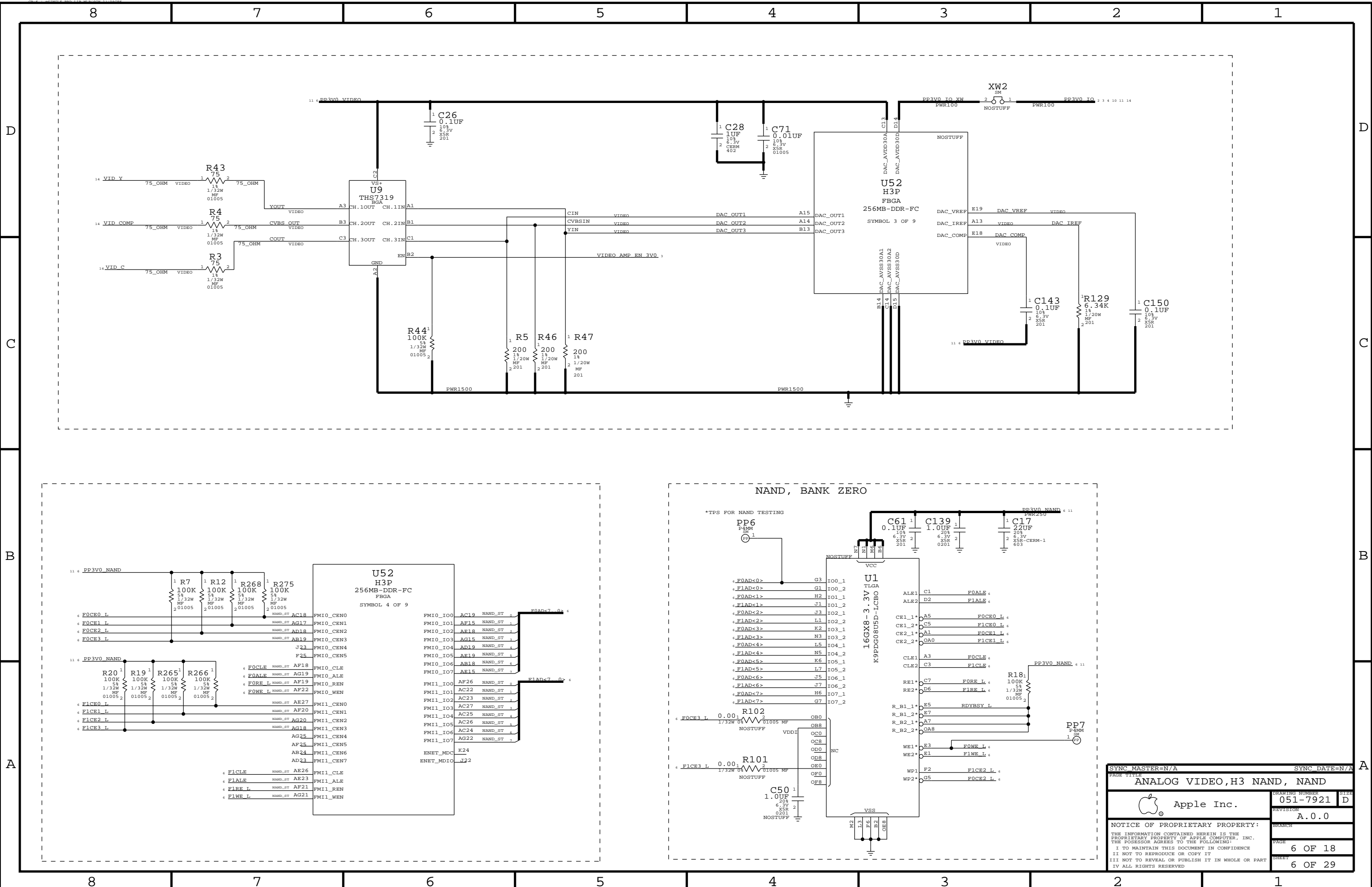
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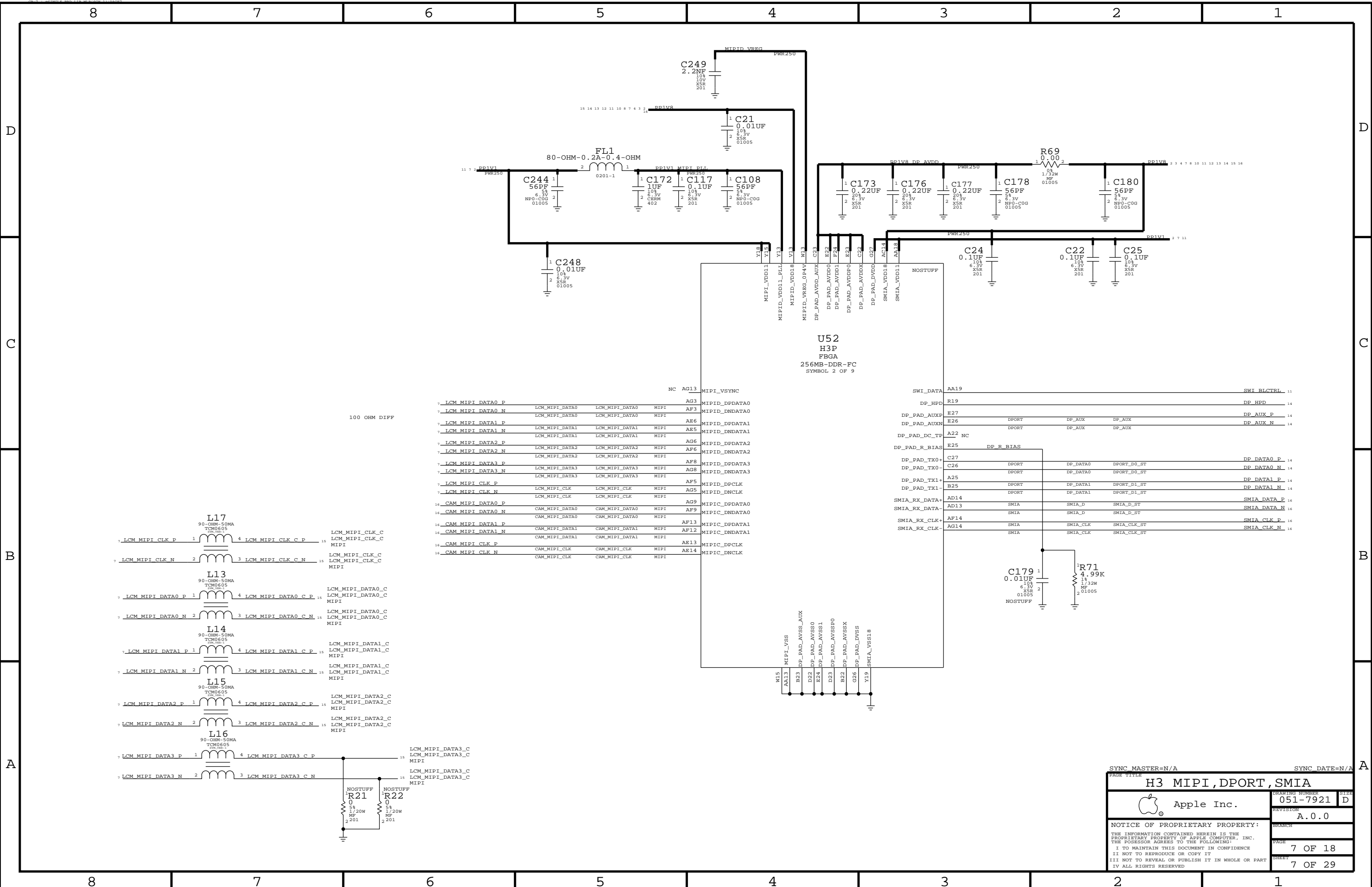


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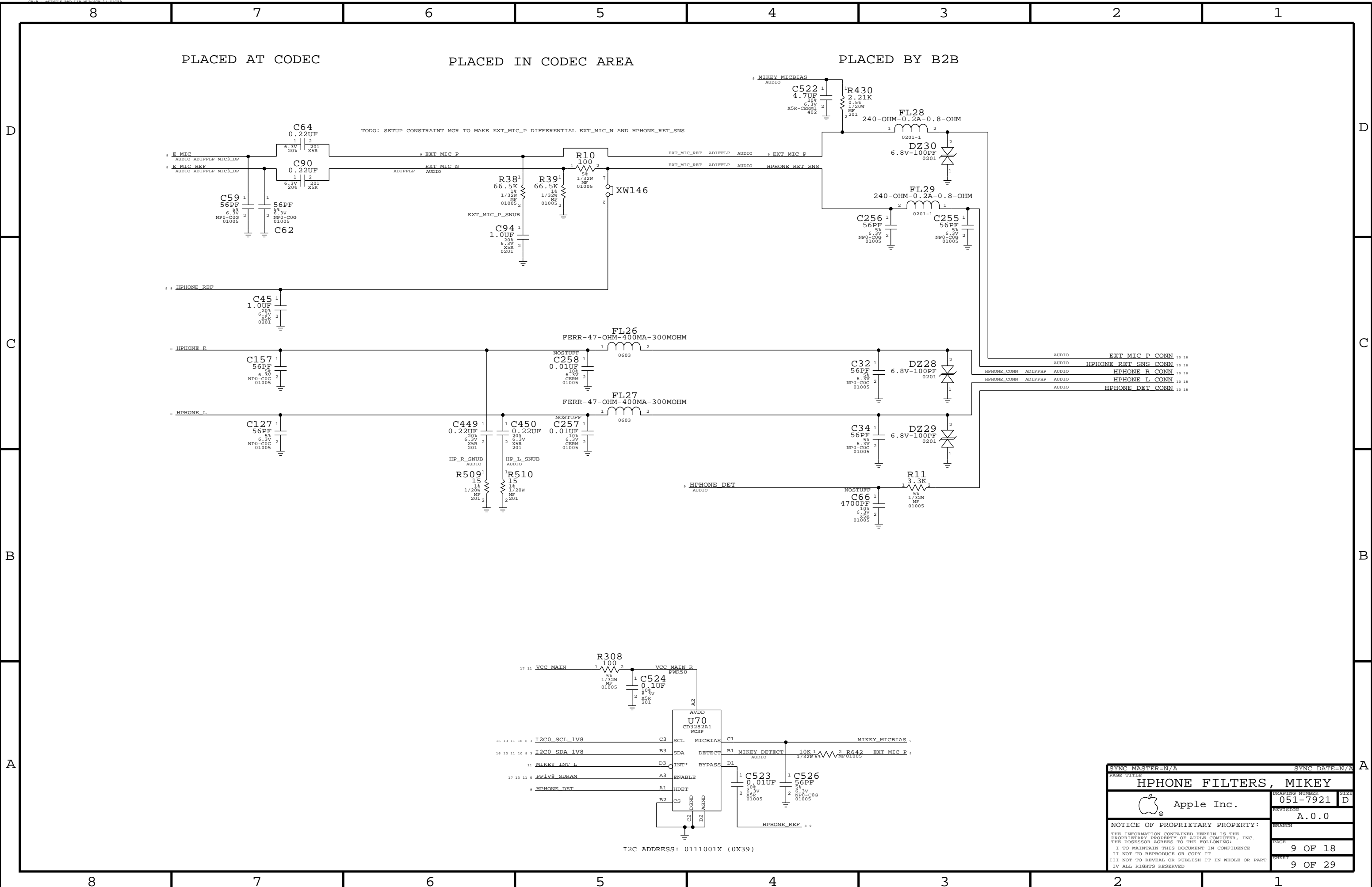
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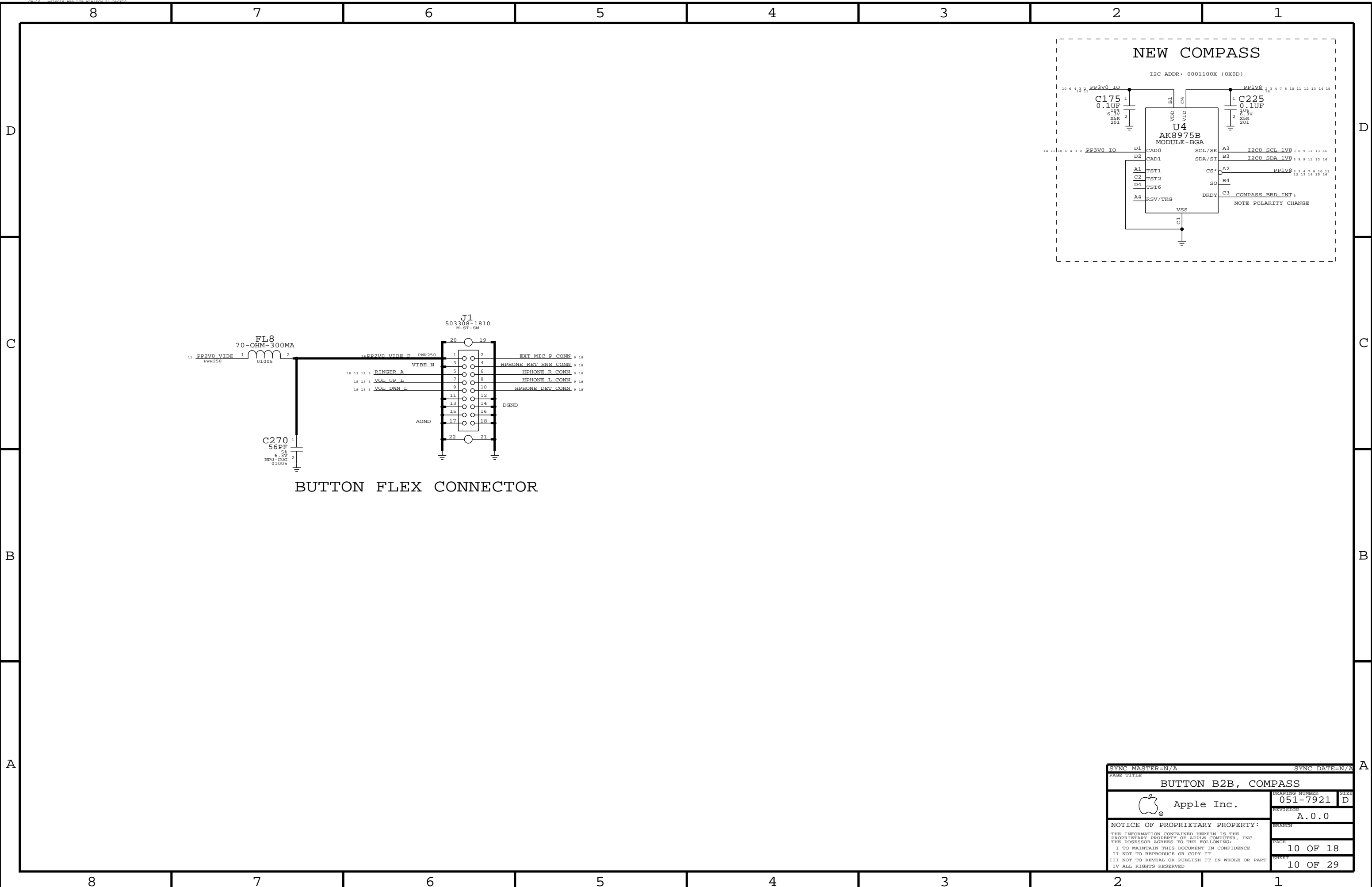
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## D

DCBA



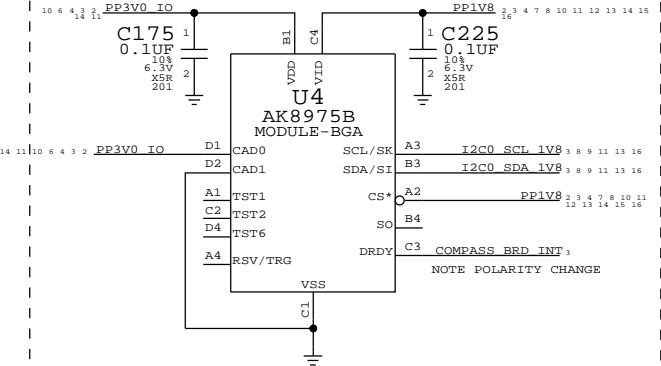
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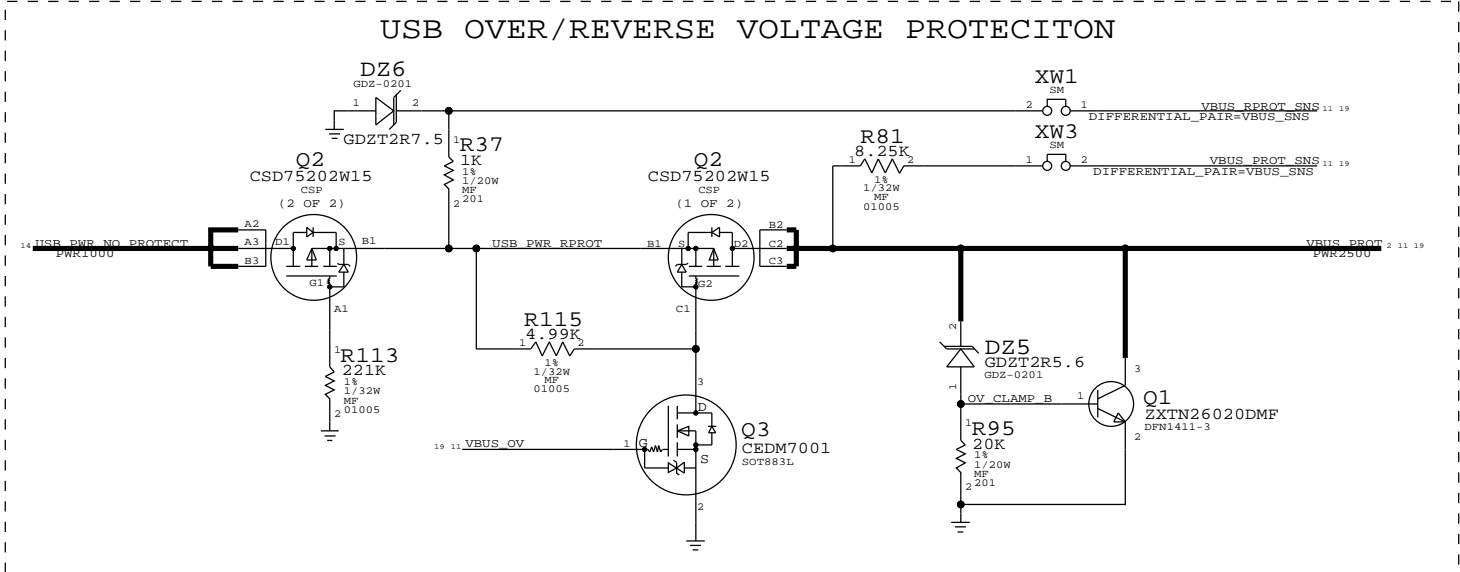
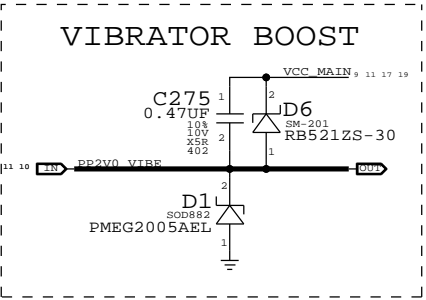
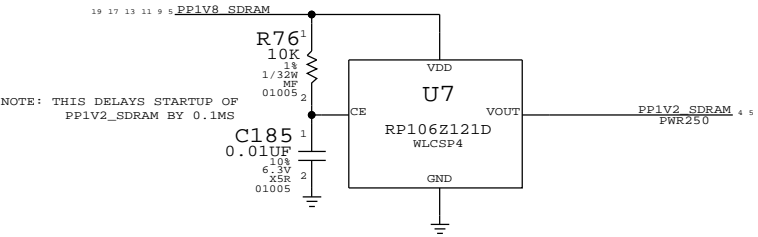
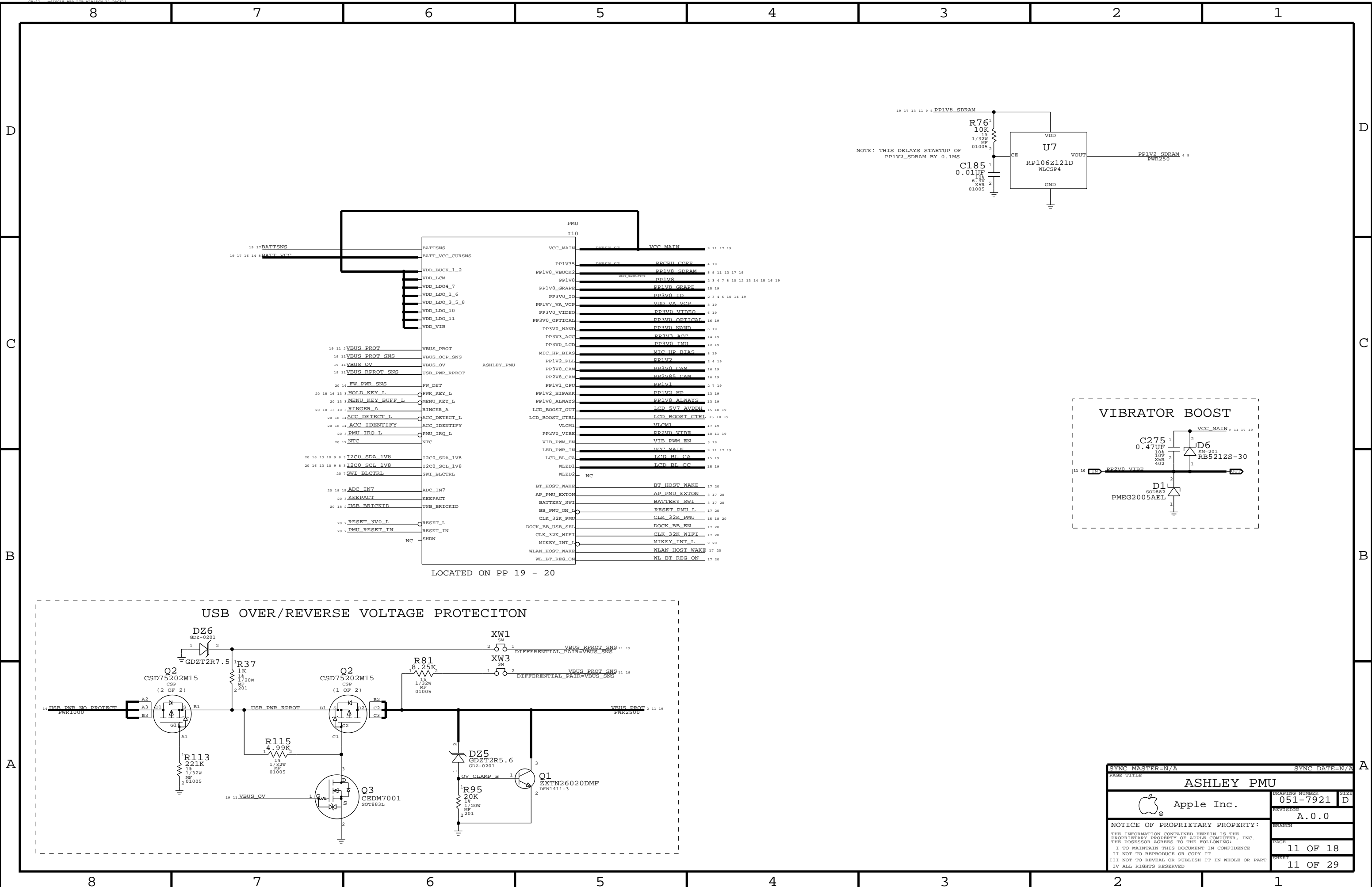
BUTTON FLEX CONNECTOR


NEW COMPASS

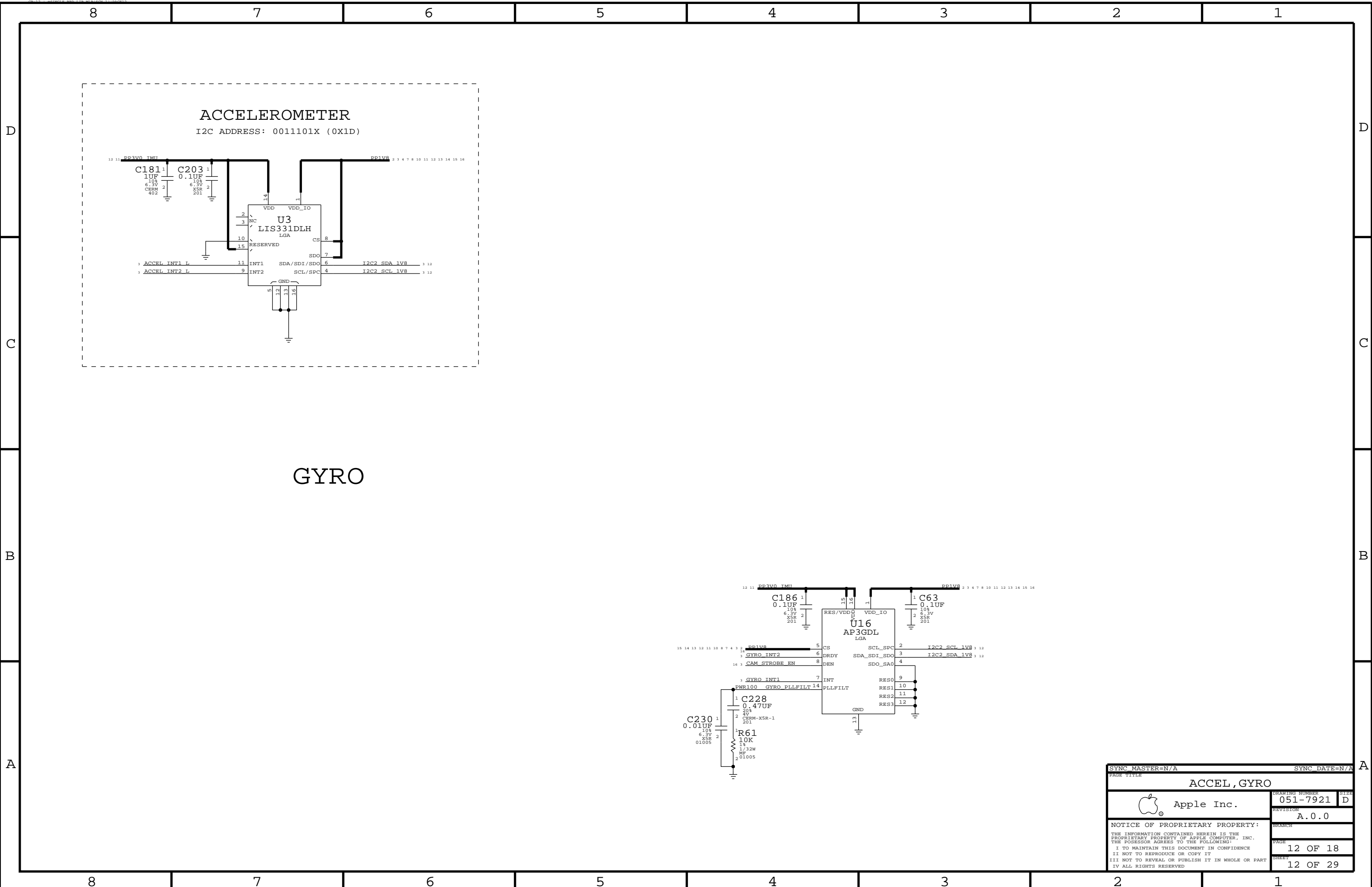
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BUTTON B2B, COMPASS			
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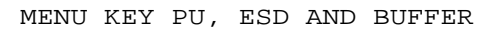
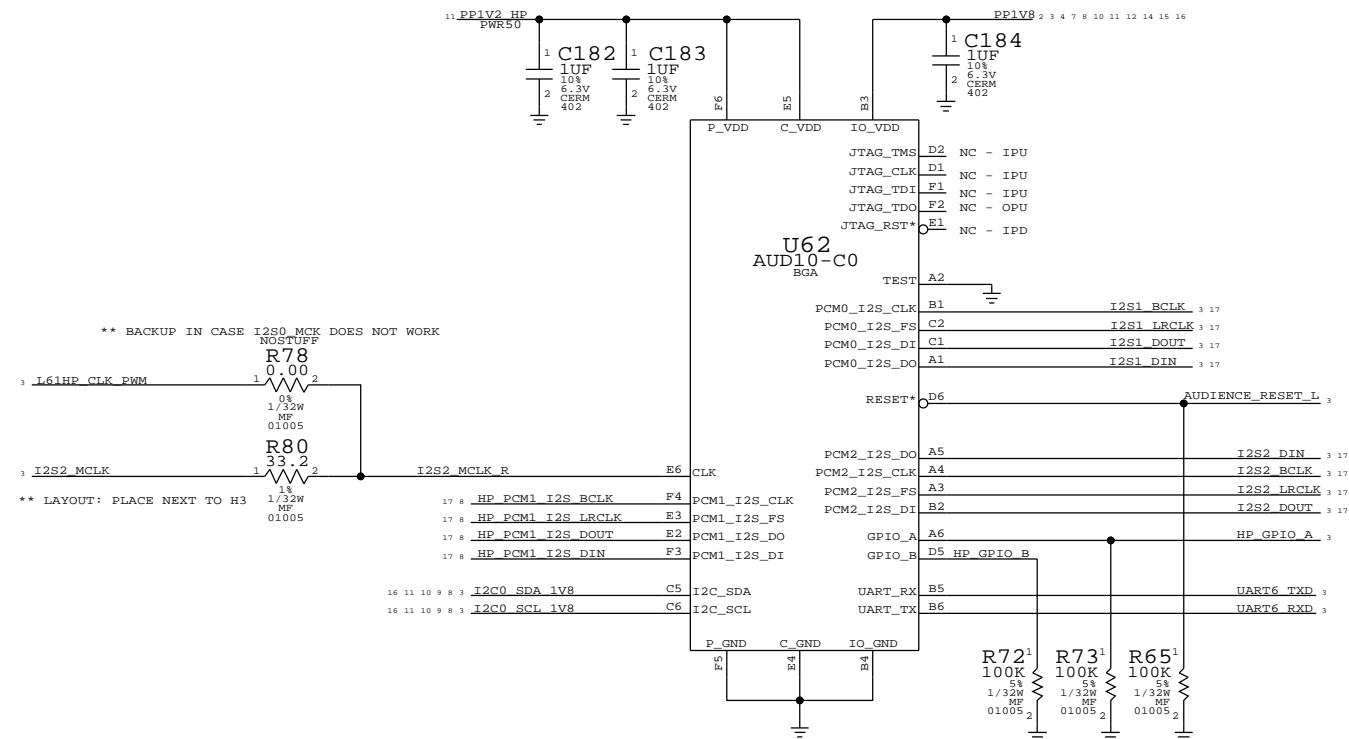
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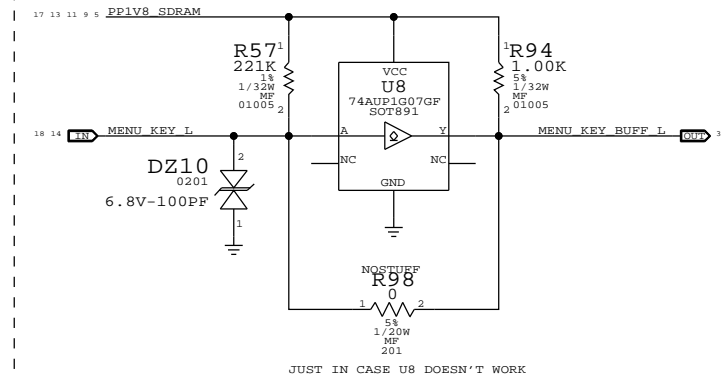
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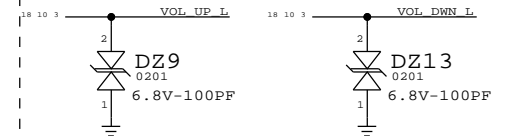


BUFFER IS NEEDED BECAUSE OF IT'S LOW V\_IH AND LEAKAGE  
IT MUST BE OPEN DRAIN SO THAT WHEN PP1V8\_SDRAM IS DOWN,  
MENU\_KEY\_BUFF\_L IS PULLED LOW THROUGH R94 AND INTERNAL  
PULLDOWN IN ASHLEY.

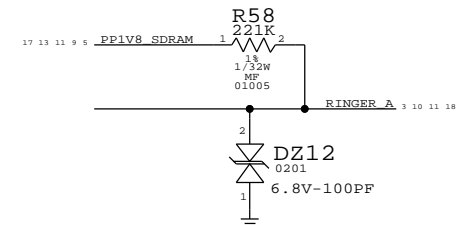


JUST IN CASE U8 DOESN'T WORK

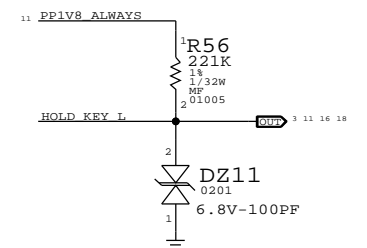
## VOLUME SWITCH ESD



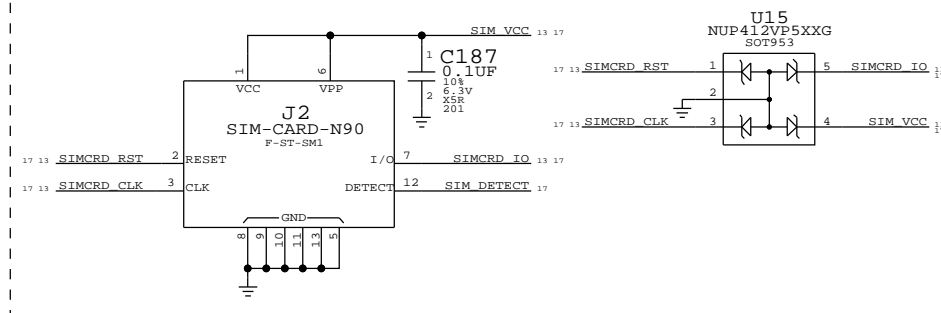
## RINGER A/B SWITCH PU AND ESD |




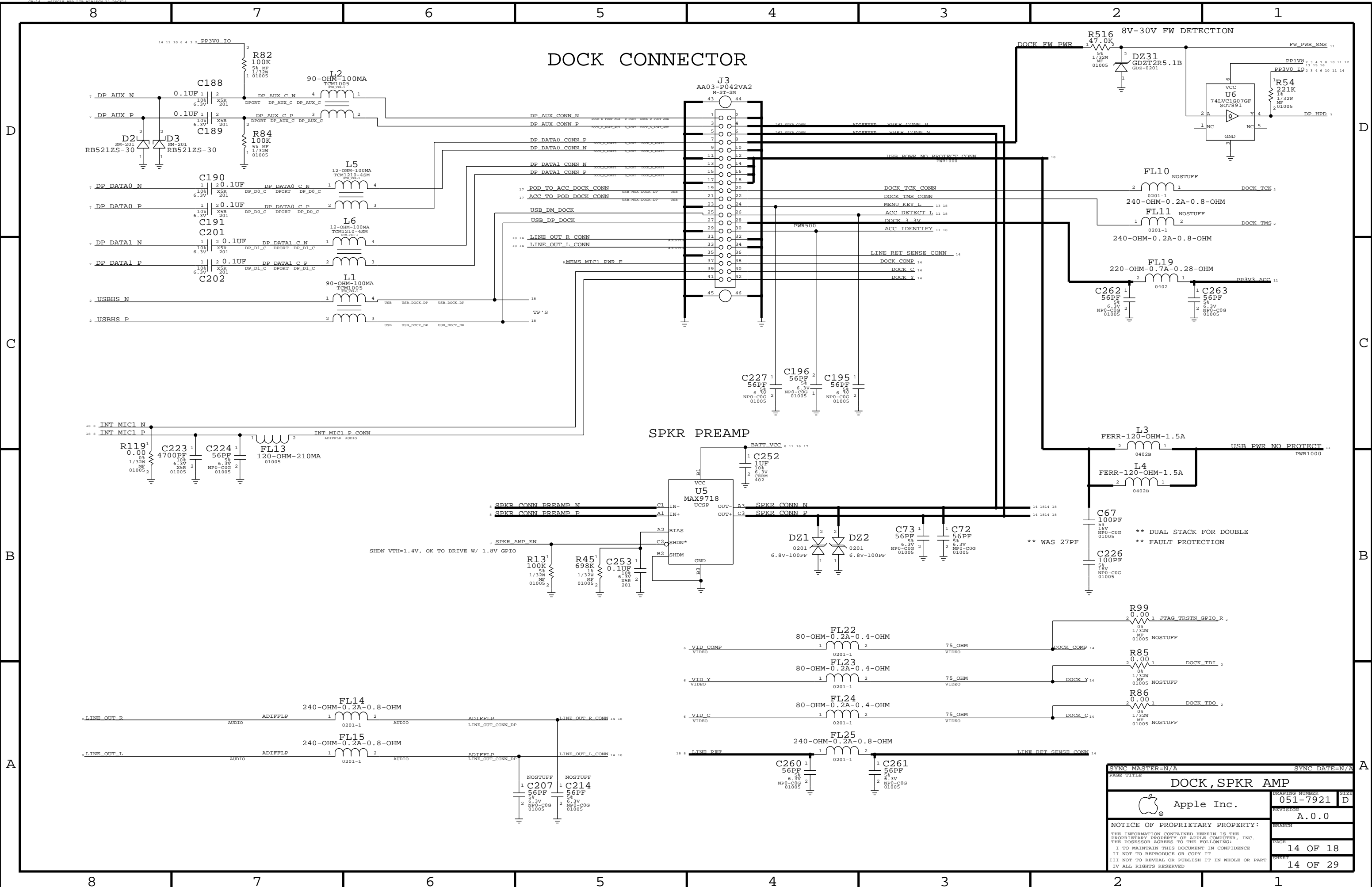
## POWER/HOLD KEY PU AND ESD



## SIM CARD SLOT AND ESD



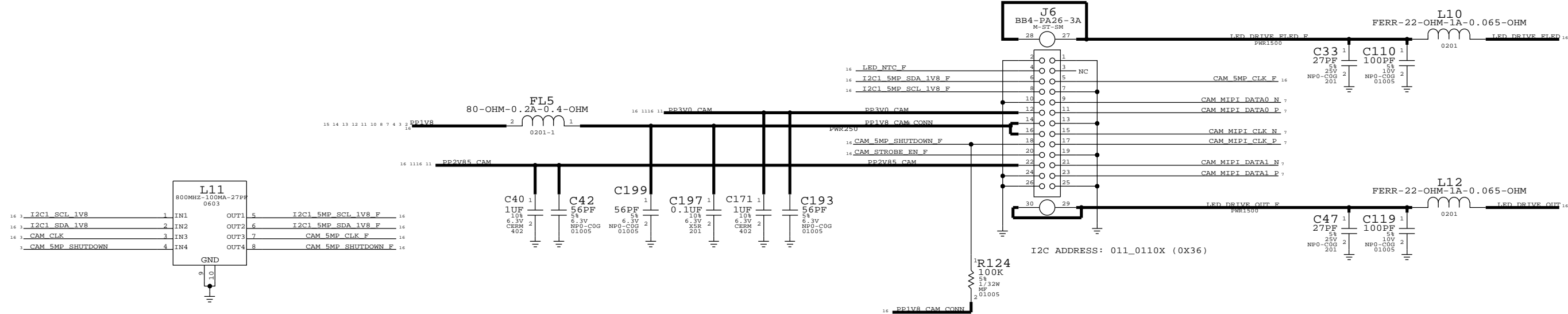
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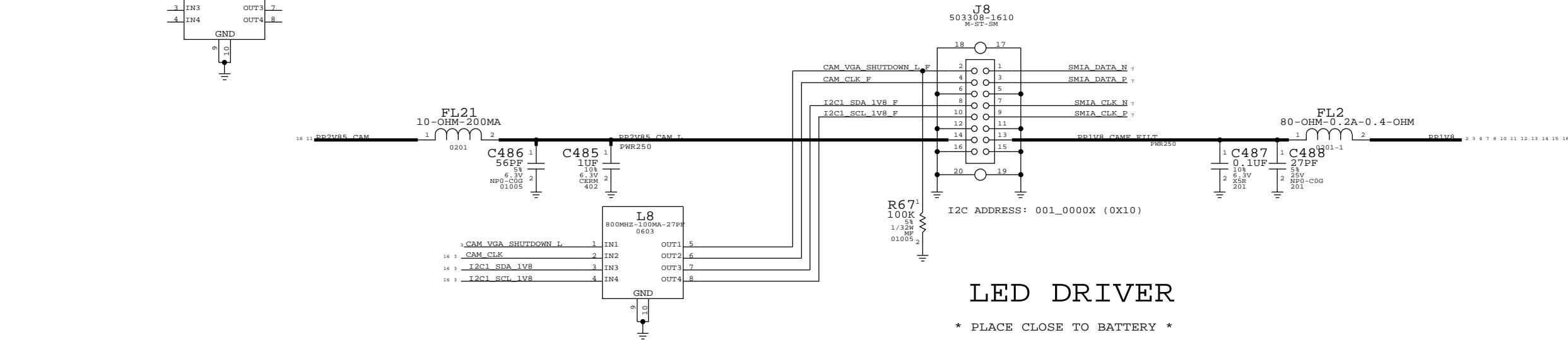
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# 5MP CAMERA CONNECTOR



## VGA CAMERA CONN



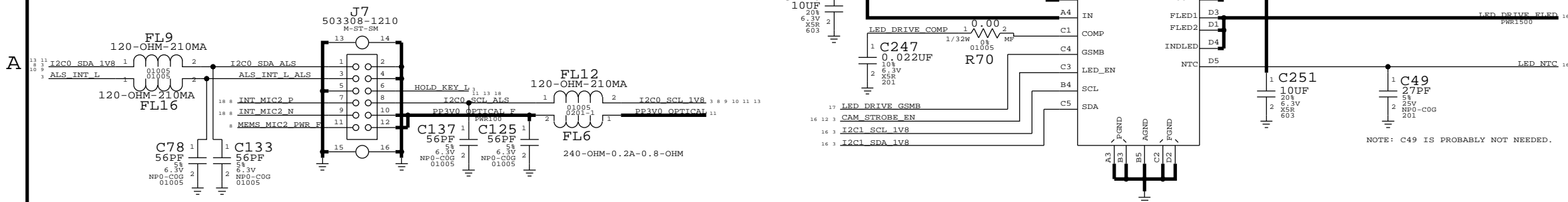
## LED DRIVER


\* PLACE CLOSE TO BATTERY \*

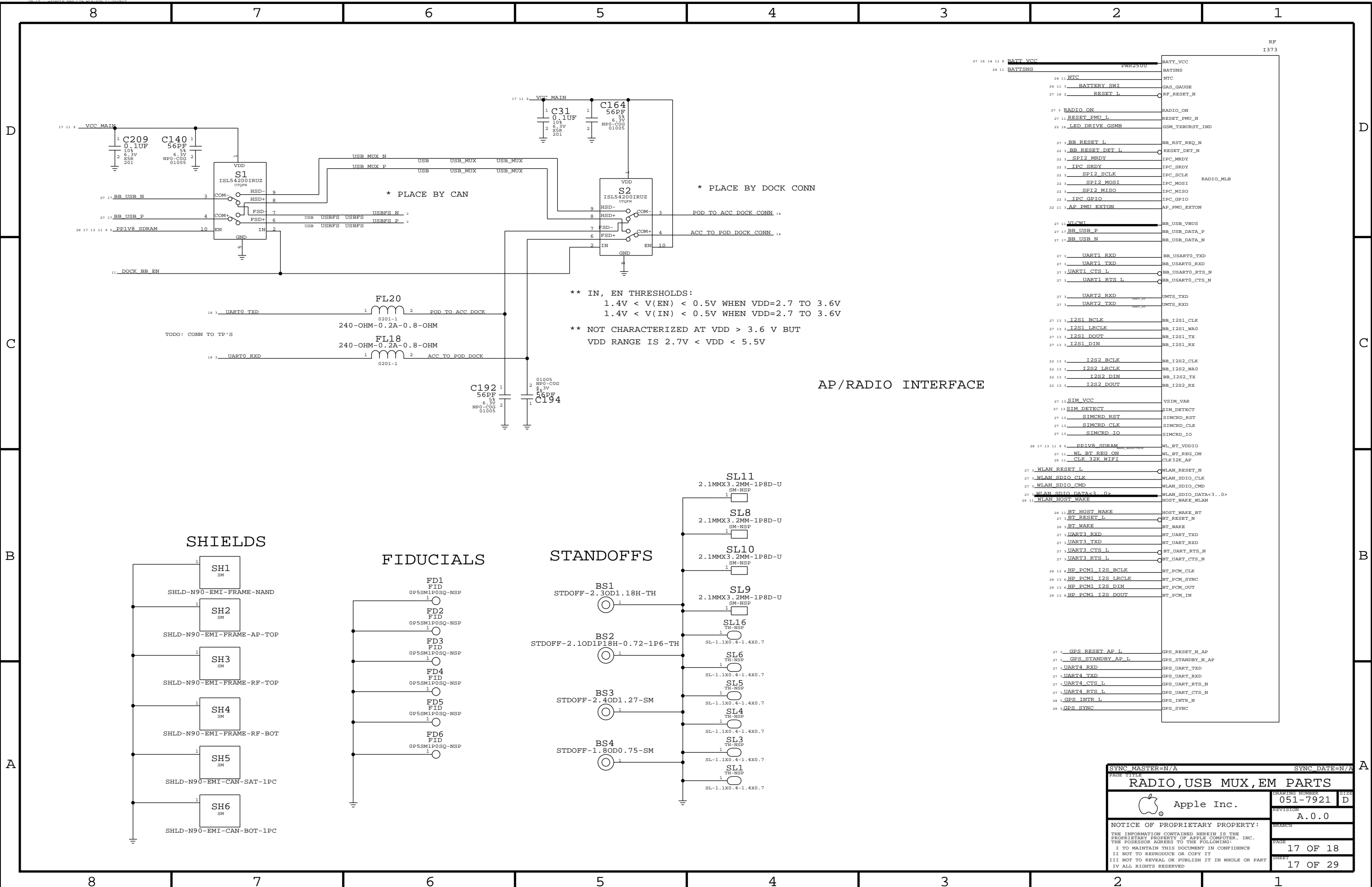
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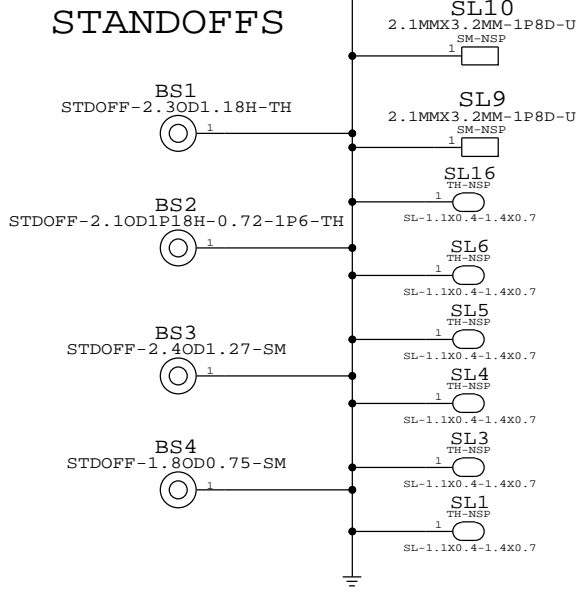
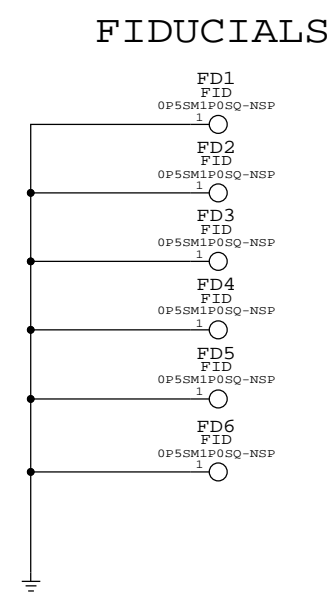
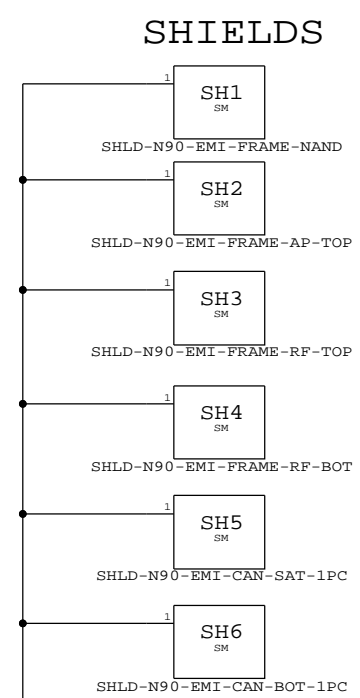
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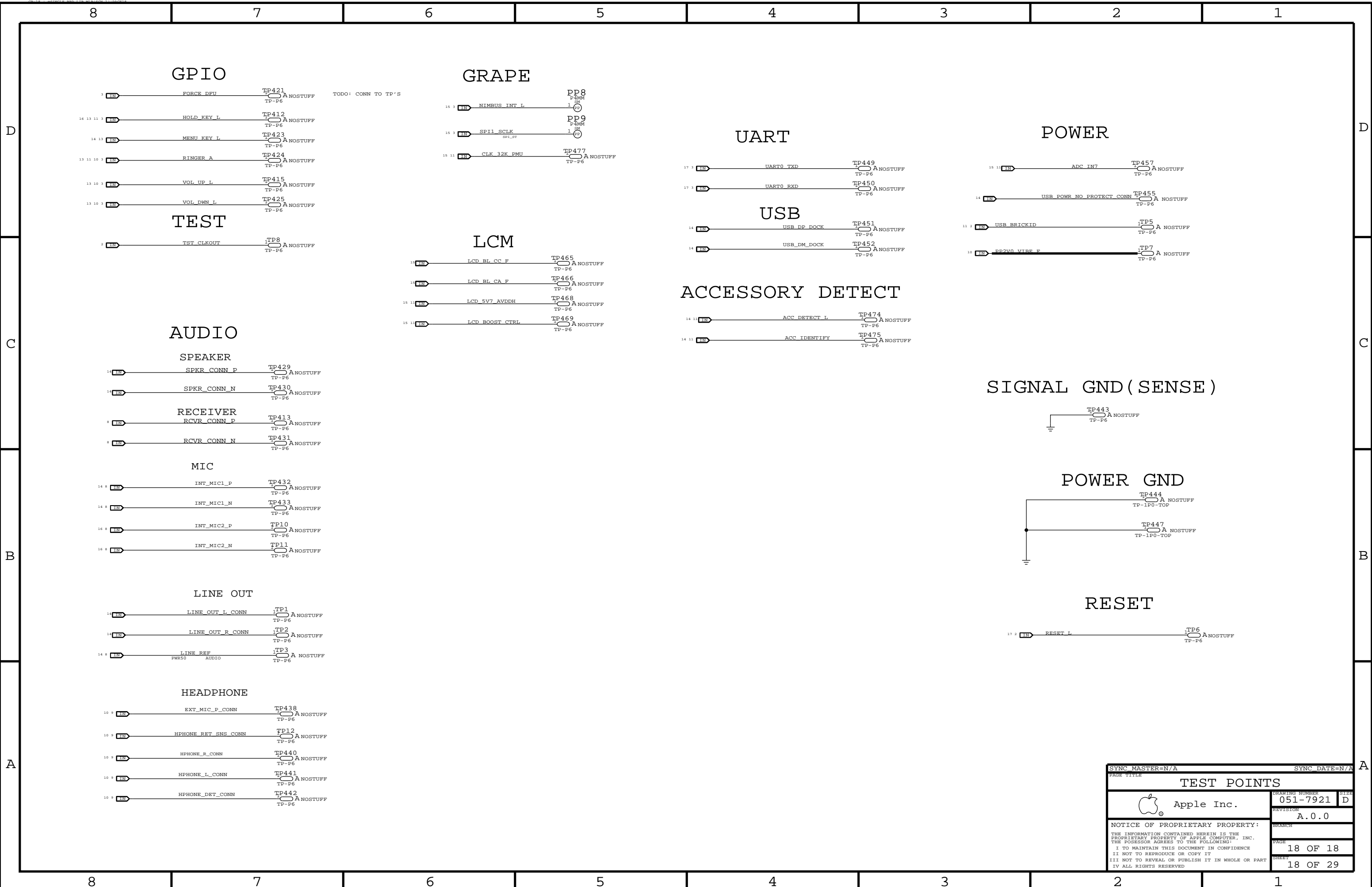
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RF I373		
27 16 14 11 8	BATT_VCC	PWR2500 BATT_VCC
24 11	BATTSNS	BATSNS
24 11	NTC	NTC
24 11 3	BATTERY SWI	GAS_GAUGE
27 18 2	RESET L	RF_RESET_N
27 3	RADIO_ON	RADIO_ON
27 11	RESET_PMU_L	RESET_PMU_N
22 16	LED_DRIVE_GSM	GSM_TXBURST_IND
27 3	BB_RESET_L	BB_RST_REQ_N
22 3	BB_RESET_DET_L	RESET_DET_N
22 3	SPI2_MRDY	IPC_MRDY
22 3	IPC_SRDY	IPC_SRDY
22 3	SPI2_SCLK	IPC_SCLK
22 3	SPI2_MOSI	IPC_MOSI
22 3	SPI2_MISO	IPC_MISO
22 3	IPC_GPIO	IPC_GPIO
22 11 9	AP_PMU_EXTON	AP_PMU_EXTON
27 11	WLCM1	BB_USB_VBUS
27 17	BB_USB_P	BB_USB_DATA_P
27 17	BB_USB_N	BB_USB_DATA_N
27 3	UART1_RXD	BB_USART0_TXD
27 3	UART1_TXD	BB_USART0_RXD
27 3	UART1_CTS_L	BB_USART0_RTS_N
27 3	UART1_RTS_L	BB_USART0_CTS_N
27 3	UART2_RXD	UMTS_TXD
27 3	UART2_TXD	UMTS_RXD
27 13 3	I2S1_BCLK	BB_I2S1_CLK
27 13 3	I2S1_LRCLK	BB_I2S1_MA0
27 13 3	I2S1_DOUT	BB_I2S1_TX
27 13 3	I2S1_DIN	BB_I2S1_RX
22 13 3	I2S2_BCLK	BB_I2S2_CLK
22 13 3	I2S2_LRCLK	BB_I2S2_MA0
22 13 3	I2S2_DIN	BB_I2S2_TX
22 13 3	I2S2_DOUT	BB_I2S2_RX
27 13	SIM_VCC	VSIM_VAR
27 13	SIM_DETECT	SIM_DETECT
27 13	SIMCRD_RST	SIMCRD_RST
27 13	SIMCRD_CLK	SIMCRD_CLK
27 13	SIMCRD_IO	SIMCRD_IO
28 17 13 11 9 5	PP1V8_SDRAM	WL_BT_VDDIO
27 11	WL_BT_REG_ON	WL_BT_REG_ON
28 11	CLK_32K_WIFI	CLK32K_AP
27 3	WLAN_RESET_L	WLAN_RESET_N
27 3	WLAN_SDIO_CLK	WLAN_SDIO_CLK
27 3	WLAN_SDIO_CMD	WLAN_SDIO_CMD
27 3	WLAN_SDIO_DATA<3..0>	WLAN_SDIO_DATA<3..0>
29 11	WLAN_HOST_WAKE	HOST_WAKE_WLAN
29 11	BT_HOST_WAKE	HOST_WAKE_BT
27 3	BT_RESET_L	BT_RESET_N
29 3	BT_WAKE	BT_WAKE
27 3	UART3_RXD	BT_UART_TXD
27 3	UART3_TXD	BT_UART_RXD
27 3	UART3_CTS_L	BT_UART_RTS_N
27 3	UART3_RTS_L	BT_UART_CTS_N
29 13 8	HP_PCM1_I2S_BCLK	BT_PCM_CLK
29 13 8	HP_PCM1_I2S_LRCLK	BT_PCM_SYNC
29 13 8	HP_PCM1_I2S_DIN	BT_PCM_OUT
29 13 8	HP_PCM1_I2S_DOUT	BT_PCM_IN
27 3	GPS_RESET_AP_L	GPS_RESET_N_AP
27 3	GPS_STANDBY_AP_L	GPS_STANDBY_N_AP
27 3	UART4_RXD	GPS_UART_TXD
27 3	UART4_TXD	GPS_UART_RXD
27 3	UART4_CTS_L	GPS_UART_RTS_N
27 3	UART4_RTS_L	GPS_UART_CTS_N
28 3	GPS_INTR_L	GPS_INTR_N
28 3	GPS_SYNC	GPS_SYNC

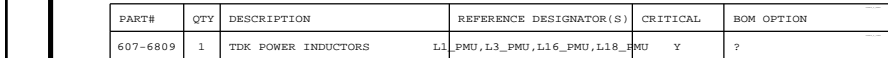


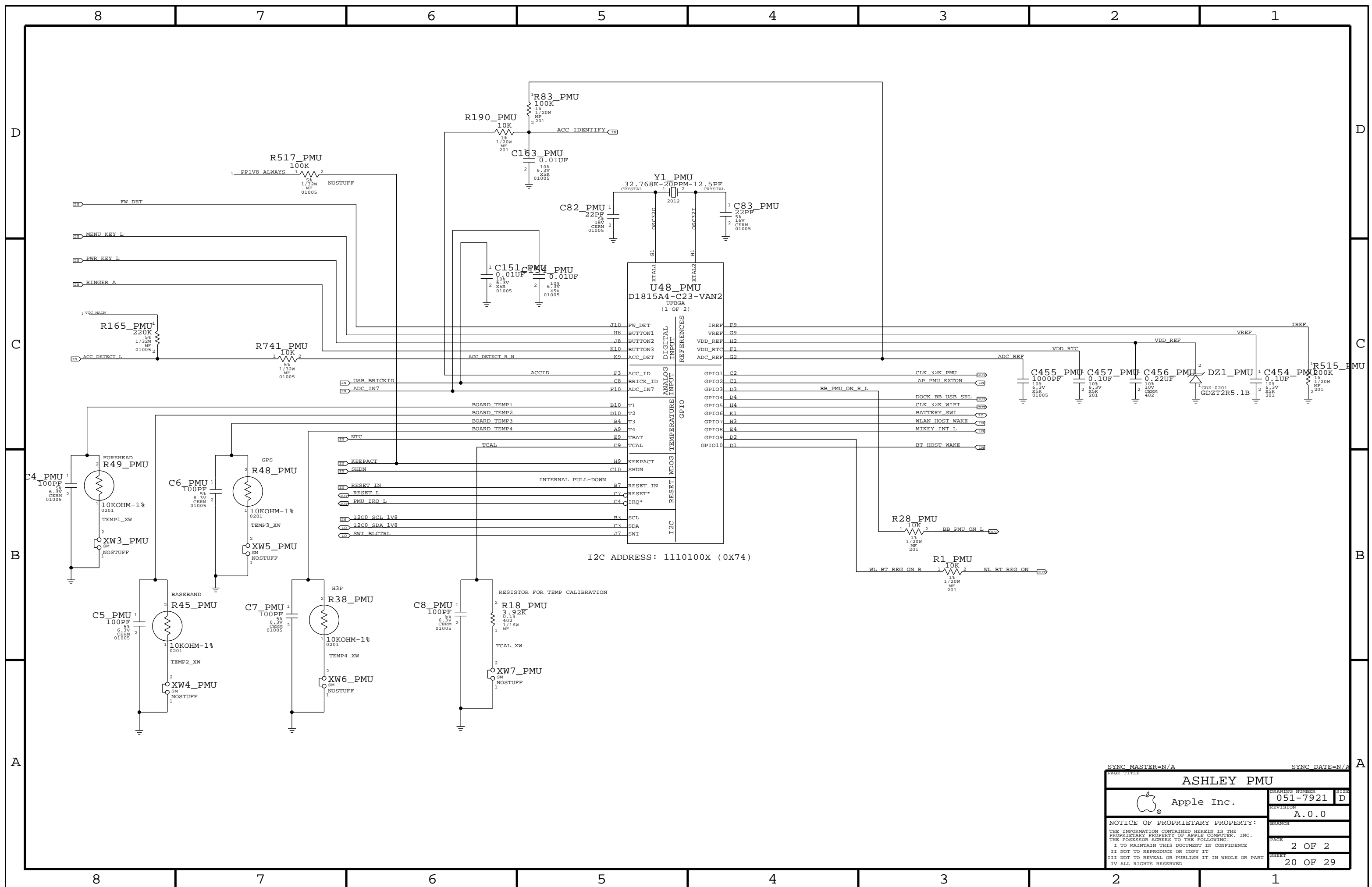
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RADIO,USB_MUX,EM PARTS			
DRAWING NUMBER		051-7921	
REVISION		A.0.0	
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```
N90 ASHLEY_PMU SUB-DESIGN
Thu Mar 18 14:15:23 2010
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SYNC MASTER-N/A		SYNC DATE-N/A	
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8		7		6		5		4		3		2		1	
1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%. 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS. 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.												REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE
												A	0000880153	PRODUCTION RELEASED	2010-03-23
N90 ( ICE3 ) HSPA RADIO PVT - 04/15/10:BRD REV9															
PAGE		CONTENTS													
02		BASEBAND I/O													
03		BASEBAND + MEMORY													
04		BASEBAND POWER SUPPLY													
05		GSM & UMTS TRANSCEIVER													
06		POWER AMPS AND RF FRONT END													
07		SYSTEM CONNECTORS													
08		A-GPS													
09		WLAN/BLEETOOTH RADIO													
BOARD - 920-XXXX SCHEMATIC - 951-XXXX BOM - 630-XXXX															
												DRAWING TITLE TITLE PAGE			
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												NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			
												PAGE 1 OF 9		SHEET 21 OF 29	
8		7		6		5		4		3		2		1	

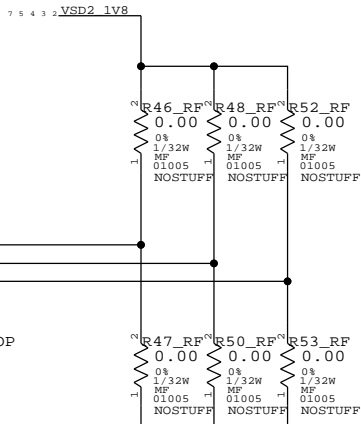
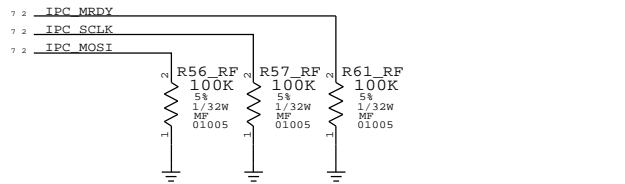
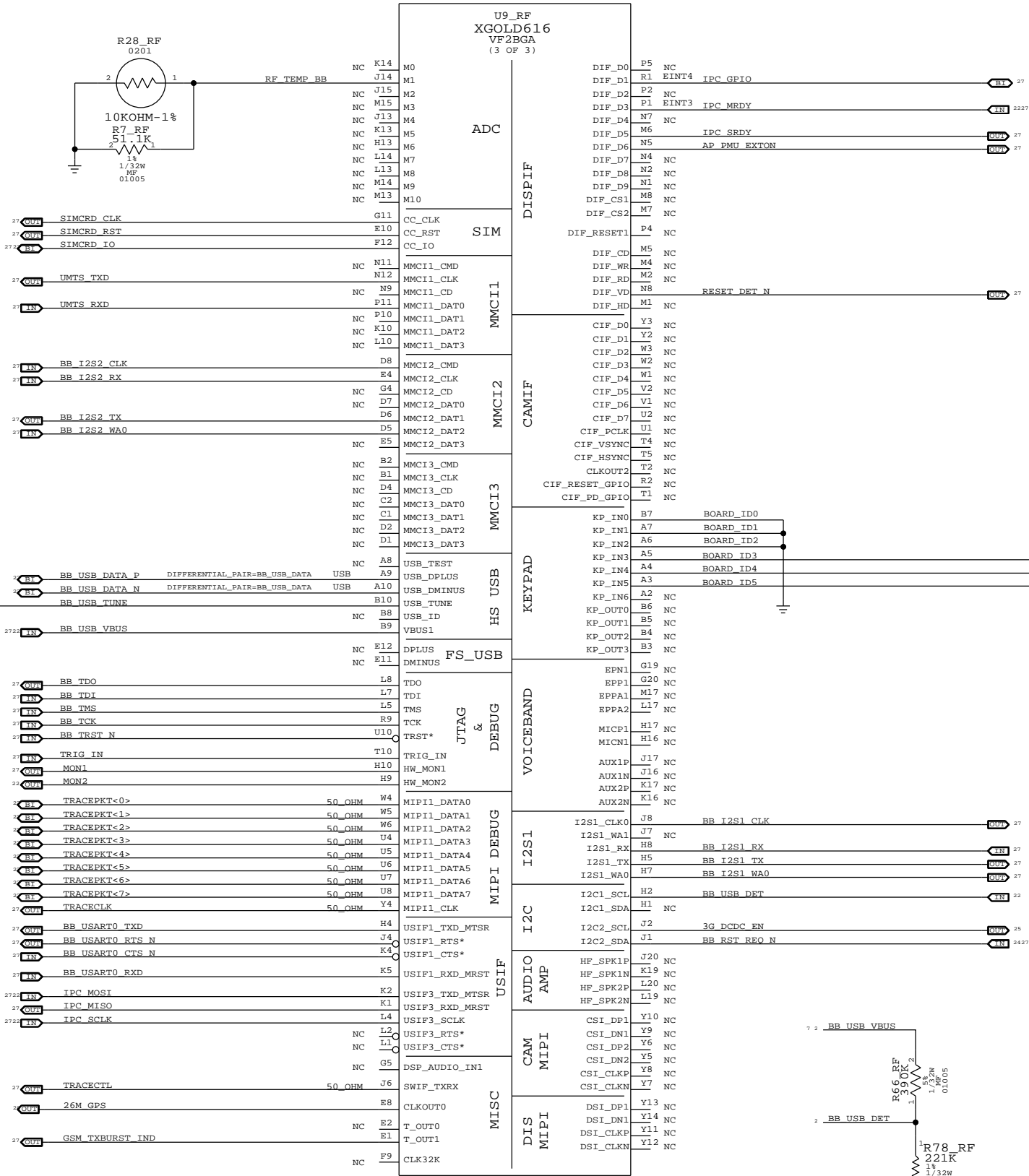
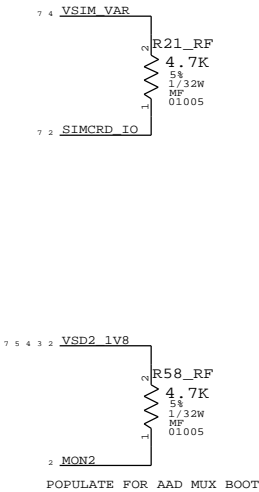
# BASEBAND I/O

D

C

B

A



WIFI MODULE VENDOR ID  
POPULATE R46/R47 AS BOM OPTION AT THE TOP

BOARD_ID3	VENDOR
NST	N/A
0	MURATA
1	USI

BOARD_ID5	FAMILY
NST	ICE3
0	ICE4
1	RESERVED

BOARD_ID4	PROJECT
NST	N90
0	RESERVED
1	RESERVED

BOARD_ID2	BOARD_ID1	BOARD_ID0	
NST	NST	NST	DEV1
NST	NST	1	DEV2
NST	1	NST	DEV3
NST	1	1	DEV4
NST	0	NST	DEV5
NST	0	0	DEV6
1	NST	NST	PROTO0
1	NST	1	PROTO1
1	1	NST	PROTO2
1	1	1	PROTO3
1	NST	0	EVT1
1	0	NST	EVT2
1	0	0	EVT3
0	NST	NST	DVT1
0	1	NST	DVT2
0	1	1	DVT3
0	0	0	PVT

## BOOT CONFIG DESCRIPTION

DSP\_AUDIO\_IN1, MON1 AND MON2

ARE ONLY ON TP AVAILABLE

DSP_AUDIO_IN1 HWCFG[3]	MON1 HWCFG[2]	MON2 HWCFG[1]	TRIG_IN HWCFG[0]	BOOT CONFIG DESCRIPTION
0	0	0	0	EXTERNAL BOOT VIA USB
0	0	0	1	NOR FLASH - EBU CONFIG: 16-BIT AD MUXED
0	0	1	0	NOR FLASH - EBU CONFIG: 16-BIT AAD MUXED

BASEBAND I/O

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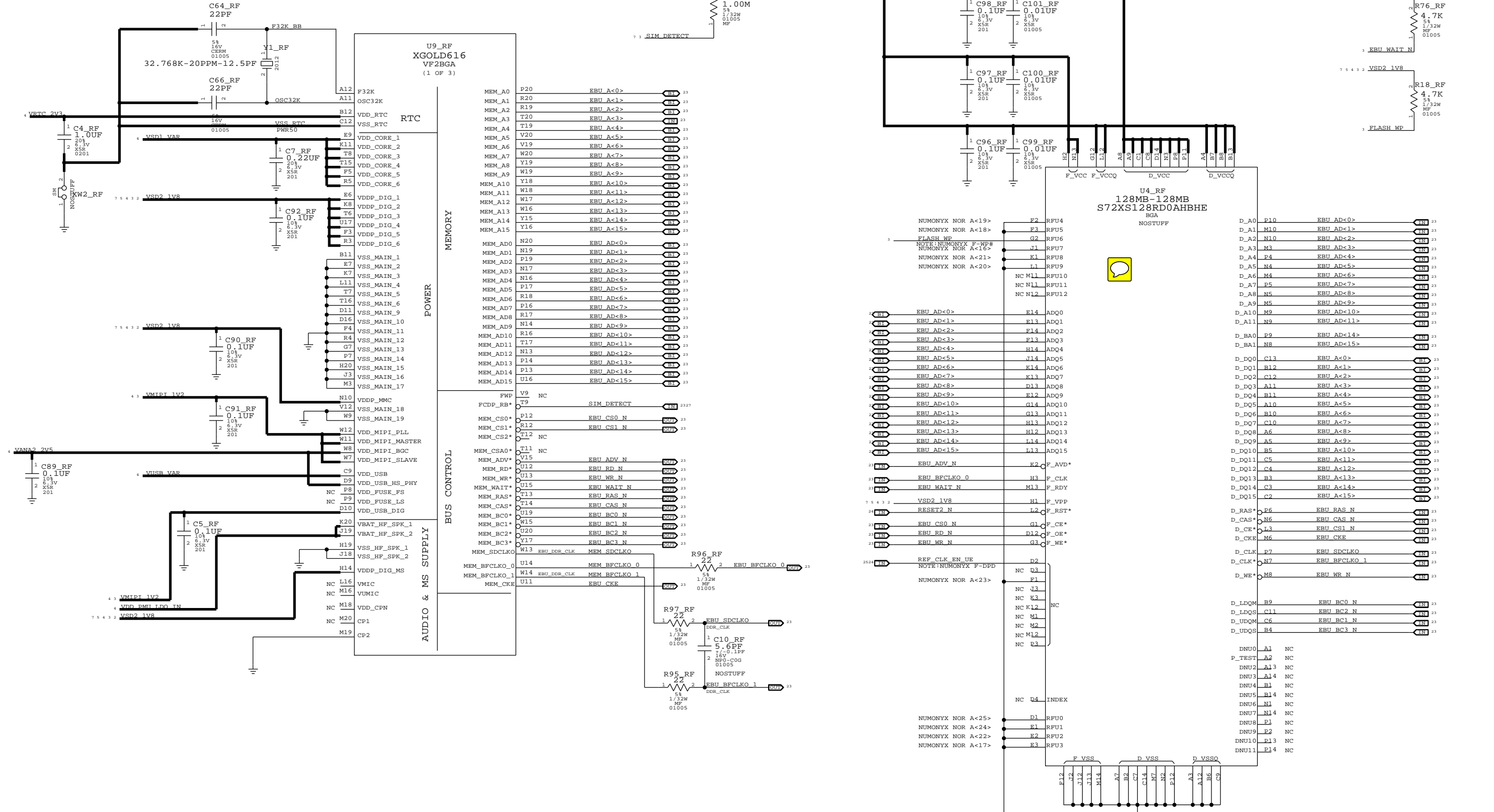
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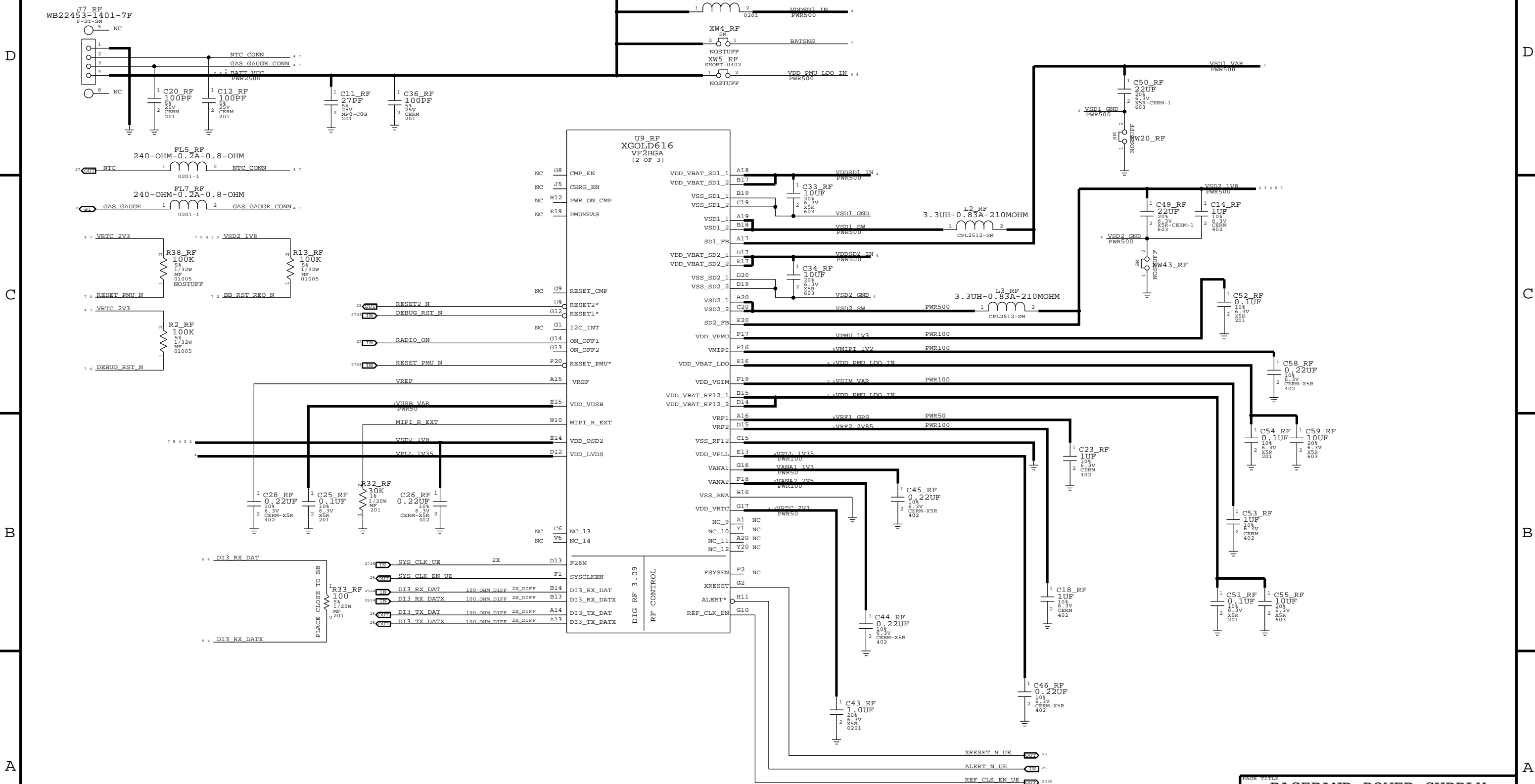
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BASEBAND MEMORY



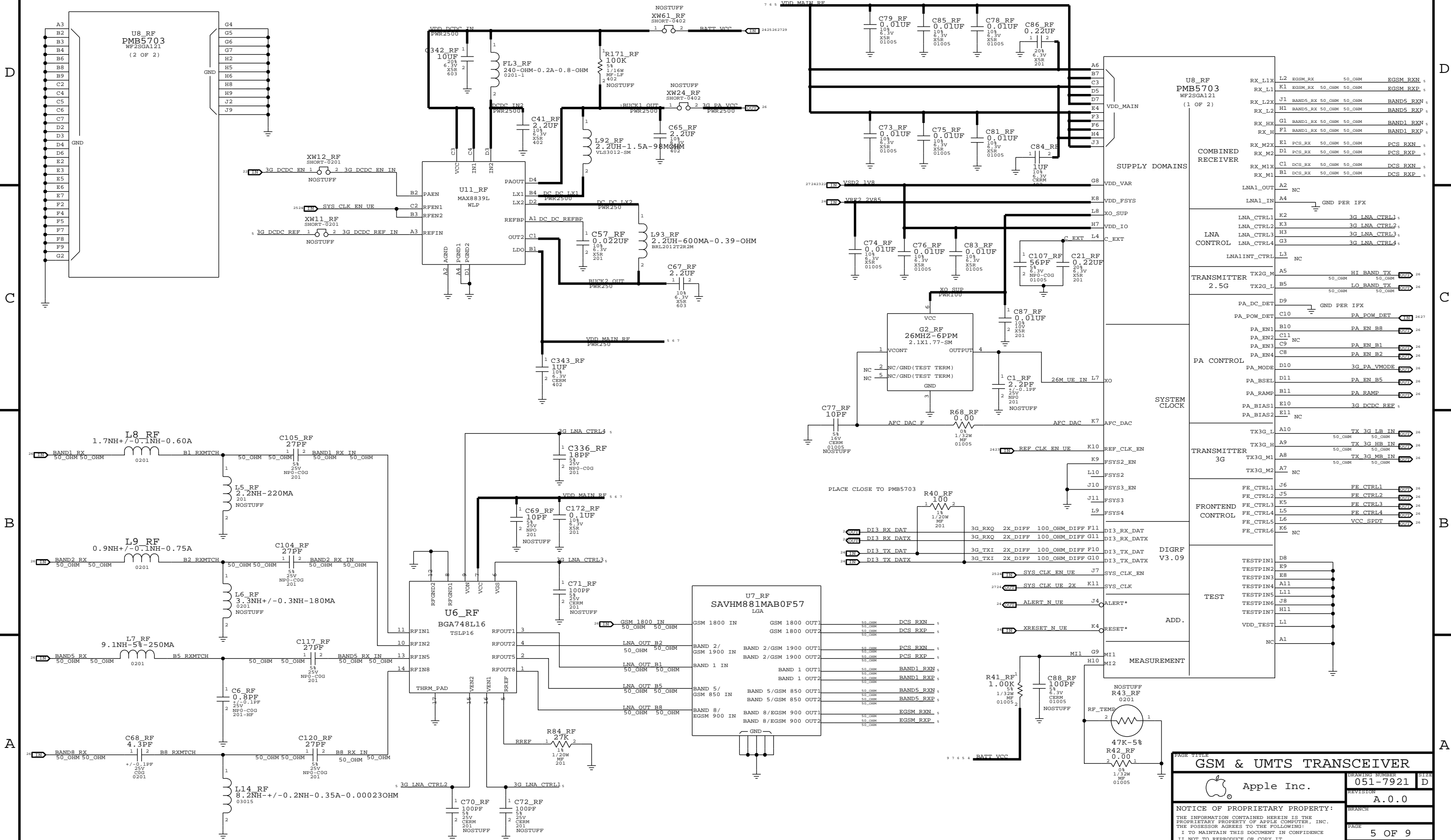
# BASEBAND POWER SUPPLY

BATTERY CONNECTOR



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BASEBAND POWER SUPPLY		
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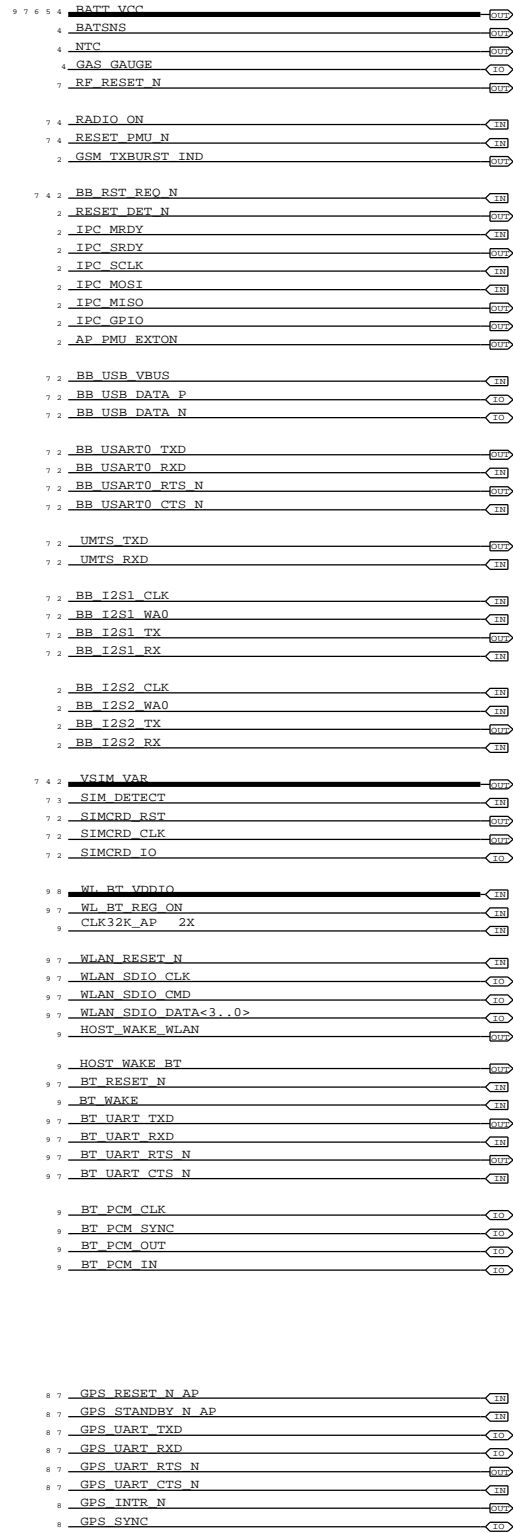
# GSM & UMTS TRANSCEIVER - SMARTI UE



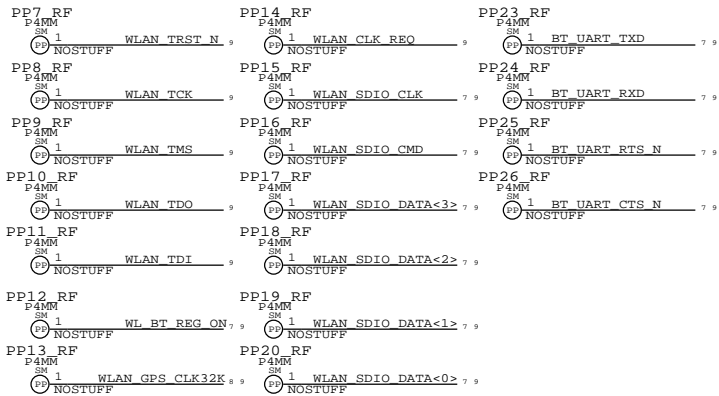
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# SYSTEM CONNECTORS

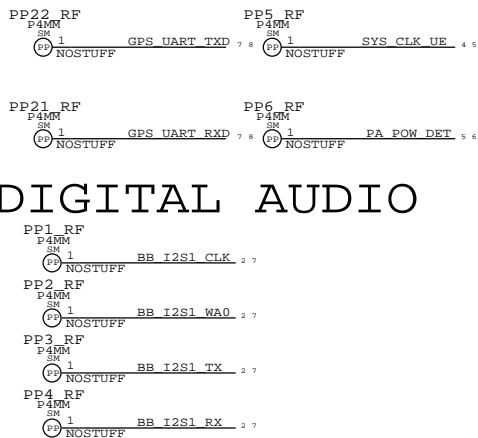
## AP CONNECTIONS



## WLAN/BT

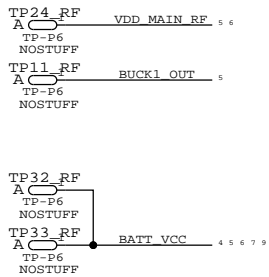


## GPS TRX

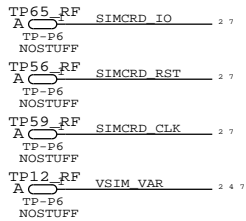


## DIGITAL AUDIO

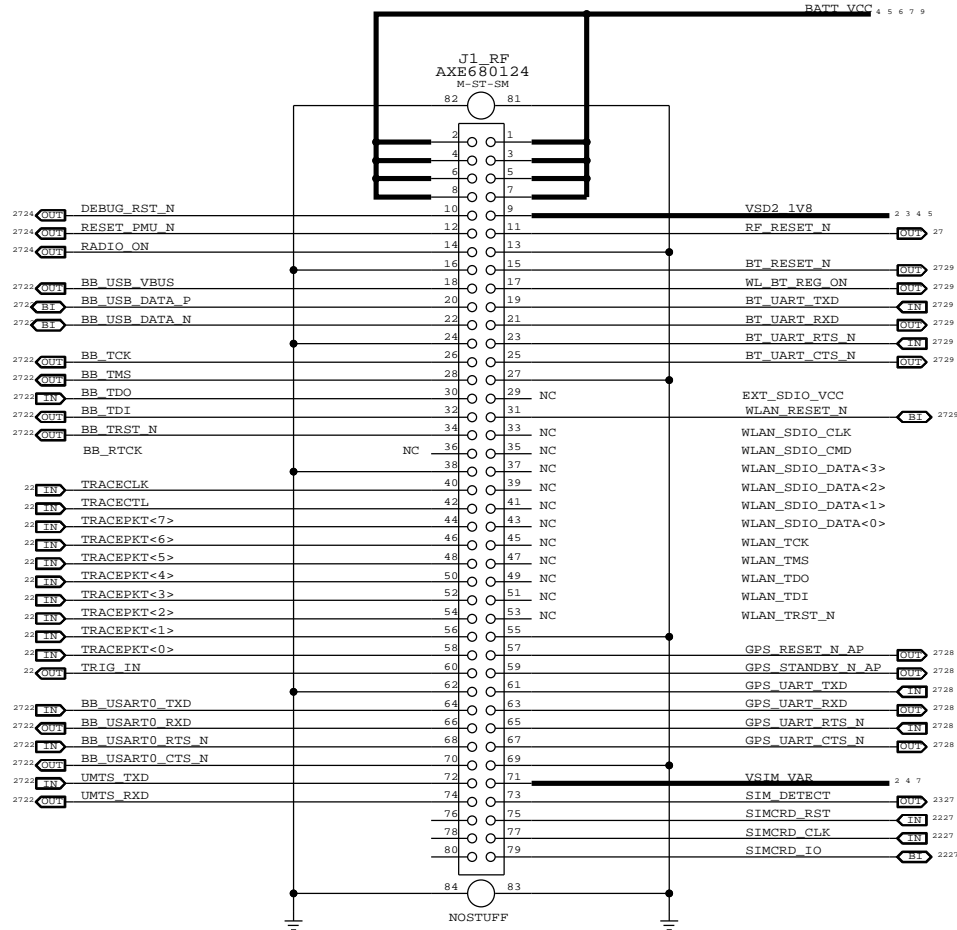
## POWER DOMAINS



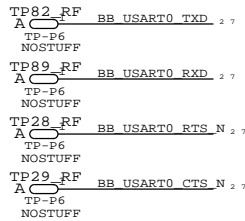
## SIM



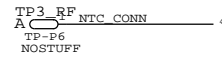
## DEBUG CONNECTOR



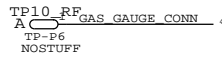
## USART0



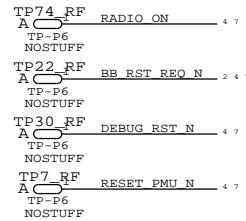
## NTC



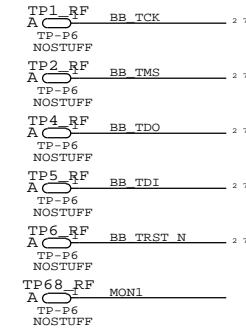
## GAS GAUGE




## CONTROL



## JTAG

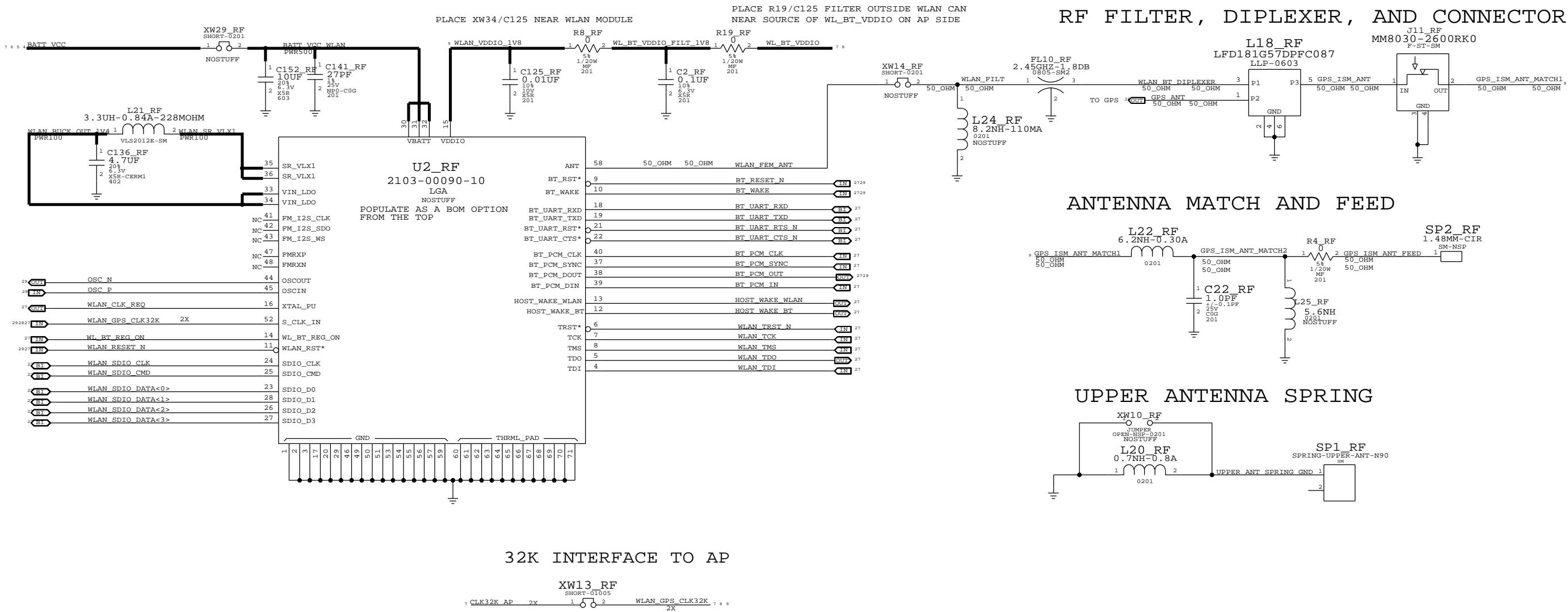


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1

# WLAN/BLUETOOTH/FM RADIO



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